

## N - CHANNEL ENHANCEMENT MODE FAST POWER MOS TRANSISTOR

PRELIMINARY DATA

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub>
STY30NA50	500 V	< 0.175 Ω	30 A

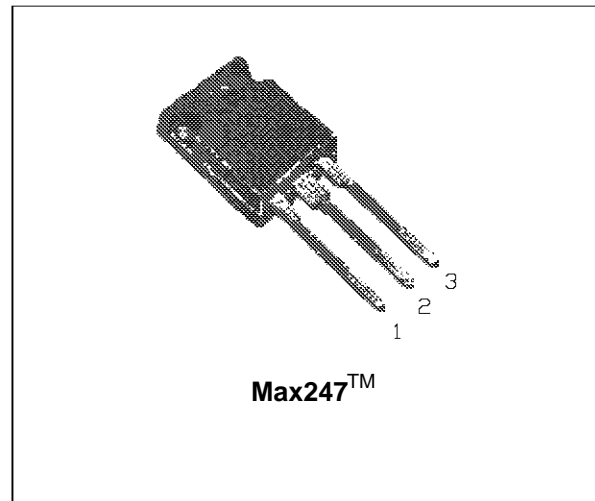
- TYPICAL R<sub>DS(on)</sub> = 0.15 Ω
- EFFICIENT AND RELIABLE MOUNTING THROUGH CLIP
- ± 30V GATE TO SOURCE VOLTAGE RATING
- REPETITIVE AVALANCHE TESTED
- LOW INTRINSIC CAPACITANCE
- 100% AVALANCHE TESTED
- GATE CHARGE MINIMIZED
- REDUCED THRESHOLD VOLTAGE SPREAD

### DESCRIPTION

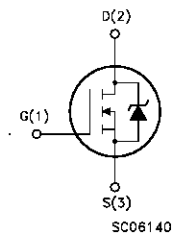
The Max247™ package is a new high volume power package exhibiting the same footprint as the industry standard TO-247, but designed to accommodate much larger silicon chips, normally supplied in bigger packages such as TO-264. The increased die capacity makes the device ideal to reduce component count in multiple paralleled designs and save board space with respect to larger packages.

### APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES (UPS)



### INTERNAL SCHEMATIC DIAGRAM



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	500	V
V <sub>DGR</sub>	Drain- gate Voltage (R <sub>GS</sub> = 20 kΩ)	500	V
V <sub>GS</sub>	Gate-source Voltage	± 30	V
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 25 °C	30	A
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 100 °C	19	A
I <sub>DM</sub> (•)	Drain Current (pulsed)	120	A
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	300	W
	Derating Factor	2.4	W/°C
T <sub>stg</sub>	Storage Temperature	-55 to 150	°C
T <sub>j</sub>	Max. Operating Junction Temperature	150	°C

(•) Pulse width limited by safe operating area

## STY30NA50

### THERMAL DATA

R <sub>thj-case</sub>	Thermal Resistance Junction-case	Max	0.42	°C/W
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	Max	40	°C/W
R <sub>thc-sink</sub>	Thermal Resistance Case-Heatsink with Conductive Grease	Typ	0.05	

### AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max, δ < 1%)	30	A
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 50 V)	3000	mJ
E <sub>AR</sub>	Repetitive Avalanche Energy (pulse width limited by T <sub>j</sub> max, δ < 1%)	180	mJ
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (T <sub>c</sub> = 100 °C, pulse width limited by T <sub>j</sub> max, δ < 1%)	19	A

### ELECTRICAL CHARACTERISTICS (T<sub>case</sub> = 25 °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source Breakdown Voltage	I <sub>D</sub> = 250 μA V <sub>GS</sub> = 0	500			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating V <sub>DS</sub> = Max Rating x 0.8 T <sub>c</sub> = 125 °C			200 1000	μA μA
I <sub>GSS</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ± 30 V			± 100	nA

ON (\*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> I <sub>D</sub> = 250 μA	2.25	3	3.75	V
R <sub>DS(on)</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 10 V I <sub>D</sub> = 15 A V <sub>GS</sub> = 10 V I <sub>D</sub> = 15 A T <sub>c</sub> = 100 °C		0.15	0.175 0.35	Ω Ω
I <sub>D(on)</sub>	On State Drain Current	V <sub>DS</sub> > I <sub>D(on)</sub> x R <sub>DS(on)max</sub> V <sub>GS</sub> = 10 V	30			A

### DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>fs</sub> (*)	Forward Transconductance	V <sub>DS</sub> > I <sub>D(on)</sub> x R <sub>DS(on)max</sub> I <sub>D</sub> = 15 A	25			S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V f = 1 MHz V <sub>GS</sub> = 0		6150	8000	pF
C <sub>oss</sub>	Output Capacitance			780	1000	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			220	290	pF

**ELECTRICAL CHARACTERISTICS** (continued)

**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ $t_r$	Turn-on Time Rise Time	$V_{DD} = 250\text{ V}$ $R_G = 4.7\ \Omega$ $I_D = 15\text{ A}$ $V_{GS} = 10\text{ V}$		40 70	55 90	ns ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 400\text{ V}$ $R_G = 47\ \Omega$ $I_D = 30\text{ A}$ $V_{GS} = 10\text{ V}$		240		A/ $\mu$ s
$Q_g$ $Q_{gs}$ $Q_{gd}$	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 400\text{ V}$ $I_D = 30\text{ A}$ $V_{GS} = 10\text{ V}$		245 27 120	320	nC nC nC

**SWITCHING OFF**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$ $t_f$ $t_c$	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 400\text{ V}$ $R_G = 4.7\ \Omega$ $I_D = 30\text{ A}$ $V_{GS} = 10\text{ V}$		75 30 110	100 40 145	ns ns ns

**SOURCE DRAIN DIODE**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{SD}$ $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				30 120	A A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 30\text{ A}$ $V_{GS} = 0$			1.6	V
$t_{rr}$ $Q_{rr}$ $I_{RRM}$	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 30\text{ A}$ $V_{DD} = 100\text{ V}$ $di/dt = 100\text{ A}/\mu\text{s}$ $T_j = 150\text{ }^\circ\text{C}$		800 17.6 44		ns $\mu$ C A

(\*) Pulsed: Pulse duration = 300  $\mu$ s, duty cycle 1.5 %

( $\bullet$ ) Pulse width limited by safe operating area

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