Tantalum capacitors

Chip tantalum capacitors with built-in open-function TCFG series

Semiconductor manufacturing technology has been used to include a temperature fuse in TCFG series capacitors. These capacitors feature low impedance and are ideal for digital circuits and low–voltage circuits in portable electronic equipment.

Features

- 1) Open-function built into every package.
- 2) High capacitance in a small package.
- 3) Low impedance.
- 4) Use of semiconductor manufacturing technology provides high reliability.
- 5) Superb solderability.

External dimensions (mm)



Case code	L	W1	W2	Н	S	G	Р
P (2012)	2.0±0.2	1.25±0.2	0.9±0.2	Max.1.20	0.45±0.3	Max. 0.15	1.3±0.3
A (3216)	3.2±0.2	1.6±0.2	1.2±0.2	1.6±0.2	0.8±0.3	Max. 0.15	1.6±0.3
B (3528)	3.5±0.2	2.8±0.2	1.9±0.2	1.9±0.2	0.8±0.3	Max. 0.15	1.9±0.3



Product designation

- \cdot When ordering, please specify the part No.
- · Please check to be sure of what combination of features you wish to order.
- · Fill in the blanks from left to right.

Three-digit number indicates the number of picofarads. The first two digits are the significant digits; the last digit gives the number of zeros.



Capacitance range

TCFG series

		Rated voltage (V. DC)						
μF	4 0G	6.3 0J	10 1A	16 1C	20 1D			
0.68								
1.0			Р	ΡA	A			
1.5		Р	А	А				
2.2	Р	Р	A	А				
3.3	Р	ΡA	ΡA	A B				
4.7	ΡA	ΡA	ΡΑΒ	A B				
6.8	ΡA	ΡA	A B	A B				
10	ΡA	ΡΑΒ	A B	В				
15	ΡAΒ	ΑB	A B					
22	A B	ΑB	В					
33	A B	В	В					
47	В	В						
68	В							
100								

red characters : under the development

Characteristics

Iter	n	Performance			Test methods / conditions (based on JIS C 5102, 5143)				
Operating temp	Operating temperature $-55\degreeC \sim +125\degreeC$								
Max. operating at rated voltage	Aax. operating temperature +85℃ at rated voltage								
Rated voltage (V. DC)	4 6.3	10	16	20				
Derated voltage	e (V. DC)	2.5 4	6.3	10	13		at 12	5℃	
Surge voltage (V. DC)	5.2 8	13	20	26				
Leakage currer	t	Less tl 0.01C "Stanc	han V. D Iard	or e etail Proc	qual s are duct	to the larger of 0.5 μ A or e given in Table 1, List".	Meas of rat	sured value 60s after ed voltage.	application
Capacitance ra	nge	1.0~4	·7 μ	F			Meas Meas Meas circui	sured frequency: 120 sured voltage: 0.5Vrn sured circuit: equivale t	±12Hz ns+1.5V. DC ent series
tan δ	P case A case B case	0.08 m 1 μ F r 1.5 μ F 3.3 μ F	0.08 max. 1 μ F max.: 0.04 max. 1.5 μ F to 22 μ F max.: 0.06 max.			Meas Meas Meas circui	sured frequency: 120 sured voltage: 0.5Vrn sured circuit: equivale t	±12Hz ns+1.5V. DC ent series	
Impedance	P case	27.5Ω	27.50 max				Meas	sured frequency: 100	±10Hz
	A case	20.0 Ω	20.0Ω max.			Meas	ured voltage: 0.5Vrm	ns max.	
	B case	15.0 Ω	ma	х.			circui	sured circuit: equivale t	ent series
Resistance to solder heat	Appearance	No not markir	No noticeable irregularities, and the markings must be easy to read.				Direc Solde	t immersion into sold er bath temperature:	ler bath 260±5℃
	L.C	Must satisfy the initial specified value.			Immersion time: 5s				
	△C/C	Within P case	Within \pm 5% P case= \pm 10%						
	$\tan \delta$	P case A · B ca	: 1.5 ase r	time nust	s or satis	less or initial specified tolerance. sfy the initial specified value.			
Fuse operation		320°C	for 2	20s (or le	SS	Direc (320	t immersion into sold ±5℃)	ler bath
Temperature cycle	Appearance	No not markir	ticea ngs r	ıble i nust	irreg be	jularities, and the easy to read.	The f	our cycles in the tabl ated five times in suc	e below are cession.
	L.C	Must s	atis	fy th	e ini	tial specified value.		Temperature	Time
		Pcase	>=v	Vithir	115		1	−55±3°C	30±3mins.
		A · B	ase	with	nin z	% ±5%	3	125±2°C	30 ± 3 mins.
	tan δ	P case 1.5 times or less or initial specified tolerance. 4 Room temperature 3 A · B case must satisfy the initial specified value. 4 Room temperature 3				3mins. max.			
Resistance to humidity	Appearance	No noticeable irregularities, and the markings must be easy to read.			Meas ±12	sured after being left hrs. at 60±2℃ and 9	for 500 90 to 95%		
(steady state)	L.C	Must s	Must satisfy the initial specified value.			tial specified value.	RH, t	hen 1 to 2 hrs. at nor erature and humidity	mal room
		P case A • B c	e wit case	hin : with	±20 nin =	% ±10%	temp		
	tan δ	P case A · B ca	P case 1.5 times or less or initial specified tolerance A · B case must satisfy the initial specified value.			less or initial specified tolerance. sfy the initial specified value.			



Item		Performance	Test methods / conditions (based on JIS C 5102,5143)		
Temperature	Temperature	-55°C			
characteristics	△C/C	P case within +0% and -15% of the value before testing. A \cdot B case within +10% and -0% of the value before testing.			
	tan ∂	P case within 1.5 times of the value before testing. A·B case must satisfy the initial specified value.			
	L.C	-			
	Temperature	+85°C			
	△C/C	P case within +0% and -15% of the value before testing. A \cdot B case within +10% and -0% of the value before testing.			
	$tan \delta$	Must satisfy the initial specified value.			
	L.C	Less than or equal to the larger of 5 μ A or 0.1CV.			
	Temperature	+125°C			
	△C/C	P case within +20% and -0% of the value before testing. A \cdot B case within +15% and -0% of the value before testing.			
	tan δ	P case within 1.5 times of the value before testing. A B case must satisfy the initial specified value.			
	L.C	Less than or equal to the larger of 6.3 μ A or 0.125CV.			
Surge resistance	Appearance	A·B case no noticeable irregularities, and the markings must be easy to read.	Apply the rated surge voltage for 30 \pm 5s at intervals of 5 \pm .05mins. 1000		
	L.C	Must satisfy the initial specified value.	times, with the temperature at $85\pm2^{\circ}$		
	△C/C	P case within $\pm 10\%$ A·B case within $\pm 5\%$			
	tan ∂	P case within 1.5 times of the value before testing. A B case must satisfy the initial specified value.			
High- temperature	Appearance	No noticeable irregularities, and the markings must be easy to read.	Apply the rated voltage continuously for 2000 to 2072hrs. via a series resister of		
load	L. C	Must satisfy the initial specified value.	after leaving the device at room		
	_C/C	Within $\pm 10\%$	temperature and humidity for 1 to 2hrs.		
	tan δ	P case within 1.5 times of the value before testing. A · B case must satisfy the initial specified value.			
Terminal	Capacitance	Value must be stable during measurement.	Apply pressure to the device using the		
strength	Appearance	No noticeable irregularities.	specified tool for 5s so that the center deflection is 1mm (see below). $\begin{array}{c} (Units:mm) \\ F (Direction of force) \\ \hline \\ $		

Item		Performance	Test conditions
Adhesion		Terminals must not detach.	With the device mounted on the printed circuit board, apply a force of 0.5kg • f from each side for a period of 10±1s.
External dimens	sions	Refer to Fig. 1, "External dimensions"	Measure using slide calipers that meet the requirements of JIS B7507 Class 2.
Markings	Resistance to solvents	Marking must be easy to read.	Immerse in isopropyl alcohol for 30±5s.
Solderability Inspect the solder cover of the terminals using a solder immersion test		At least 3 / 4 of the surface of the immersed terminals must be covered with new solder.	Immersion speed: 25±2.5mm / s Pre-processing (accelerated aging): leave for 1hr over boiling distilled water Solder temperature: 235±5°C Immersion time: 2±0.5s Solder type: H63A Flux: rosin 25%, IPA 75%
Resistance to	Capacitance	Value must be stable during measurement.	Vibrate in the X / Y axis at frequencies of
vibration Appearance		No noticeable irregularities.	10Hz, 55Hz and 10Hz for two hours each, with a total vibration amplitude of 1.5mm.
Reverse Appearance polarity		No noticeable irregularities, and the markings must be easy to read.	Apply either 0.1 times the rated voltage, or 3V, whichever is smaller, via a series
withstanding voltage	L.C	Must be less than or equal to twice the initial specified value.	resister of 3Ω max. and 0.1Ω min. at a temperature of $85\pm2^{\circ}$ C.
	△C/C	Within $\pm 10\%$ of the value before the test.	
	tan δ	Must be less than or equal to 1.5 times the initial specified value.	

•Table 1 Standard parts list, TCFG series

Part No.	Rated voltage at 85℃	Derated voltage at 125℃	Surge voltage at 85℃	Capacitance	Tolerance	Leakage current at 25°C	DF 120Hz 25℃
	(V)	(V)	(V)	(µF)	(%)	1000.60S (μA)	(%)
TCF GP 0G 225_	4	2.5	5.2	2.2	±20,10	0.5	8
TCF GP 0G 335_	4	2.5	5.2	3.3	±20,10	0.5	8
TCF GP 0G 475_	4	2.5	5.2	4.7	±20,10	0.5	6
TCF GA 0G 685_	4	2.5	5.2	6.8	±20,10	0.5	6
TCF GA 0G 106_	4	2.5	5.2	10	±20,10	0.5	6
TCF GA 0G 156_	4	2.5	5.2	15	±20,10	0.6	6
TCF GB 0G 156_	4	2.5	5.2	15	±20,10	0.6	6
TCF GA 0G 226_	4	2.5	5.2	22	±20,10	0.9	6
TCF GB 0G 226_	4	2.5	5.2	22	±20,10	0.9	6
TCF GB 0G 336_	4	2.5	5.2	33	±20,10	1.3	6
TCF GB 0G 476_	4	2.5	5.2	47	±20,10	1.9	6
TCF GP 0J 155_	6.3	4	8	1.5	±20,10	0.5	6
TCF GP 0J 225_	6.3	4	8	2.2	±20,10	0.5	6
TCF GA 0J 335_	6.3	4	8	3.3	±20,10	0.5	6
TCF GA 0J 475_	6.3	4	8	4.7	±20,10	0.5	6
TCF GA 0J 685_	6.3	4	8	6.8	±20,10	0.5	6
TCF GA 0J 106_	6.3	4	8	10	±20,10	0.6	6
TCF GA 0J 156_	6.3	4	8	15	±20,10	0.9	6
TCF GB 0J 106_	6.3	4	8	10	±20,10	0.6	6
TCF GB 0J 156_	6.3	4	8	15	±20,10	0.8	6
TCF GB 0J 226_	6.3	4	8	22	±20,10	1.4	6
TCF GB 0J 336_	6.3	4	8	33	±20,10	2.1	6
TCF GP 1A 105_	10	6.3	13	1.0	±20,10	0.5	8
TCF GA 1A 225_	10	6.3	13	2.2	±20,10	0.5	6
TCF GA 1A 335_	10	6.3	13	3.3	±20,10	0.5	6
TCF GA 1A 475_	10	6.3	13	4.7	±20,10	0.5	6
TCF GB 1A 475_	10	6.3	13	4.7	±20,10	0.5	6
TCF GA 1A 685_	10	6.3	13	6.8	±20,10	0.7	6
TCF GB 1A 685_	10	6.3	13	6.8	±20,10	0.7	6
TCF GA 1A 106_	10	6.3	13	10	±20,10	1.0	6
TCF GB 1A 106_	10	6.3	13	10	±20,10	1.0	6
TCF GB 1A 156_	10	6.3	13	15	±20,10	1.5	6
TCF GA 1C 105_	16	10	20	1.0	±20,10	0.5	4
TCF GA 1C 155_	16	10	20	1.5	±20,10	0.5	6
TCF GA 1C 225_	16	10	20	2.2	±20,10	0.5	6



Part No.	Rated voltage at 85°C	Derated voltage at 125℃	Surge voltage at 85°C	Capacitance	Tolerance	Leakage current at 25°C 1WV.60s	DF 120Hz 25℃
	(V)	(V)	(V)	(μF)	(%)	(μA)	(%)
TCF GA 1C 335_	16	10	20	3.3	±20,10	0.5	6
TCF GB 1C 335_	16	10	20	3.3	±20,10	0.5	6
TCF GB 1C 475_	16	10	20	4.7	±20,10	0.8	6
TCF GB 1C 685_	16	10	20	6.8	±20,10	1.1	6
TCF GB 1C 106_	16	10	20	10	±20,10	1.6	6
TCF GA 1D 105_	20	16	26	1.0	±20,10	0.5	4

Packaging specifications



Packaging style

Part no.	Package type	Packag	ing style	Symbol	Basic ordering unit (pcs)
TCFG	Taping	Plastic taping	<i>∳</i> 180 mm reel	R	2,000
TCFP	Taping	Plastic taping	<i>ø</i> 180 mm reel	R	2,000



Electrical characteristics and operation notes

(1) Soldering conditions (soldering temperature and soldering time)



Fig.1 Reflow (Infrared Ray, Hot Plate, Hot Air)



(Dipping wave soldering)











(3) Derating voltage as function of temperature



ROHM

TEMPERATURE (℃) Fig.5

(4) Reliability

The malfunction rate of tantalum solid state electrolytic capacitors varies considerably depending on the conditions of usage (ambient temperature, applied voltage, circuit resistance).

Formula for calculating malfunction rate

 $\lambda p = \lambda b \times (\pi_E \times \pi_{SR} \times \pi_Q \times \pi_{CV})$

- : Malfunction rate stemming from operation λp
- λb : Basic malfunction rate
- : Environmental factors πF
- : Series resistance π_{SR}
- : Level of malfunction rate πα
- : Capacitance π_{CV}

For details on how to calculate the malfunction rate stemming from operation, see the tantalum solid state electrolytic capacitors column in MIL-HDBK-217.

Malfunction rate as function of operating temperature and rated voltage



Malfunction rate as function of circuit resistance (Ω/V)





(5) External temperature vs. fuse blowout



Note: Solder the chip at 300° C or less. If it is soldered using a temperature higher than 300° C, the built–in temperature fuse may blow out.

(7) Maximum power dissipation

Warming of the capacitor due to ripple voltage balances with warming caused by Joule heating and by radiated heat. Maximum allowable warming of the capacitor is to 5° C above ambient temperature. When warming exceeds 5° C, it can damage the dielectric and cause a short circuit.

Power dissipation (P) =I² • R

Ripple current

- P: As shown in table at right
- R: Equivalent series resistance

(6) Power vs. fuse blowout characteristics / Product sur-

face temperature



Fig.9

Notes:

1. Please be aware that when case size is changed, maximum allowable power dissipation is reduced.

2. Maximum power dissipation varies depending on the package. Be sure to use a case which will keep warming within the limits shown in the table below.

Allowable power dissipation (W) and maximum temperature rising

Ambient temp. Case	+25℃	+55℃	+85℃	+125℃
P case (2012)	0.025	0.022	0.020	0.010
A case (3216)	0.070	0.063	0.056	0.028
B case (3528)	0.080	0.072	0.064	0.032
Max. temp rise	5	5	5	2

(8) Impedance frequency characteristics



Fig.10





(10) Temperature characteristics





Fig.14 Inrush current



Fig.13



Fig.15

Beware of inrush current.

Inrush currents are inversely proportional to ESR. Large inrush currents can cause component failure.



Inrush current can be limited by means of a protective resistor.



Fig.17 Imax change due to protective resistor R

(11) Ultrasonic cleaning

Carry out cleaning under the mildest conditions possible. The internal element of a tantalum capacitor are larger than those of a transistor or diode, so it is not as resistant to ultrasonic waves.



Example: water

Propagation speed 1500 m/s Solvent density 1g/cm³ Frequency and wavelength Frequency Wavelength 20kHz 7.5cm

20kHz	7.5cm
28kHz	5.3cm
50kHz	3.0cm

Precautions

- 1) Do not allow solvent to come to a boil (kinetic energy increases).
 - Ultrasonic output 0.5W/cm² or less
 - · Use a solvent with a high boiling point.
 - · Lower solvent temperature.
- 2) Ultrasonic cleaning frequency

28 kHz or less

- 3) Keep cleaning time as short as possible.
- 4) Move item being cleaned.

Standing waves caused by the ultrasonic waves can cause stress to build up in part of the item being cleaned.

Reference

Kinetic energy = $2 \times \pi \times frequency \times \pi$

2 x ultrasonic output propagation speed x solvent density

