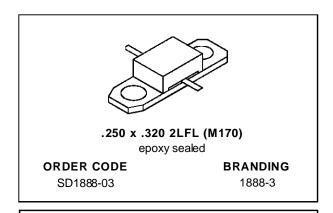


# SD1888-03

# RF & MICROWAVE TRANSISTORS 1.6 GHz SATCOM APPLICATIONS

- 1.65 GHz
- 28 VOLTS
- EFFICIENCY 50% MIN.
- CLASS C OPERATION
- COMMON BASE
- INPUT/OUTPUT MATCHING
- Pout = 24 W MIN. WITH 9.0 dB GAIN

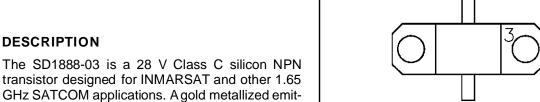


PIN CONNECTION

3. Base

1. Collector

2. Emitter



transistor designed for INMARSAT and other 1.65 GHz SATCOM applications. Agold metallized emitter-ballasted die geometry is employed providing high gain and efficiency while ensuring long term reliability and ruggedness under severe operating conditions. SD1888-03 is packaged in a cost-effective epoxy sealed housing

## **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit	
Vcво	Collector-Base Voltage	45	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	12	V	
V <sub>EBO</sub>	Emitter-Base Voltage	3.0	V	
Ic	Device Current	2.6	А	
P <sub>DISS</sub>	Power Dissipation	50	W	
TJ	Junction Temperature	+200	°C	
T <sub>STG</sub>	Storage Temperature	– 65 to +150	°C	

#### THERMAL DATA

R <sub>TH(j-c)</sub> Junction-Case Thermal Resistance	3.5	°C/W
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1/4 July 1993

## **ELECTRICAL SPECIFICATIONS** (Tcase = 25°C)

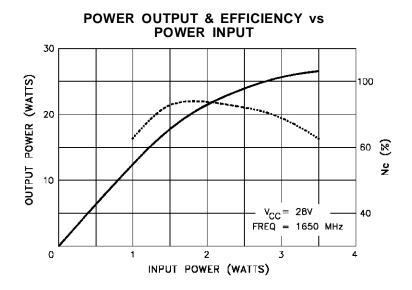
### **STATIC**

Symbol	Test Conditions	Value			Unit	
		Min.	Тур.	Max.		
ВУсво	I <sub>C</sub> = 6 mA	$I_E = 0 \text{ mA}$	45	_	_	V
BVCEO	I <sub>C</sub> = 6 mA	$I_B = 0 \text{ mA}$	12	—	_	V
BV <sub>EBO</sub>	I <sub>E</sub> = 6 mA	$I_C = 0 \text{ mA}$	3.0	_	_	V
h <sub>FE</sub>	V <sub>CE</sub> = 5 V	I <sub>C</sub> = 1.2 A	15	-	150	_

### **DYNAMIC**

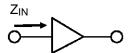
Symbol	Symbol Test Conditions		Value			Unit	
Symbol		rest conditions		Min.	Тур.	Max.	Oiiit
Pout	f = 1.65 GHz	$P_{IN} = 3.0 W$	$V_{CE} = 28 \text{ V}$	24	_	_	W
G <sub>P</sub>	f = 1.65 GHz	$P_{IN} = 3.0 W$	$V_{CE} = 28 \text{ V}$	9.0	_	_	dB
ης	f = 1.65 GHz	P <sub>IN</sub> = 3.0 W	V <sub>CE</sub> = 28 V	50	_	_	%

### TYPICAL PERFORMANCE

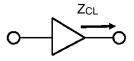


### **IMPEDANCE DATA**



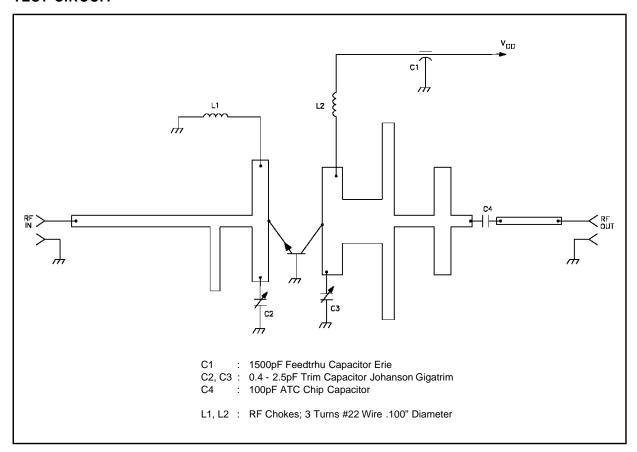


TYPICAL COLLECTOR LOAD IMPEDANCE

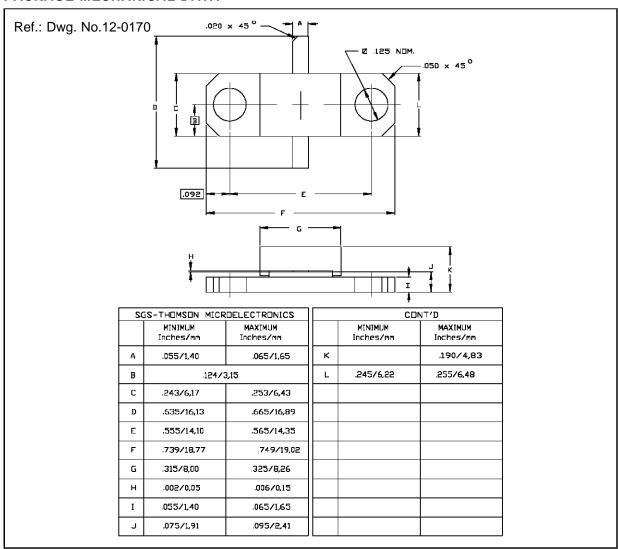


FREQ.	Z <sub>IN</sub> (Ω)	Z <sub>CL</sub> (Ω)		
1600 MHz	9.0 + j 14.0	11.0 + j 2.0		
1650 MHz	11.5 + j 12.0	9.0 + j 4.0		
1700 MHz	23.0 + j 8.0	8.0 + j 5.5		

### **TEST CIRCUIT**



#### PACKAGE MECHANICAL DATA



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