# Emitter common (dual digital transistors)

# **UMG9N / FMG9A**

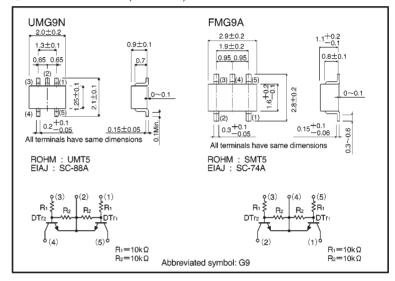
#### Features

- Two DTC114E in a UMT or SMT package.
- Mounting cost and area can be cut in half.
- Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr<sub>1</sub> and DTr<sub>2</sub>.

## ●External dimensions (Units: mm)



### ● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Voc	50	٧	
Input voltage		Vn	40	٧	
		VM.	-10		
Output current		la 50		mA.	
		lo (Max.)	100	mA	
Power dissipation	UMG9N	Pd	150(TOTAL)	*1	
	FMG9A	Pu	300(TOTAL)	mW +2	
Junction temperature		Τj	150	C.	
Storage temperature		Tstg	-55~+150	Ċ	

#1 120mW per element must not be exceeded.

#2 200mW per element must not be exceeded.

Transistors UMG9N/FMG9A

#### ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	_	_	0.5	٧	Vcc=5V, Io=100 μ A	
Input voltage	VI (on)	3	_	_		Vo=0.3V, Io=10mA	
Output voltage	V <sub>O</sub> (on)	_	0.1	0.3	٧	Io/Iı=10mA/0.5mA	
Input current	lı	_	_	0.88	mA	V <sub>1</sub> =5V	
Output current	lo (off)	_	_	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	30	_	_	_	Vo=5V, lo=5mA	
Transition frequency	f⊤	_	250	_	MHz	V <sub>CE</sub> =10mA, I <sub>E</sub> =-5mA, f=100MHz *	
Input resistance	R <sub>1</sub>	7	10	13	kΩ	_	
Resistance ratio	R2/R1	8.0	1.0	1.2	_	_	

<sup>\*</sup> Transition frequency of the device

#### Packaging specifications

	Packaging type	Taping	
	Code	TR	T148
Part No.	Basic ordering unit (pieces)	3000	3000
UMG9N		0	_
FMG9A		_	0

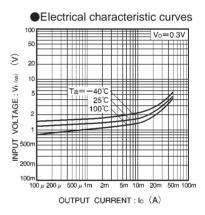


Fig.1 Input voltage vs. output current (ON characteristics)

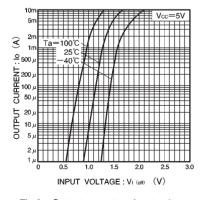


Fig.2 Output current vs. input voltage (OFF characteristics)

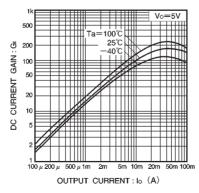


Fig.3 DC current gain vs. output current

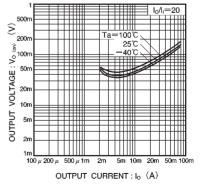


Fig.4 Output voltage vs. output current