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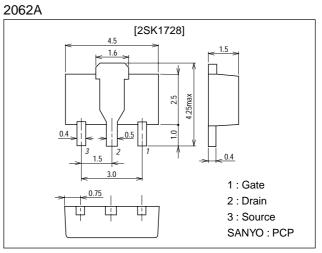
# **Ultrahigh-Speed Switching Applications**

### Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

## **Package Dimensions**

unit:mm



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		100	V
Gate-to-Source Voltage	VGSS		±15	V
Drain Current (DC)	۱ <sub>D</sub>		1	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10µs, duty cycle≤1%	4	A
Allowable Power Dissipation	D-	Tc=25°C	3.5	W
	PD	Mounted on a ceramic board (250mm <sup>2</sup> ×0.8mm)	1.3	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Electrical Characteristics at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	100			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =100V, V <sub>GS</sub> =0			100	μA
Gate-to-Source Leakage Current	IGSS	$V_{GS}=\pm 12V, V_{DS}=0$			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.0		2.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =500mA	0.6	1.0		S
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =500mA, V <sub>GS</sub> =10V		2.7	3.5	Ω
	R <sub>DS(on)</sub>	I <sub>D</sub> =500mA, V <sub>GS</sub> =4V		3.2	4.2	Ω

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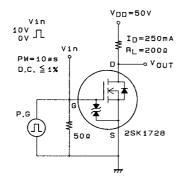
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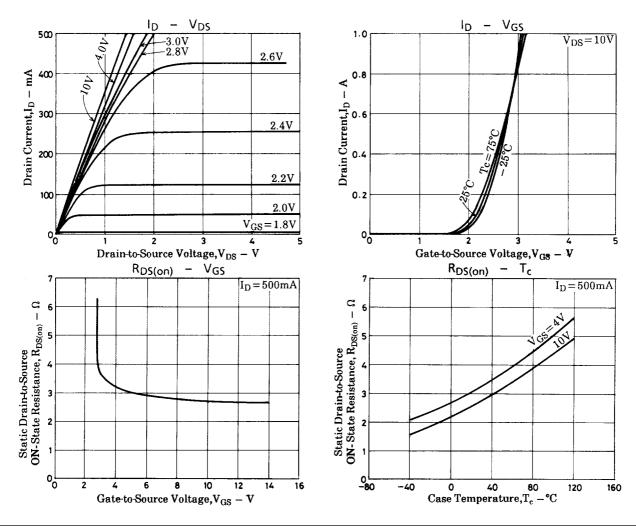
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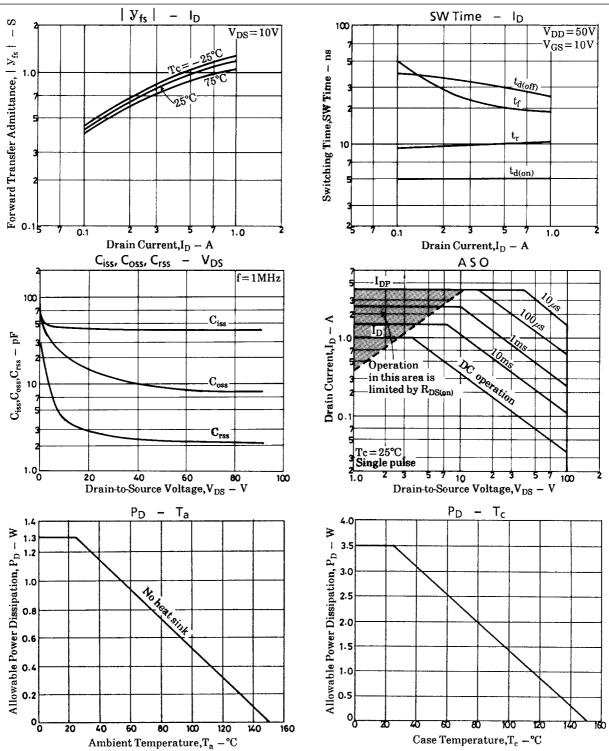
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Parameter	Symbol	Conditions	Ratings	Unit
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz	45	pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz	15	pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz	3	pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit	5	ns
Rise Time	tr	See specified Test Circuit	10	ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	30	ns
Fall Time	tf	See specified Test Circuit	20	ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0	1.0	V

#### **Switching Time Test Circuit**







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