

2SJ189

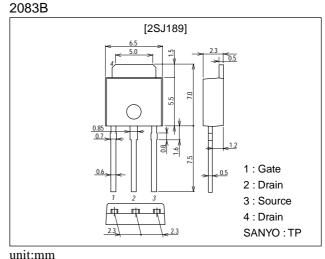
Ultrahigh-Speed Switching Applications

Features

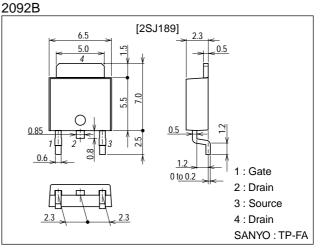
- \cdot Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

Package Dimensions

unit:mm



unit:mm



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Specifications

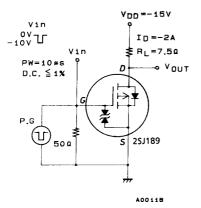
Absolute Maximum Ratings at $Ta = 25^{\circ}C$

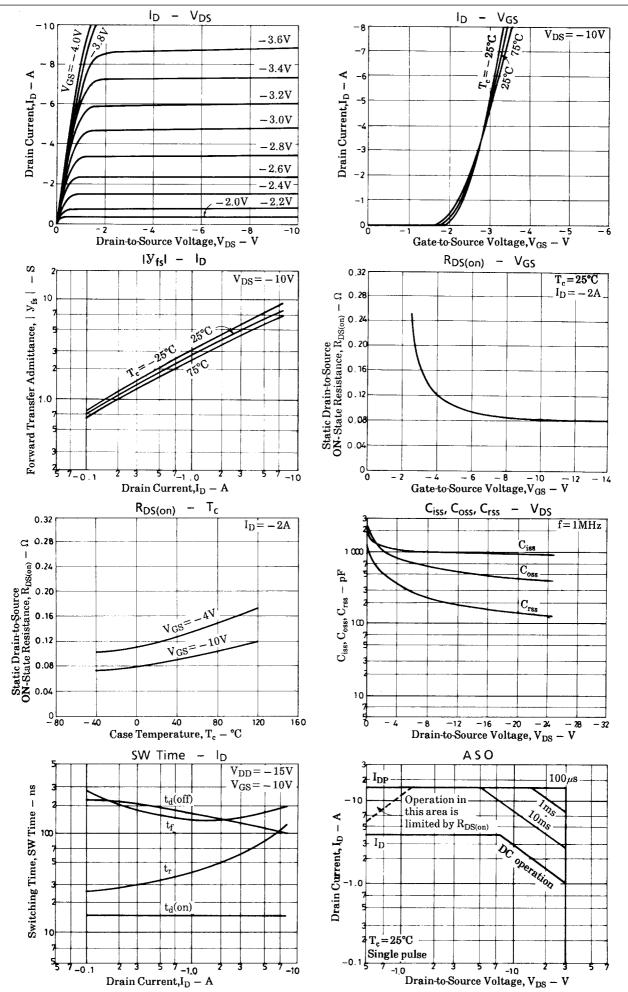
| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|------------------|------------------------|-------------|------|
| Drain-to-Source Voltage | V _{DSS} | | -30 | V |
| Gate-to-Source Voltage | V _{GSS} | | ±15 | V |
| Drain Current (DC) | ۱ _D | | -4 | A |
| Drain Current (Pulse) | IDP | PW≤10µs, duty cycle≤1% | -16 | A |
| Allowable Power Dissipation | PD | Tc=25°C | 30 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

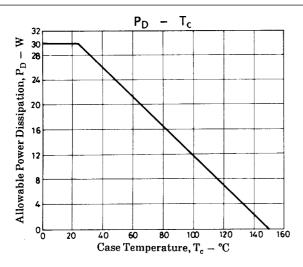
Electrical Characteristics at Ta = 25°C

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|--|-----------------------|---|---------|------|------|------|
| | | | min | typ | max | Unit |
| Drain-to-Source Breakdown Voltage | V _(BR) DSS | I _D =-1mA, V _{GS} =0 | -30 | | | V |
| Gate-to-Source Breakdown Voltage | V(BR)GSS | I _G =±100µA, V _{DS} =0 | ±15 | | | V |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =-30V, V _{GS} =0 | | | -100 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±12V, V _{DS} =0 | | | ±10 | μΑ |
| Cutoff Voltage | V _{GS(off)} | V _{DS} =-10V, I _D =-1mA | -1.0 | | -2.0 | V |
| Forward Transfer Admittance | yfs | V _{DS} =-10V, I _D =-2A | 2.5 | 4 | | S |
| Static Drain-to-Source ON-State Resistance | R _{DS(on)} | I _D =-2A, V _{GS} =-10V | | 85 | 120 | mΩ |
| | R _{DS(on)} | I _D =-2A, V _{GS} =-4V | | 120 | 170 | mΩ |
| Input Capacitance | Ciss | V _{DS} =-10V, f=1MHz | | 1000 | | pF |
| Output Capacitance | Coss | V _{DS} =-10V, f=1MHz | | 600 | | pF |
| Reverse Transfer Capacitance | Crss | V _{DS} =-10V, f=1MHz | | 220 | | pF |
| Turn-ON Delay Time | td(on) | See specified Test Circuit | | 15 | | ns |
| Rise Time | t _r | See specified Test Circuit | | 50 | | ns |
| Turn-OFF Delay Time | td(off) | See specified Test Circuit | | 145 | | ns |
| Fall Time | t _f | See specified Test Circuit | | 145 | | ns |
| Diode Forward Voltage | V _{SD} | I _S =-4A, V _{GS} =0 | | -1.0 | -1.5 | V |

Switching Time Test Circuit







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