NPN Triple Diffused Planar Silicon Transistor

2SD1159



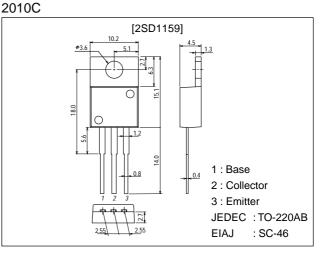
# TV Horizontal Deflection Output, High-Current Switching Applications

### Features

 $\cdot$  Capable of efficient drive with small internal loss due to excellent  $t_{\rm f}.$ 

## Package Dimensions

unit:mm



# **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		200	V
Collector-to-Emitter Voltage	VCEO		60	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	ι <sub>C</sub>		4.5	A
Collector Current (Pulse)	I <sub>CP</sub>		10	A
Collector Dissipation	PC	Tc=25°C	40	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V <sub>CB</sub> =40V, I <sub>E</sub> =0			0.1	mA
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =5V, I <sub>C</sub> =0			0.1	mA
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =5V, I <sub>C</sub> =1A	30		160	
	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =4A	25			
Gain-Bandwidth Product	fT	V <sub>CE</sub> =5V, I <sub>C</sub> =1A		10		MHz
Collector-to-Emitter Saturation Voltage	VCE(sat)	I <sub>C</sub> =4A, I <sub>B</sub> =0.4A		0.5	1.0	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.4A			1.5	V

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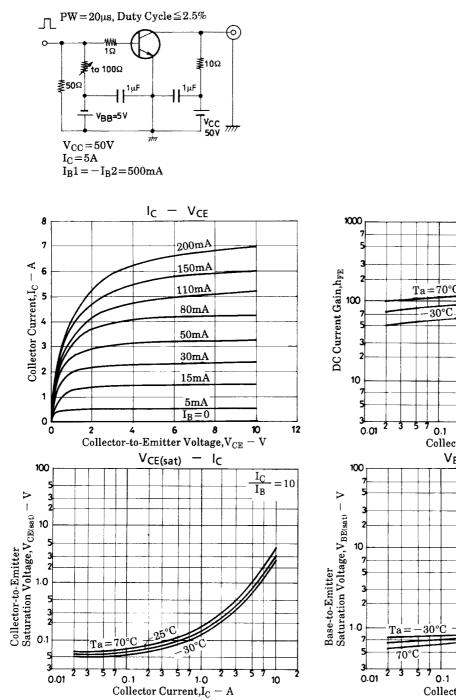
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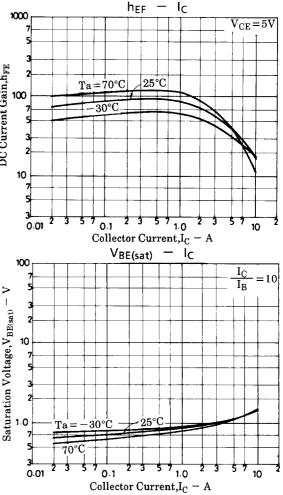
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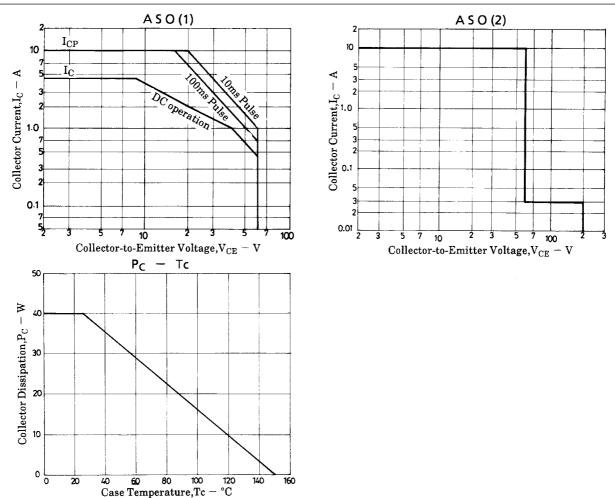
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =5mA, I <sub>E</sub> =0	200			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =5mA, R <sub>BE</sub> =∞	60			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=5mA, IC=0	6			V
Fall Time	t <sub>f</sub>	See specified Test Circuit.		0.2	0.5	μs

### **Specified Test Circuit**







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