NPN Triple Diffused Planar Silicon Transistor



2SC4423

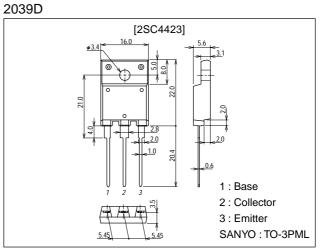
# 400V/12A Switching Regulator Applications

### Features

- $\cdot$  High breakdown voltage, high reliability.
- $\cdot$  Fast switching speed (t\_f : 0.1 \mu s typ).
- $\cdot$  Wide ASO.
- · Adoption of MBIT process.
- $\cdot$  Micaless package facilitating easy mounting.

## **Package Dimensions**

unit:mm



# **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		500	V
Collector-to-Emitter Voltage	VCEO		400	V
Emitter-to-Base Voltage	VEBO		7	V
Collector Current	۱ <sub>C</sub>		12	Α
Collector Current (Pulse)	ICP	PW≤300µs, duty cycle≤10%	25	Α
Base Current	Ι <sub>Β</sub>		4	А
Collector Dissipation	PC		3	W
		Tc=25°C	55	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Electrical Characteristics at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V <sub>CB</sub> =400V, I <sub>E</sub> =0			10	μA
Emitter Cutoff Current	IEBO	$V_{EB}=5V, I_{C}=0$			10	μΑ
DC Current Gain	h <sub>FE</sub> 1*	$V_{CE}=5V, I_{C}=1.6A$	15		50	
	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =8A	10			
	hFE3	V <sub>CE</sub> =5V, I <sub>C</sub> =10mA	10			

\*: The  $h_{FE}1$  of the 2SC4423 is classified as follows. When specifying the  $h_{FE}1$  rank, specify two ranks or more in principle.

15 L 30 20 M 40 30 N 50

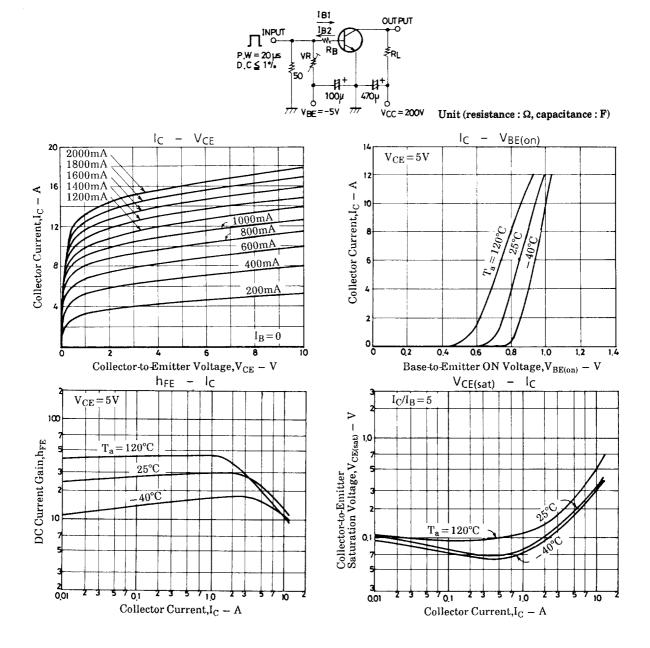
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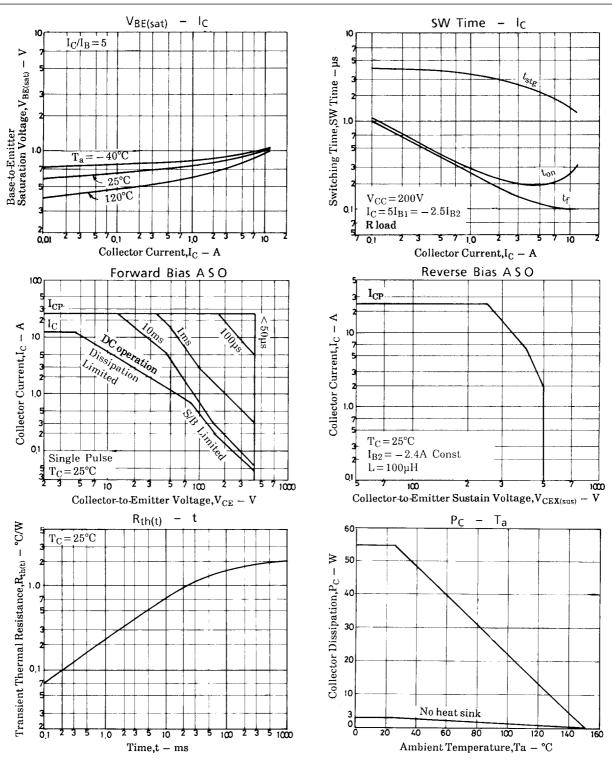
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =8A, I <sub>B</sub> =1.6A			0.8	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =8A, I <sub>B</sub> =1.6A			1.5	V
Gain-Bandwidth Product	fT	V <sub>CE</sub> =10V, I <sub>C</sub> =1.6A		20		MHz
Output Capacitance	Cob	V <sub>CB</sub> =10V, f=1MHz		160		pF
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=1mA, IE=0	500			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =5mA, R <sub>BE</sub> =∞	400			V
Emitter-to-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =1mA, I <sub>C</sub> =0	7			V
Collector-to-Emitter Sustain Voltage	V <sub>CEX(sus)</sub>	I <sub>C</sub> =6A, I <sub>B1</sub> =0.6A, I <sub>B2</sub> =–2.4A, L=500µH, Clamped	400			V
Turn-ON Time	ton	I <sub>C</sub> =10A, I <sub>B1</sub> =2A, I <sub>B2</sub> =-4A, R <sub>L</sub> =20Ω, V <sub>CC</sub> =200V			0.5	μs
Storage Time	tstg	I <sub>C</sub> =10A, I <sub>B1</sub> =2A, I <sub>B2</sub> =-4A, R <sub>L</sub> =20Ω, V <sub>CC</sub> =200V			2.5	μs
Fall Time	tf	I <sub>C</sub> =10A, I <sub>B1</sub> =2A, I <sub>B2</sub> =-4A, R <sub>L</sub> =20Ω, V <sub>CC</sub> =200V			0.3	μs

### Switching Time Test Circuit





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