

2SB1167/2SD1724

100V/3A Switching Applications

Features

· Relay drivers, high-speed inverters, converters.

Features

- · Low collector-to-emitter saturation voltage.
- · High f_T.
- · Excellent linearity of hFE.
- · Fast switching time.

(): 2SB1167

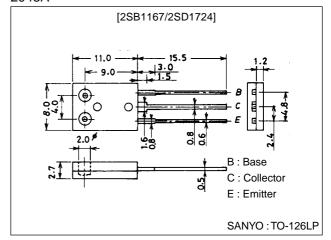
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2043A



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(–)120	V
Collector-to-Emitter Voltage	V _{CEO}		(-)100	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	IC		(–)3	Α
Collector Current (Pulse)	I _{CP}		(–)6	Α
Collector Dissipation	PC		1.2	W
		Tc=25°C	20	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =(-)100V, I _E =0			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)5V, I _C =(-)0.5A	70*		400*	
	h _{FE} 2	V _{CE} =(-)5V, I _C =(-)2A	40			
Gain-Bandwidth Product	fT	V _{CE} =(-)10V, I _C =(-)0.5A		(130)		MHz
				180		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		25(40)		pF

 $[\]ast$: The 2SB1167/2SD1724 are classified by 0.5A h_{FE} as follows :

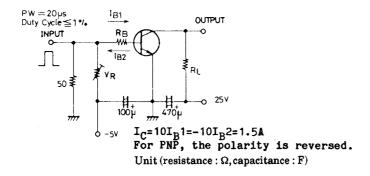
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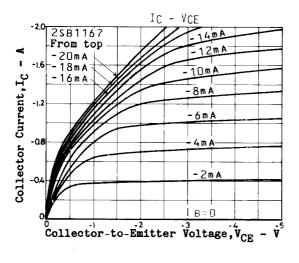
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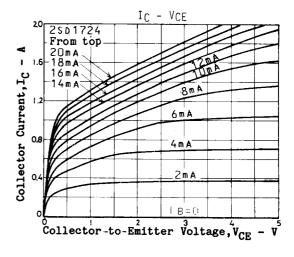
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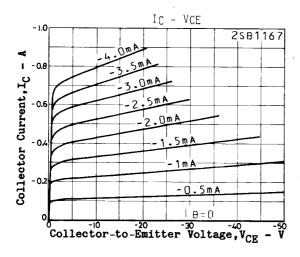
Parameter	Symbol	Conditions	Ratings			Lloit
	Symbol		min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)1.5A, I _B =(-)0.15A		(-200)	(-500)	mV
				150	400	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)1.5A, I _B =(-)0.15A		(-)0.9	(–)1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μΑ, I _E =0	(–)120			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =(−)1mA, R _{BE} =∞	(-)100			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	$I_E=(-)10\mu A, I_C=0$	(–)6			V
Turn-ON Time	ton	See specified Test Circuit		(100)		ns
				100		ns
Storage Time	t _{stg}	See specified Test Circuit		900		ns
				(800)		ns
Fall Time	t _f	See specified Test Circuit		50(50)		ns

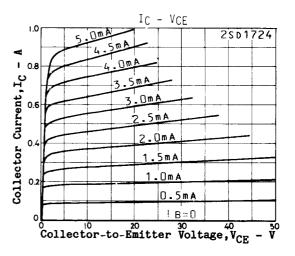
Switching Time Test Circuit

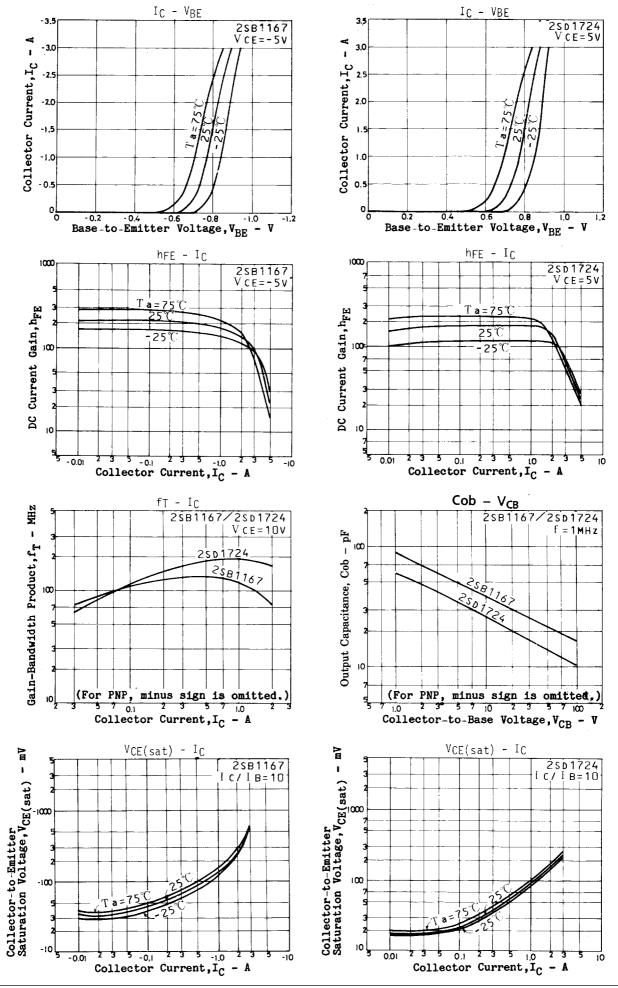




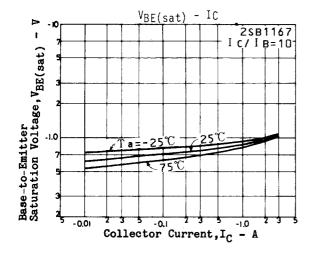


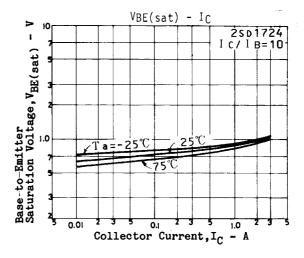


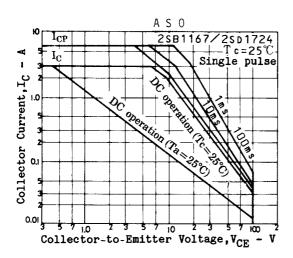


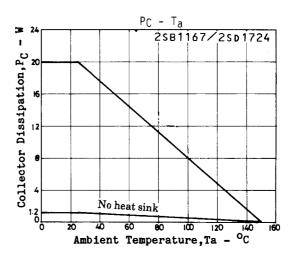


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