PNP Epitaxial Planar Silicon Transistor



2SA1866

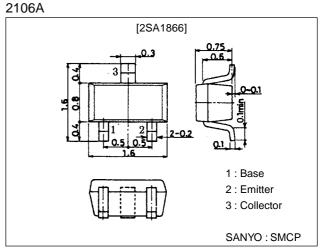
Muting Circuits, Driver Applications

Features

- · On-chip bias resistors (R1=47k Ω , R2=47k Ω).
- Very small-sized package making 2SA1866-applied sets small and slim.
- · Small ON resistance.
- \cdot High gain-bandwidth product $f_{\text{T}}.$

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-15	V
Collector-to-Emitter Voltage	VCEO		-15	V
Emitter-to-Base Voltage	V _{EBO}		-10	V
Input Voltage	VIN		-14	V
Collector Current	IC		-50	mA
Collector Current (Pulse)	I _{CP}		-100	mA
Base Current	Ι _Β		-10	mA
Collector Dissipation	PC		150	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CBO}	V _{CB} =-10V, I _E =0			-0.1	μA
Collector Cutoff Current	ICEO	V _{CE} =-10V, I _E =0			-0.5	μA
Emitter Cutoff Current	IEBO	V _{EB} =-5V, I _C =0	-30	-53	-80	μA
DC Current Gain	hFE	V _{CE} =-2V, I _C =-5mA	100			
Gain-Bandwidth Product	fT*	V _{CE} =-5V, I _C =-10mA		600		MHz
Output Capacitance	C _{ob*}	V _{CB} =-10V, f=1MHz		0.9		pF

* : Charactersistic of the constituent transistor.

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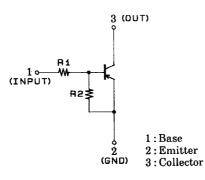
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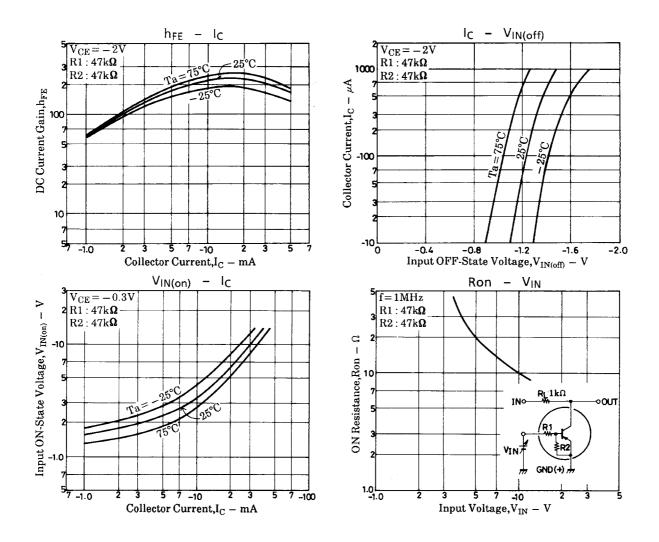
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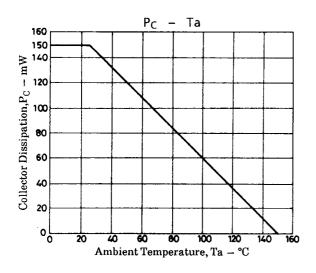
Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =-2mA, I _B =-0.2mA		-20	-60	mV
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =-10µA, I _E =0	-15			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =−1mA, R _{BE} =∞	-15			V
Input OFF-State Voltage	VIN(off)	V _{CE} =-2V, I _C =-100µA	-0.8	-1.2	-1.5	V
Input ON-State Voltage	V _{IN(on)}	V _{CE} =-0.3V, I _C =-5mA	-1.0	-2.3	-4.0	V
Input Resistance	R1		32	47	62	kΩ
Resistance Ratio	R1/R2		0.9	1.0	1.1	
On Resistance	R _{on}	V _{IN} =-10V, f=1MHz		10.0		Ω

Marking : CA

Electrical Connection







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