PNP Epitaxial Planar Silicon Transistor



2SA1838

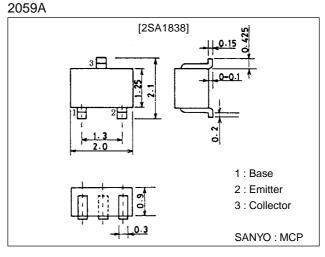
Muting Circuit Applications

Features

- Very small-sized package permitting 2SA1838applied sets to be made small and slim.
- \cdot Small output capacitance.
- \cdot Low collector-to-emitter saturation voltage.
- · Low ON resistance.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		-15	V
Collector-to-Emitter Voltage	VCEO		-10	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	ιc		-100	mA
Colletor Current (Pulse)	ICP		-200	mA
Base Current	Ι _Β		-20	mA
Collector Dissipation	PC		200	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

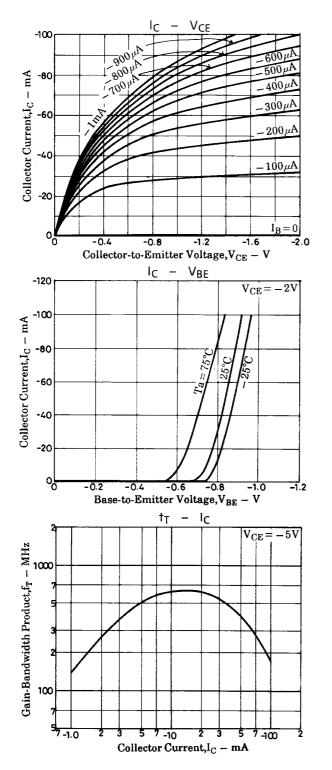
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =-12V, I _E =0			-0.1	μA
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0			-0.1	μA
DC Current Gain	h _{FE}	$V_{CE}=-2V, I_{C}=-5mA$	200		600	
Gain-Bandwidth Product	fT	V _{CE} =-5V, I _C =-10mA		600		MHz
Output Capacitance	C _{ob}	V _{CB} =-10V, f=1MHz		0.9		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =-10mA, I _B =-1mA		-0.04	-0.15	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =-10mA, I _B =-1mA		-0.82	-1.1	V

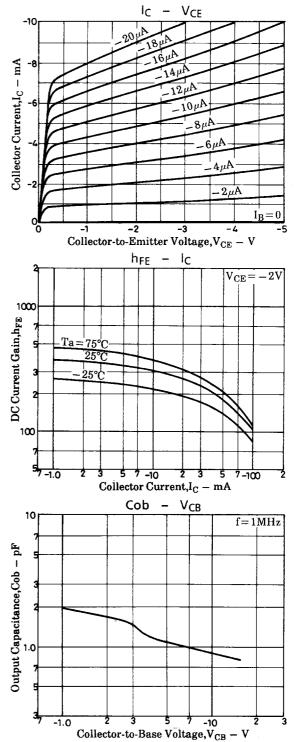
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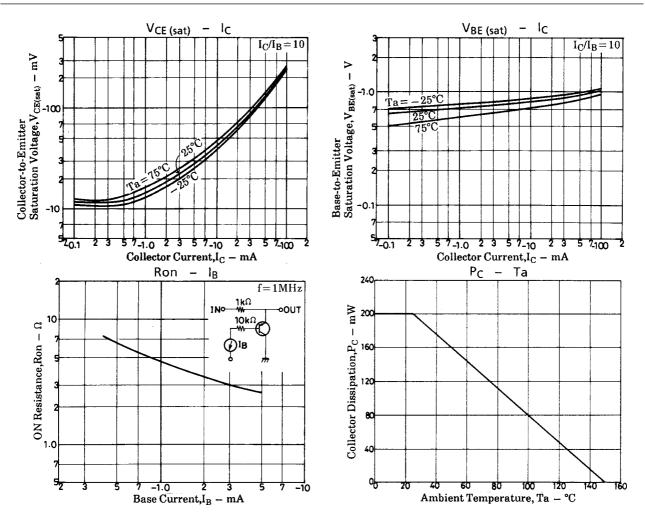
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μΑ, I _E =0	-15			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =−1mA, R _{BE} =∞	-10			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μΑ, I _C =0	-5			V
On Resistance	R _{on}	I _B =–3mA, f=1MHz		3.0		Ω







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