2SA1827/2SC4731



100V/4A Switching Applications

Applications

· Relay drivers, high-speed inverters, converters, and other general high-current switching applications.

Features

- · Low collector-to-emitter saturation voltage.
- · High Gain-Bandwidth Product.
- · Excellent linearity of DC Current Gain.
- · Fast switching speed.

(): 2SA1827

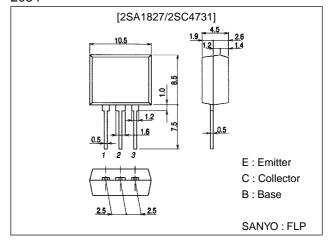
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2084



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)120	V
Collector-to-Emitter Voltage	V _{CEO}		(-)100	V
Emitter-to-Base Voltage	V _{EBO}		(-)6	V
Collector Current	IC		(-)4	Α
Collector Current (Pulse)	ICP		(-)8	Α
Base Current	IB		(–)0.8	Α
Collector Dissipation	PC		1.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
Falameter	Symbol		min	typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)100V, I _E =0			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(–)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)5V, I _C =(-)500mA	100*		400*	
	h _{FE2}	V _{CE} =(-)5V, I _C =(-)3A	40			
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)500mA		(130)		MHz
				180		MHz

^{*:} The 2SA827/2SC4731 are classified by 500mA hFE as follows:

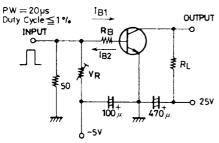
	100	R	200	140	S	280	200	Т	400
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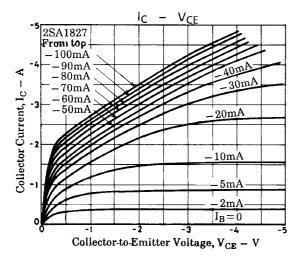
2SA1827/2SC4731

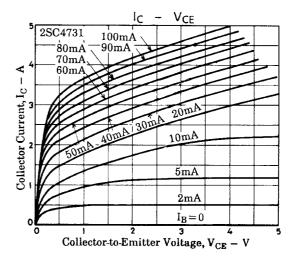
Parameter	Cymphol	Conditions		Unit		
Parameter	Symbol	Conditions		typ	max	Unit
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(65)40		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)2A, I _B =(-)0.2A		(-200)	(-500)	mV
				150	400	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)2A, I _B =(-)0.2A		(–)0.9	(–)1.2	mV
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μΑ, I _E =0	(-)120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	(-)100			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)10μA, I _C =0	(-)6			V
Turn-ON Time	ton	See specified Test Circuit		100		ns
Storage Time	tstg	See specified Test CIrcuit		(800)		ns
				900		ns
Fall Time	t _f	See specified Test Circuit		50		ns

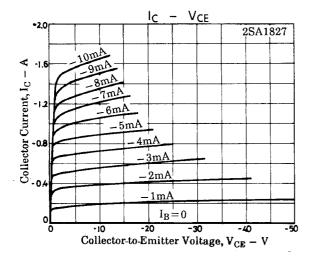
Switching Time Test Circuit

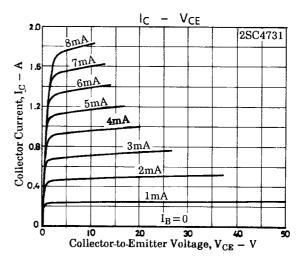


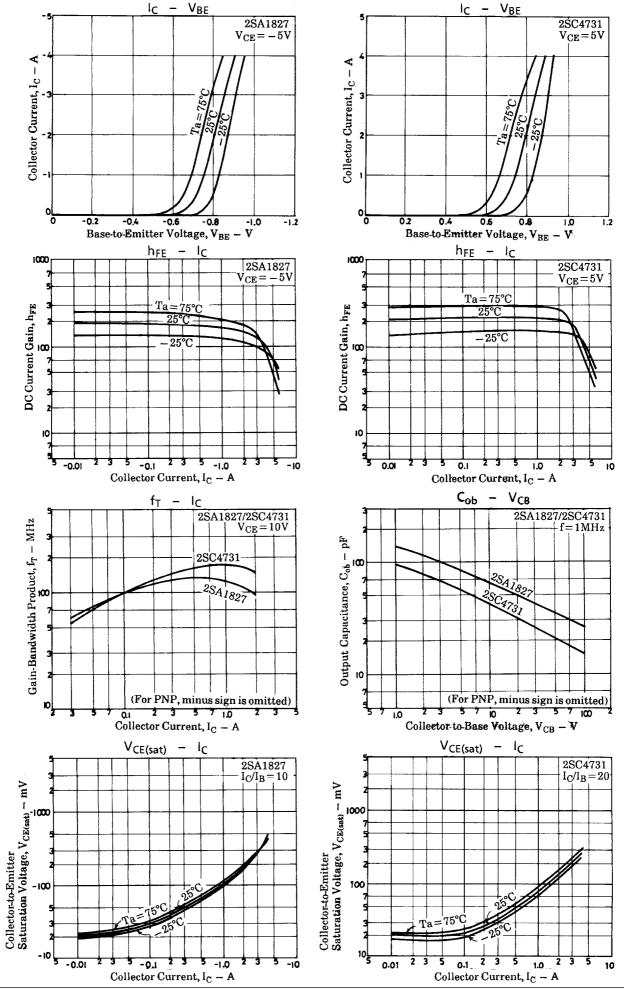
 $I_C = 10I_B1 = -10I_B2 = 2A$ (For PNP, the polarity is reversed). Unit (resistance : Ω , capacitance : F)

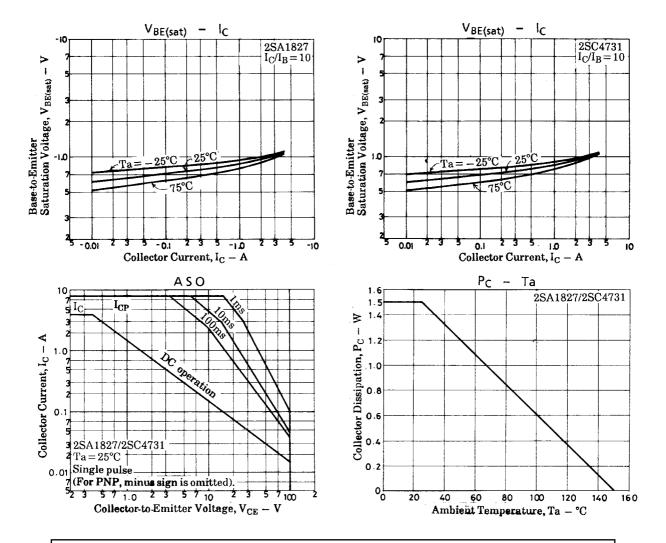












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