

NOTES: UNLESS OTHERWISE SPECIFIED;

1. ALL RESISTOR VALUES ARE IN OHMS, 1/10W, 5%
2. ALL CAPACITORS ARE 50VDC OR HIGHER.
3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).

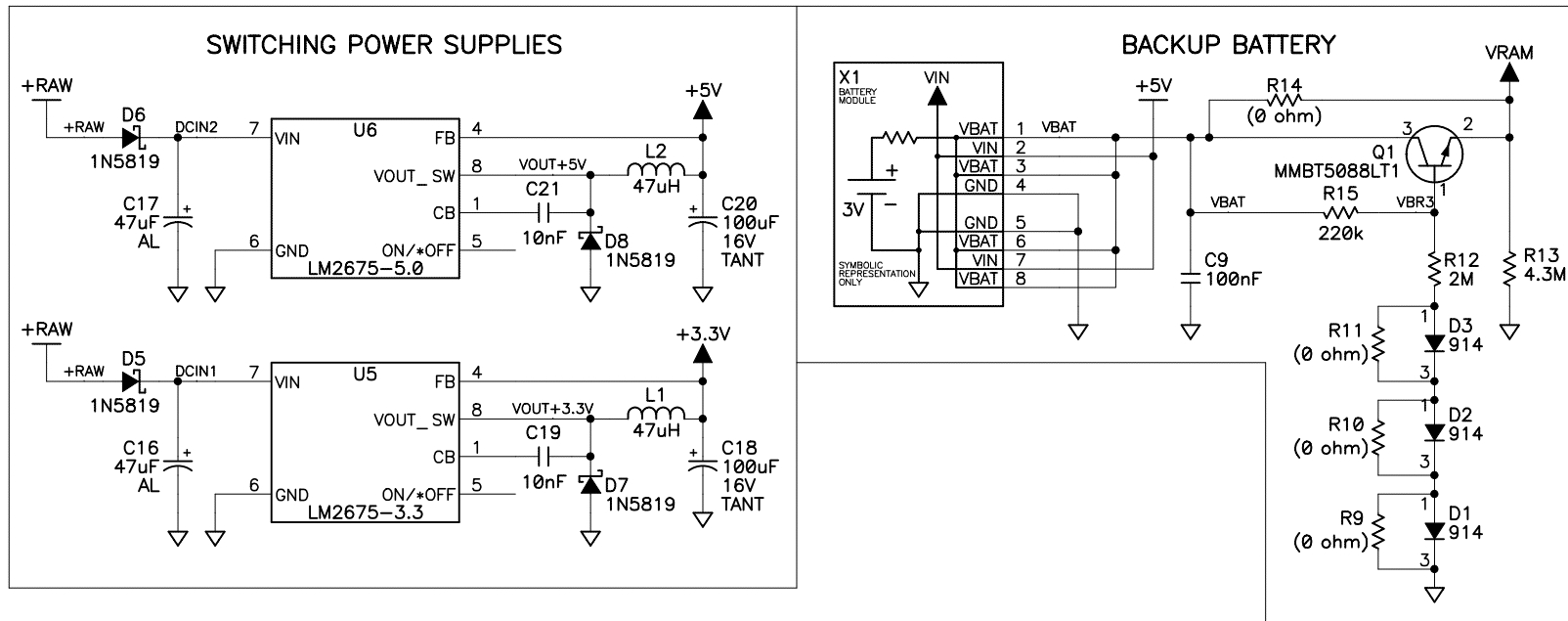
REVISION HISTORY

REVISION APPROVAL

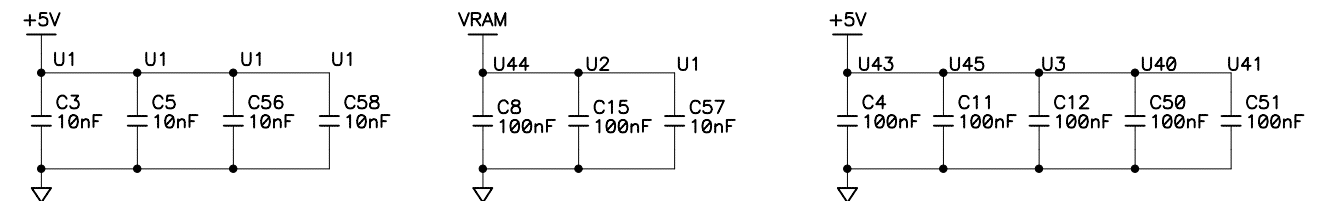
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E10844	INITIAL RELEASE - TRACKS A/W @ REV-A				

TABLE A

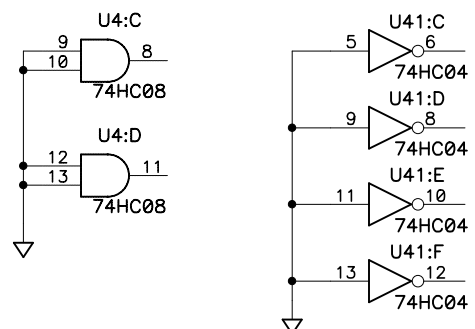
REF DES	DEVICE	DEVICE VOLTAGE INFORMATION					DEVICE: FILTER CAP REF DES(S)
		GND	+5V	+3.3V	VRAM	NO CONNECTS	
U1	RABBIT 2000	2,27,39,52,77,89	3,28,53,78,92		42		C3, 5 56, 58 PIN-42 - C57
U2	HA7210	4			1		C15
U3	ETC811L	1	4				C12
U4	74HC08	7	14				
U5	LM2675-3.3					2,3	
U6	LM2675-5.0					2,3	
U40	SP483E	5	8				C50
U41	74HC04	7	14				C51
U42	74HC125	7	14				
U43	232A	15	16				C4
U44	SRAM 512K X 8	16			32		C8
	SRAM 32K X 8	14			28		
U45	FLASH	24	8				C11
U46	FLASH	24	8				



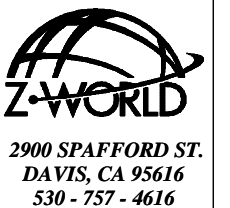
Decoupling Capacitors

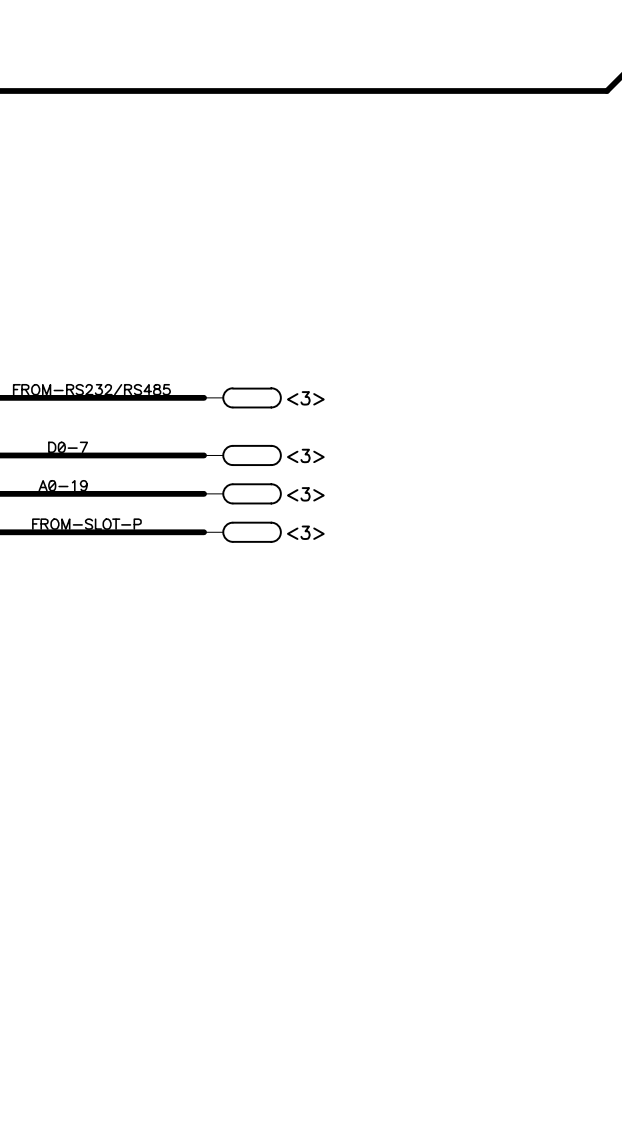
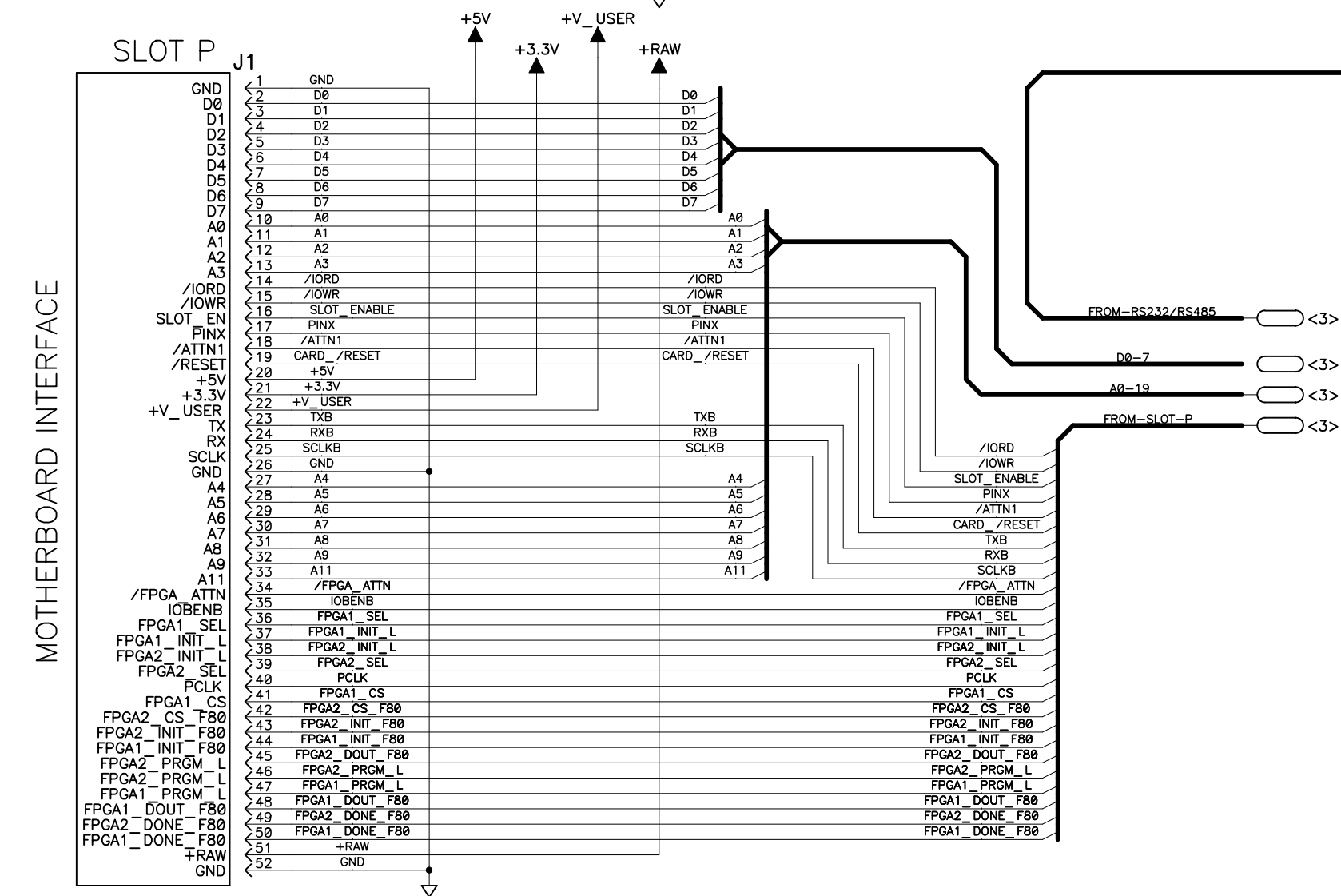
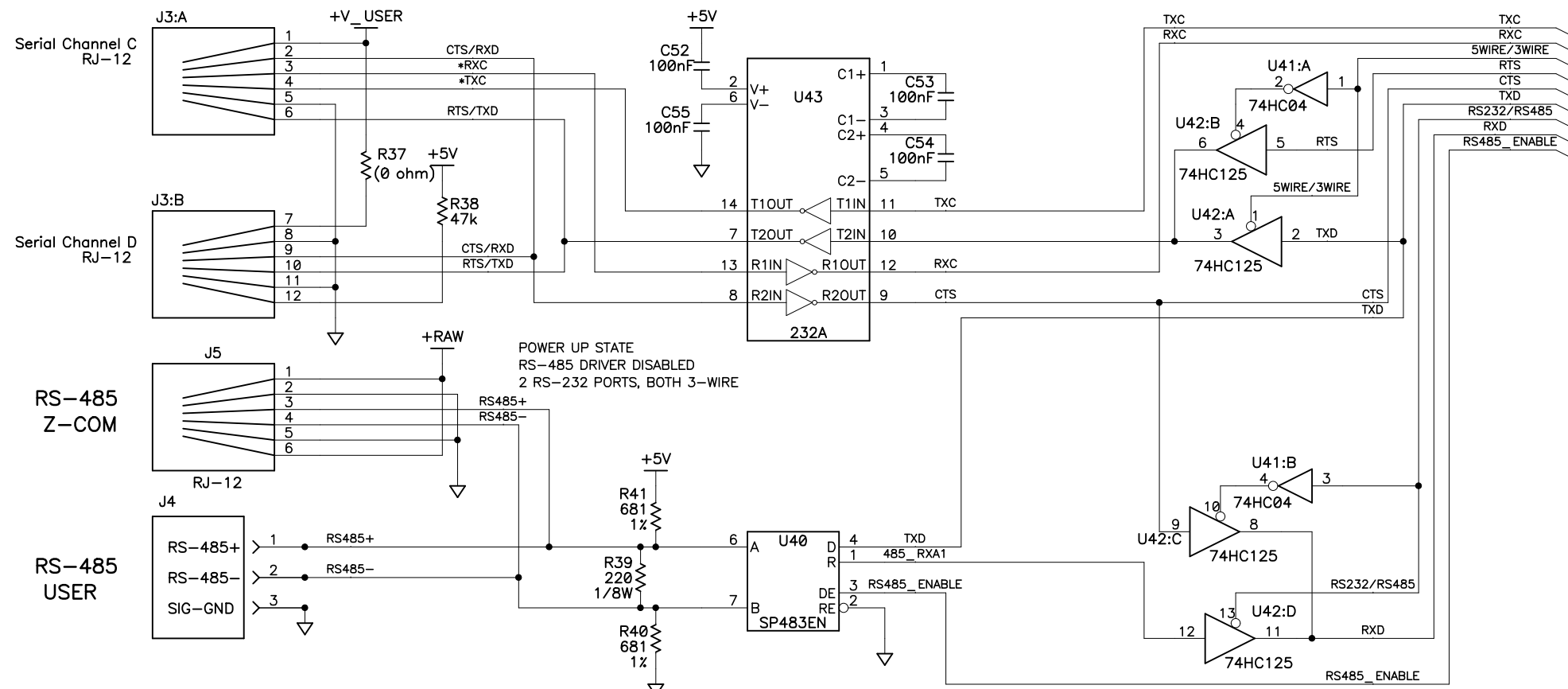


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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	
		DRAWN BY: (INITIAL RELEASE)	21JUN99	SCHEMATIC GALAXY PROCESSOR BOARD	
		REVISED BY:	10Apr00		
		APPROVALS: INITIAL RELEASE		SIZE B DWG NO. 090-0093	
		PROJECT ENGINEER:			
		ENGINEERING MANAGER:		SCALE NONE	RELEASE DATE
		SIGNATURES	DATE	SHEET 1 OF 3	



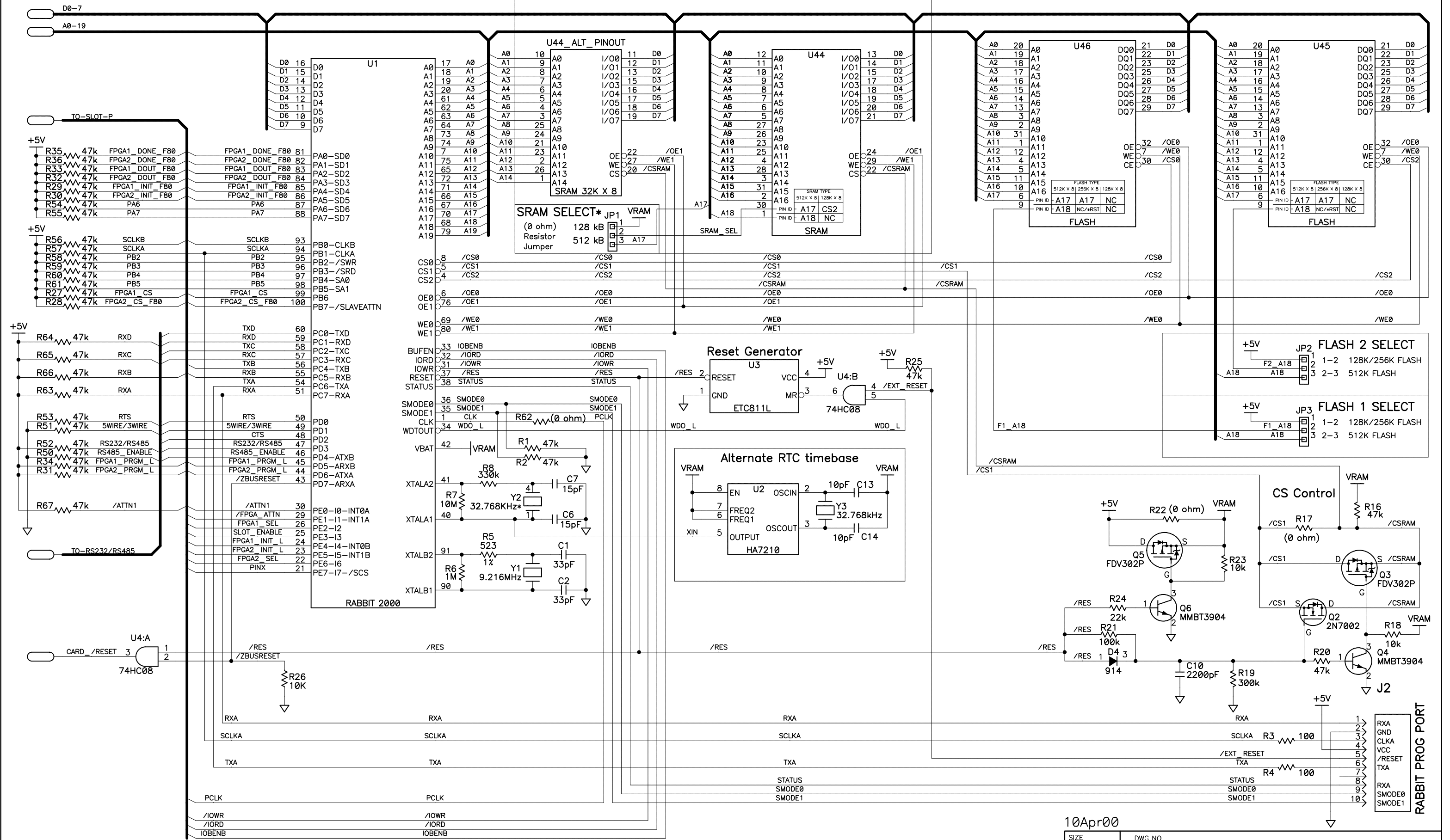


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10Apr00

SIZE B	DWG NO. 090-0093
SCALE NONE	REV LTR A
SHEET 2 OF 3	

ONLY ONE DEVICE INSTALLED U44: OPTIONAL 512K, 256K, 128K, OR 32K SRAM



10Apr00

SIZE	DWG NO.
B	090-0093
SCALE	REV LTR
NONE	A
SHEET	3 OF 3

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