

TM-1320-15



General Description

The TM-1320-15 is a miniature, high-resolution monochrome progressive scan CCD camera. The imager resolution is 1300 x 1030 pixels and the frame rate is 15 frames per second. The interline transfer CCD permits full vertical and horizontal resolution of high-speed shuttered images. The electronic shutter has speeds up to 1/16,000 sec. and can be reset asynchronously by external pulse control.

The TM-1320-15 has a patent-pending, PULNiX exclusive, built-in look-up table (LUT). This full dynamic range control function can be set at externally selectable knee slopes to optimize the CCD's full dynamic range in the normal output signal range. It also provides fast 10-bit to 8-bit pre-processing for effective image feature enhancement. The camera has both analog and digital (RS-644) output for interfacing with frame grabbers.

All camera control functions are externally controlled via a user-friendly RS-232C graphical interface provided by PULNiX. The TM-1320-15 has a pixel clock locking phase lock loop (PLL) for multi-camera operation which requires exact data and clock phases from each camera (optional).*

Applications for the TM-1320-15 include machine vision, medical imaging, intelligent transportation systems, high-definition graphics, gauging, character recognition, documents archiving, and surveillance.

Asynchronous Reset

The TM-1320-15's asynchronous reset is flexible and accepts external horizontal drive (HD) for phase locking. When the VINIT pulse is applied, it resets the camera's scanning and purging of the CCD. There are two modes to control the asynchronous reset and shutter speed:

- 1 External VINIT with pulse width. The duration between pulse edges controls the shutter speed externally.
- 2 Internal shutter speed control. The speed control varies from 1/125 to 1/16,000 sec. The video signal and FDV starts with internal V reset timing related to shutter speed.

Product Summary

- High-resolution 2/3" progressive scan 1300(H) x 1030(V) interline transfer CCD imager
- Miniature 44x44x64 mm housing with high-rel connector
- Digital RS-644 (LVDS) output or analog output
- 15 frames per second (12 frame, 24 frame models available)
- Maximum dynamic range control with PULNiX-exclusive, patent-pending built-in look-up table (Gamma, knee, user parameters)
- Full frame integration, two-row binning (1300x515 pixels) at 30 fps.
- Full-frame shutter to 1/16,000 sec.
- Asynchronous reset with external shutter control
- RS-232 external control

Electronic Shutter

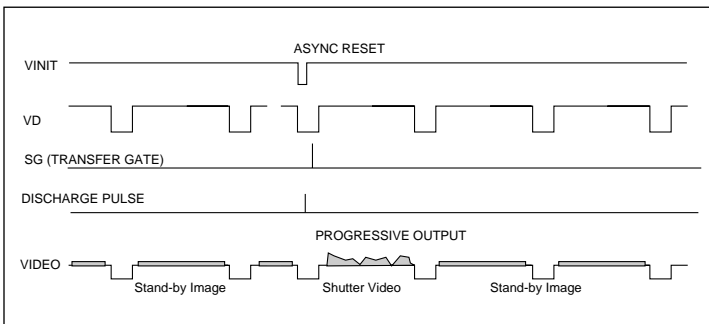
The TM-1320-15 has a substrate drain-type shutter mechanism which provides a superb picture at various speeds without smearing. A built-in manual shutter speed control selects the electronic shutter rate of 1/60 (non-async mode only), 1/125, 1/250, 1/500, 1/1,000, 1/2,000, 1/4,000, 1/8,000, or 1/16,000 second.

The CCD discharges when discharge pulse is applied via internal shutter control. With a negative pulse to VINIT, the camera resets and purges the CCD charge momentarily. Then it starts integrating for the period of preset shutter control time by either an external pulse width or internal shutter control.

Progressive scanning permits a full 1030 lines of vertical resolution, as compared to a conventional CCD camera which captures only half the vertical lines per shutter.

Integration

The CCD imager of the TM-1320-15 can be exposed for longer than the normal scan timing of 1/15 sec. This integration feature provides extra sensitivity for dark-environment applications. The progressive-scan imager permits a full frame of resolution in non-interlace format. Integration is achieved by applying INTEG signal to pin #11 of the 12-pin connector or pin #6 of the 31-pin connector, or by feeding VINIT pulse width control up to 1 sec at the pulse width.



* Option not currently available

TM-1320-15

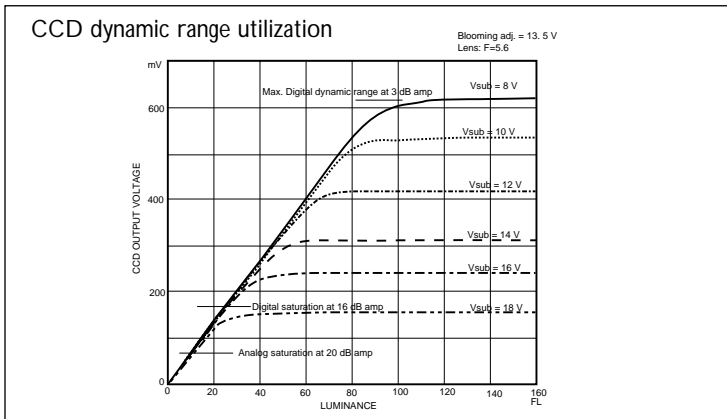
TM-1320-15 Progressive Scan High-Resolution Shutter Camera

Dynamic Range Control

Typical interline transfer CCDs have fixed noise levels based on dark current (thermal or KT noise), pattern noise, and operating clock speed. Typically for a 1k x 1k CCD operating at 20MHz pixel clock, the noise level is around 30 electrons. The maximum capacity of the CCD charges is limited by the well capacity at saturation. The range is limited by the structure and the pixel size.

The TM-1320-15 uses a 2/3" CCD with 6.7 μm x 6.7 μm pixel and three-phase vertical shift register structure. The well capacity is 16,000 electrons. The theoretical dynamic range is 16,000:30 = 5331:1 (54 dB).

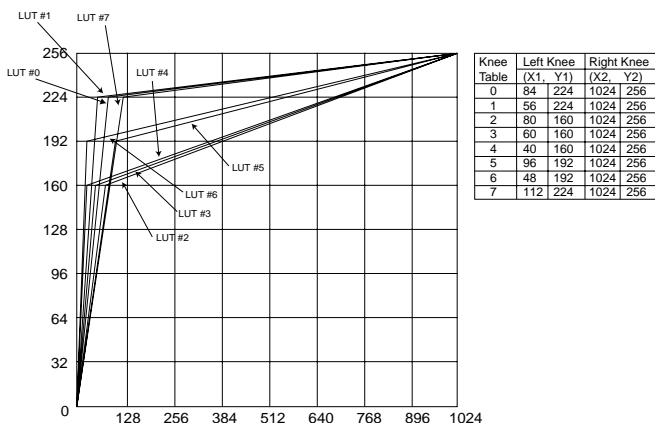
A typical CCD camera does not use the full dynamic range due to the nominal gain and the output specification such as RS-170. The typical CCD camera has its gain set at 16 to 22 dB and the RS-170 video level is 714 mV. Using 20 dB gain for the calculation, CCD output is limited to 714/10 = 71.4 mV. Since the CCD's saturation voltage is 400 mV to 500 mV, it uses less than 1/5 of the full dynamic range.



Machine vision and outdoor applications cannot afford to miss image information behind the saturation, which is why the dynamic range adaptation is critical.

Programmable LUT and Knee Control (patent pending)

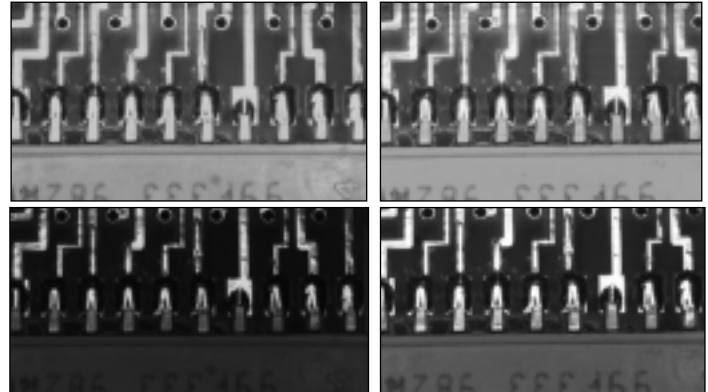
The TM-1320-15 has a built-in LUT (look-up table) for dynamic range control.



Note: The second knee point on the built-in LUT defaults to position (1024, 256). To reposition this point, click on it and drag it to a new location.

At a specific gain setting, the offset (minimum level... dark point) and A/D reference top voltage (maximum level... saturation point) are set to 10-bit A/D input so that the full dynamic range of the CCD is utilized at 10-bit references as the input and the LUT output is converted into 8-bit to adjust the gamma correction.

The look-up table has two knee points (variable gamma selection) that allow the 10 bits to be segmented into three regions. The look-up table selection can be made either by variable knee curve or by direct input of the knee coordinates.



Linear image: When PCB surface is visible, metal trace is saturated. If the metal surface appears, then the plastic surface is too dark.

Knee-controlled image: The upper is LUT #6, the lower is #7. Both show the full dynamic range with different effects.

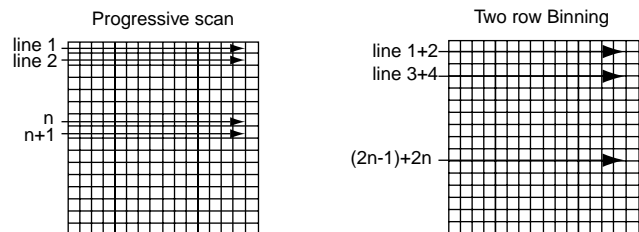
Scan Mode

Full Progressive Scan

Normal scanning mode for the TM-1320-15 is 1300 x 1030 pixels. The standard speed with single-channel output is 15 frames/sec at the pixel clock of 25 MHz*. Unlike an interlace scan camera, the TM-1320-15 reads every line from top to bottom, resulting in all lines being obtained per captured image frame with electronic shutter.

Two-Row Binning

This is a standard feature in the TM-1320-15. The camera scans two rows together and outputs 1300 (H) x 515 (V) pixels at 30 frames per second. Two-row binning is typically implemented to increase the frame rate.



The TM-1320-15 is also available with options for 12fps (20MHz pixel clock) and 24 fps (40MHz pixel clock).

TM-1320-15 Progressive Scan High-Resolution Shutter Camera

External Sync

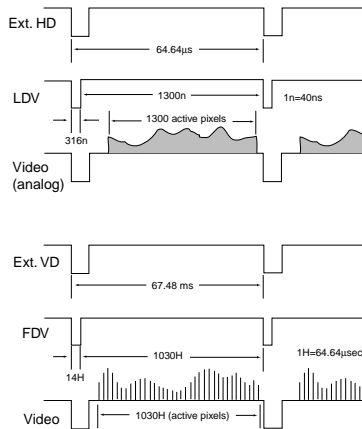
The TM-1320-15 accepts an external sync. of standard HD and VD at TTL level for general locking to a system sync. and clock. The external sync. is only available for 15-frame mode. The frequency requirement is as follows:

$$f_{HD} = 15.47 \text{ KHz} \pm 5\%$$

$$f_{VD} = 15.00 \text{ Hz} \pm 5\%$$

(Internal Master clock = 50.0 MHz,
Pixel clock = 25.0 MHz)

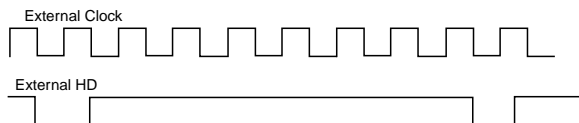
Please contact PULNiX for TM-1320-15 timing charts.



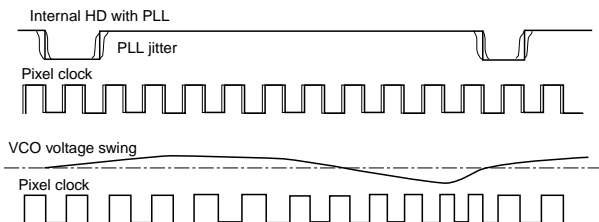
Pixel Clock Locking (optional)*

For multiple-camera applications such as 2D or 3D measurement, a parallel digital data input capability is important. The high resolution and precision measurement require that camera data be consistent and free from phase lock loop (PLL) jitter. This is especially important for digital output multiplexing.

The TM-1320-15 has an optional capability for pixel clock locking. It takes RS-644 (differential) external clock and H and V reset sync (TTL or RS-644). By doing this, each camera's digital data is in-phase with the pixel clock (data clock) for multiplexing or parallel input to a frame grabber.

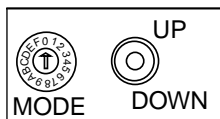


Phase lock loop problem for pixel clock phase



Mode Switches

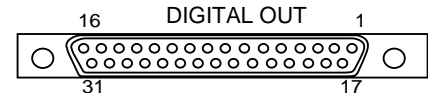
Various modes can be implemented with the rear panel-mode selection switch and Up/Down switch as well as RS-232 external control. When RS-232 is connected, the command over-rites the rear panel switch settings.



Mode Switch	Up/Down Switch	Functions
0 Switch Disabled	Switch Disabled	None
1 Set Gain	Up / Down	Change gain
2 Set Vtop (A/D)	Up / Down	Change A/D ref. top
3 Set Vbottom (A/D)	Up / Down	Change A/D ref bottom
4 Gain Selection #1	Up: 9dB, Down: 12dB	Lower gain selection
5 Gain Selection #2	Up: 18dB, Down: 22dB	Higher gain selection
6 Linear LUT	Up	Back to linear table
7 Knee Selection	Up / Down (Scroll)	Scroll 10 different LUTs
8 Async Reset Mode	Up: Normal, Dwn: Async	Async and normal shutter
9 Factory Default Recall	Up / Down: Recall	Factory setting
A Power up Setting	Up: Recall, Dwn: Save	Power up page setting
B User Page Storage#1	Up: Recall, Dwn: Save	User page storage setting
C User Page Storage#2	Up: Recall, Dwn: Save	User page storage setting
D Direct Shutter Control	Up / Down	Shutter speed increment by 1H
E Scan Format2	Up: Optional, Dwn: Binning	Two-row binning selection
F Scan Format1	Up: Normal, Dwn: Optional	Custom option scanning

Connector and Pin Configurations

Digital Output Connector



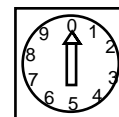
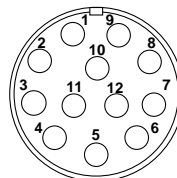
31-Pin Connector (MP211-031-113-4300)

Pin#	Description	I/O	Pin#	Description	I/O
1	CLK+	Out	17	CLK-	Out
2	LDV+	Out	18	LDV-	Out
3	FDV+	Out	19	FDV-	Out
4	GND		20	VINIT (TTL)	In
				[INTEG (TTL)	In]
5	EXT HD (TTL)	in	21	EXT VD (TTL)	In
	[EXT CLK+	In]		[EXT CLK-	In]
6	INTEG (TTL)	In	22	N/C	
	[HD+	In]		[HD-	In]
7	N/C		23	GND	
	[Vinit+/(VD+)	In]		[Vinit-/(VD-)	In]
8	D0+	Out	24	D0-	Out
9	D1+	Out	25	D1-	Out
10	D2+	Out	26	D2-	Out
11	D3+	Out	27	D3-	Out
12	D4+	Out	28	D4-	Out
13	D5+	Out	29	D5-	Out
14	D6+	Out	30	D6-	Out
15	D7+	Out	31	D7-	Out
16	GND				

Note: CLK: data clock, LDV: Line data valid, FDV: Frame data valid, INTEG: Integration control, EXT CLK: external pixel clock, []: Differential input option

12-Pin Connector

1 GND (power)	7 VD in
2 +12V	8 GND
3 GND (analog)	9 HD in
4 Video out	10 RXD(RS232)
5 GND (digital)	11 Int. cont
6 VINIT in	12 TXD(RS232)



Shutter Control Switch

	Manual	Async
0	no shutter (1/15)	no shutter (1/15)
1	1/60	1/16,000
2	1/125	1/8,000
3	1/250	1/4,000
4	1/500	1/2,000
5	1/1,000	1/1,000
6	1/2,000	1/500
7	1/4,000	1/250
8	1/8,000	1/125
9	1/16,000	Ext. pulse width control

* Option not currently available

TM-1320-15 Progressive Scan High-Resolution Shutter Camera

SPECIFICATIONS

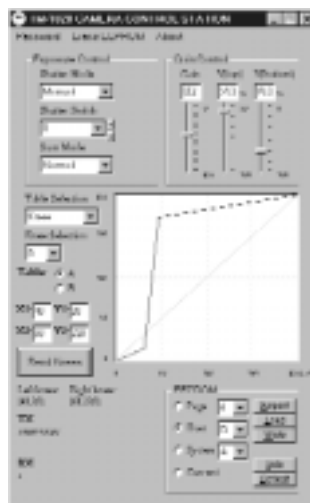
Imager	2/3" progressive scan interline transfer CCD	AGC	OFF
Active Area	8.7mm x 6.9mm	Gamma	Programmable LUT(1.0 std.)
Active Pixels	1300 (H) x 1030 (V)	Lens mount	C-mount (use >2/3" format lenses)
Cell size	6.7 μm x 6.7 μm	Power req.	12V DC±10%, 500 mA (current measured at 25°)
Scanning	1044 lines; 15 Hz; 522 lines, 30Hz (two-row binning)	Operating temp.	-10°C to 45°C*
Sync	Internal/external auto switch HD/VD, 4.0 Vp-p impedance 4.7KΩ VD=15 Hz ±5%, non-interlace, HD=15.47 kHz±5%	Vibration	7Grms (10 Hz to 2000 Hz) Random
Data clock output	25.00 MHz	Shock	70G
Resolution	Digital: 1300 (H) x 1030 (V), Analog: over 900 TV lines (H) x 800 TV lines (V)	Size (W x H x L)	44mm x 44mm x 64mm (1.73" x 1.73" x 2.51")
S/N ratio	50dB min. (AGC off)	Weight	133 grams, 4.7 oz (without tripod)
Min. illumination	1.0 lux, f=1.4 without IR cut filter (no shutter) Sensitivity: 10μV/e-	Functional options	Adjustable back-focus front end, 12 fps, 24 fps, pixel clock locking
Video output	Analog: 1.0 Vp-p composite video, 75Ω Digital output: 8-bit RS-644	I/O accessories	30DG-02 digital output cable CS-232E serial communication kit
		Power cable	12P-02S
		Power supply	PD-12UUP series (includes power connector)

* Image quality will degrade with increasing temperature.

Graphical User Interface

A user-friendly graphical user interface (GUI) is provided in the CS-232E kit. This interface allows users to control the following functions of the TM-1320-15 camera:

- Shutter control for manual async. and pulse width control
- Gain control
- A/D reference voltages control for Vtop and Vbottom
- Save settings
- Load settings
- Report settings
- LUT setting and graphic display
- Scanning mode selection and Option selections



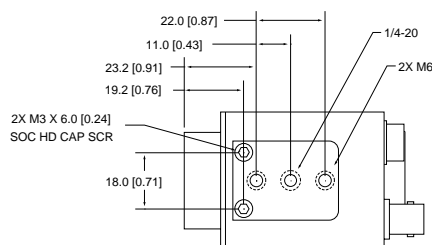
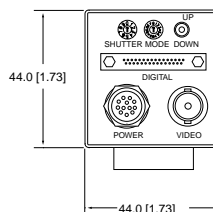
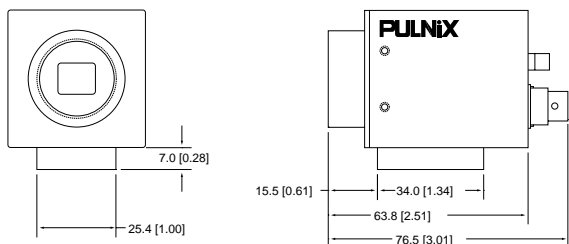
Camera parameters can be uploaded from the PC to the camera. Once these parameters are stored in EEPROMs, an instantaneous change from one setting to another can be done with a delay of few frames in between.

Serial Communication Kit

The serial communication kit CS-232E consists of serial cable RS-232B-12, a software disk, and a quick-start guide. The RS-232B-12 cable has a 12-pin connector on the camera end, and a 9-pin D-sub connector (RS-232) and a 12-pin connector (power and sync signals) on the other end.

TM-1320-15

71-0051 Rev. A



Japan
PULNiX America, Inc.
1-11-14 Hongo Bunkyo-ku,
Tokyo 113-0033
Tel: 81-3-5805-2455
Fax: 81-3-5805-8082
www.pulnix.co.jp

Australia
PULNiX America, Inc.
16 / 35 Garden Road
Clayton, VIC 3168
Tel: 61-(0)3-9546-0222
Fax: 61-(0)3-9562-4892
Email:
pulnix@ozemail.com.au

United Kingdom
PULNiX Europe, Ltd.
PULNiX House,
Aviary Court, Wade Road
Basingstoke, Hampshire
RG24 8PE
Tel: 44(0)1256-475555
Fax: 44(0)1256-466268
www.pulnix.eu.com

Germany
PULNiX Deutschland, GmbH.
Siemensstrasse 12
D-63755 Alzenau
Tel: 49(0)6023-9625-0
Fax: 49(0)6023-9625-11
www.pulnix.de

PULNiX

Innovation at work!

PULNiX America, Inc. Tel: 408-747-0300
1330 Orleans Drive Tel: 800-445-5444
Sunnyvale, CA 94089 Fax: 408-747-0660