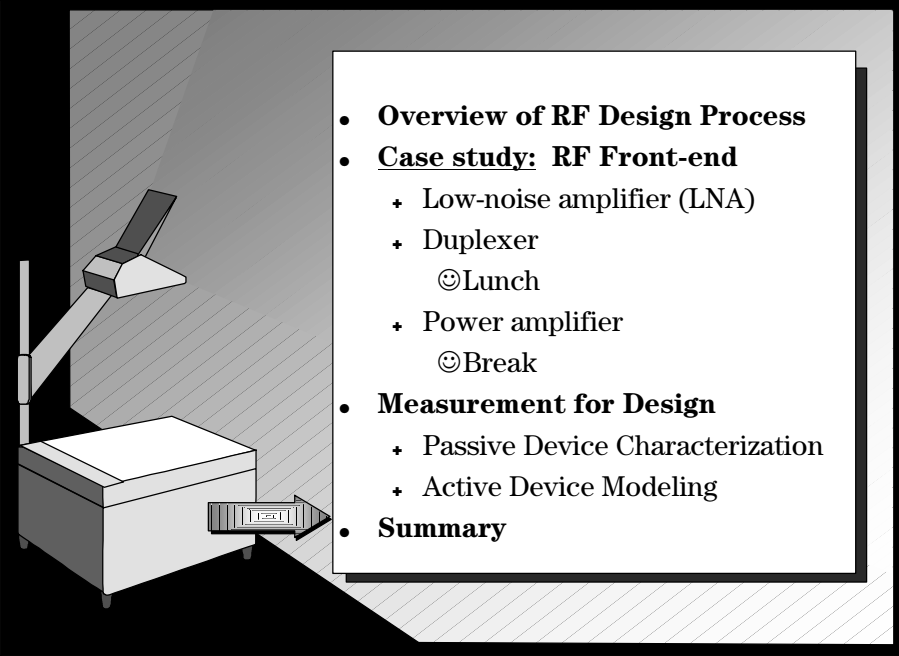



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Slide #163



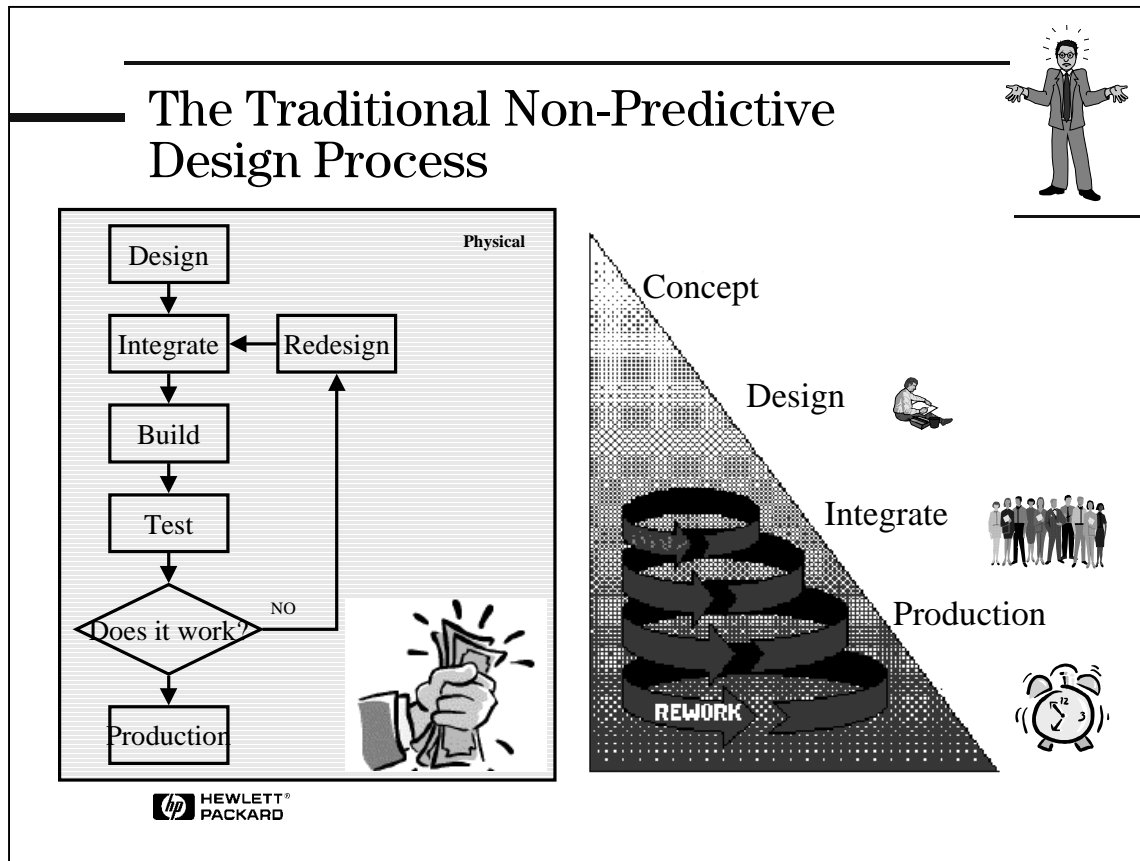
Agenda

- **Overview of RF Design Process**
- **Case study: RF Front-end**
 - + Low-noise amplifier (LNA)
 - + Duplexer
 - ☺Lunch
 - + Power amplifier
 - ☺Break
- **Measurement for Design**
 - + Passive Device Characterization
 - + Active Device Modeling
- **Summary**

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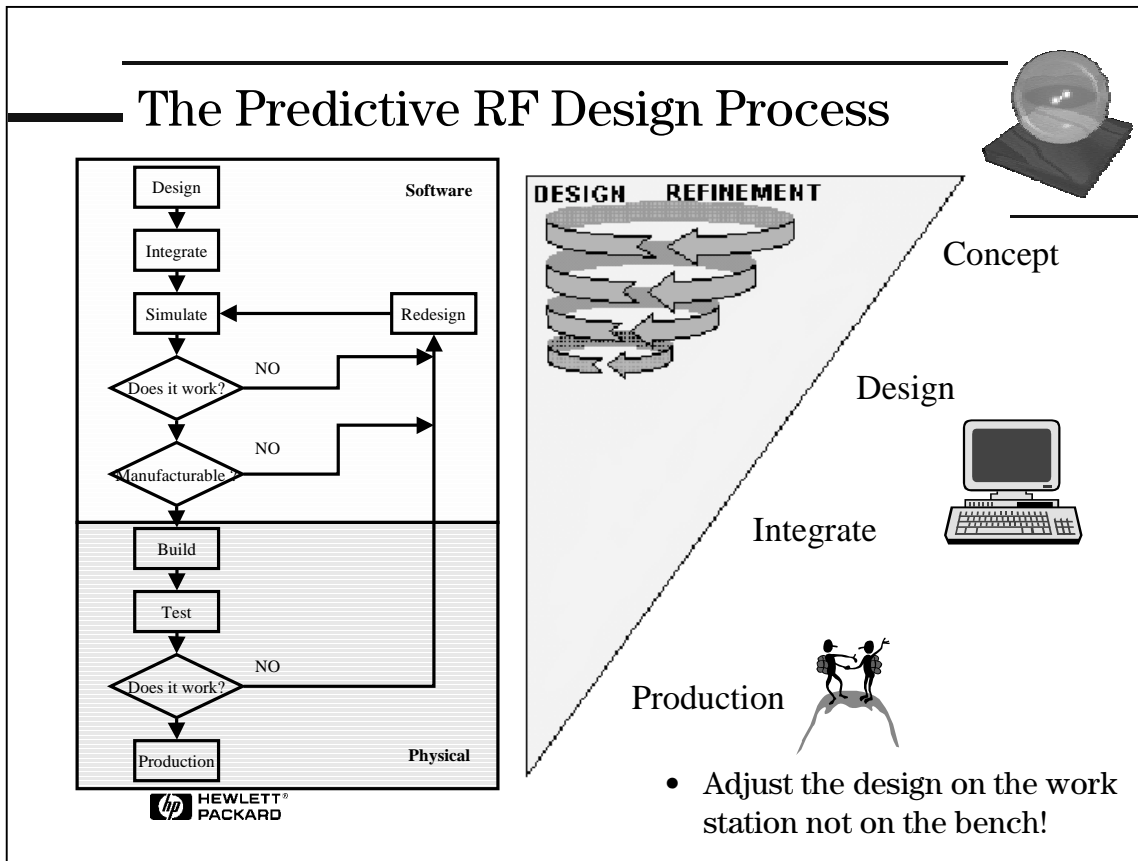
Slide #164



We started the day discussing the traditional design process. The problem is that this process is non-predictive. You hope that your design works, but in case it doesn't you run multiple physical prototype boards varying some of the components hoping that one works.

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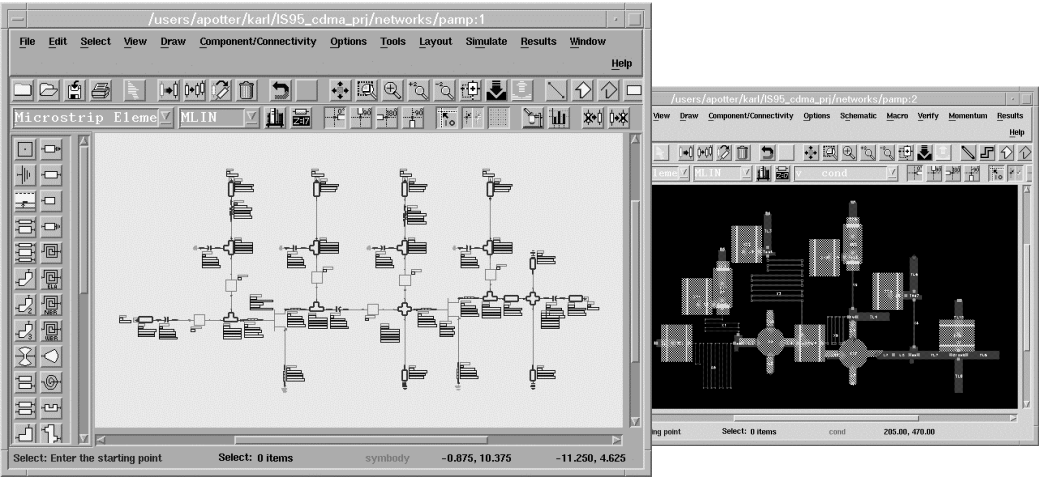
Next we introduced a new process that uses EDA software to refine the design early in the development process.


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Slide #166

HP Advanced Design System 

Total integration of schematic, simulation, and layout





Modern Predictive RF Design is made possible with the HP Advanced Design System. System level, circuit level, simulation and layout are all provided in an integrated environment and much more.

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Concept: System-Level Design (Simple PCS-band transceiver front end)

Power Amp

Freq. = 1880 MHz +/- 50 MHz min.
 Pout (1 dB) = +27 dBm min
 Psat = +30 dBm min
 Gain = 25 dB min

1.9 GHz

Duplexer

Freq. lower band = 1880 MHz +/- 30 MHz
 Freq. upper band = 1960 MHz +/- 30 MHz

LNA

Freq. = 1960 MHz +/- 50 MHz min.
 Gain = 25 dB min
 NF < 3 dB

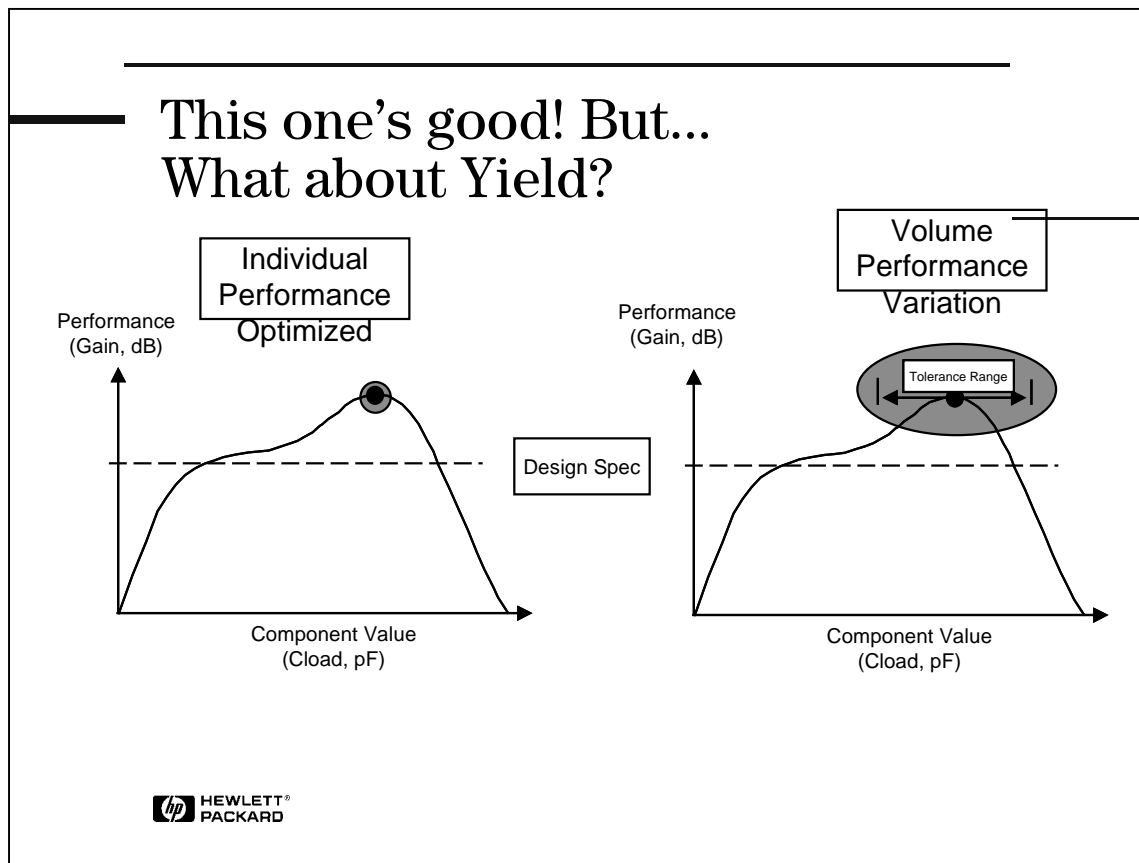
**The first part of the case study
will focus on the LNA**

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The first case study focused on an LNA design.

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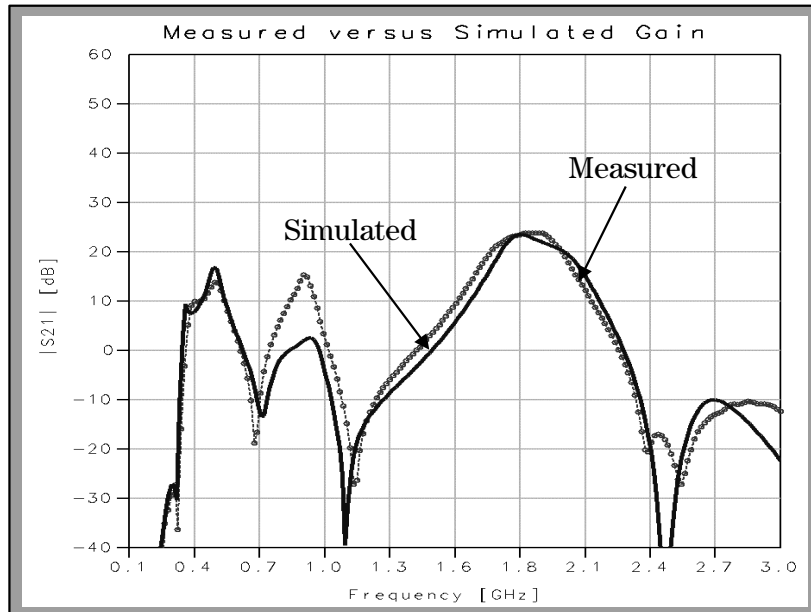


We then looked at the effects of component tolerances.

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Slide #169

Working Power Amplifier !!!



The second case study covered the design of a power amplifier.

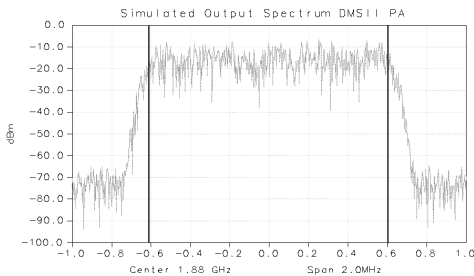
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Slide #170

CDMA Tests

Channel Power and ACPR

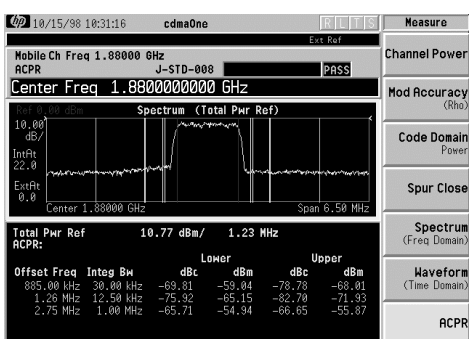
Simulation



TransACPR		Channel Power [dBm]
TransACPR(1)	-72.5	11.2
TransACPR(2)	-74.3	


Pwr = 11.2 dBm
ACPR < -70 dBc

Measured



Total Pwr Ref		Lower		Upper	
Offset Freq	Integ Bw	dBc	dBm	dBc	dBm
895.00 kHz	30.00 kHz	-69.81	-59.04	-78.78	-69.01
1.26 MHz	12.50 kHz	-75.92	-65.15	-82.70	-71.93
2.75 MHz	1.00 MHz	-65.71	-54.94	-66.65	-55.87

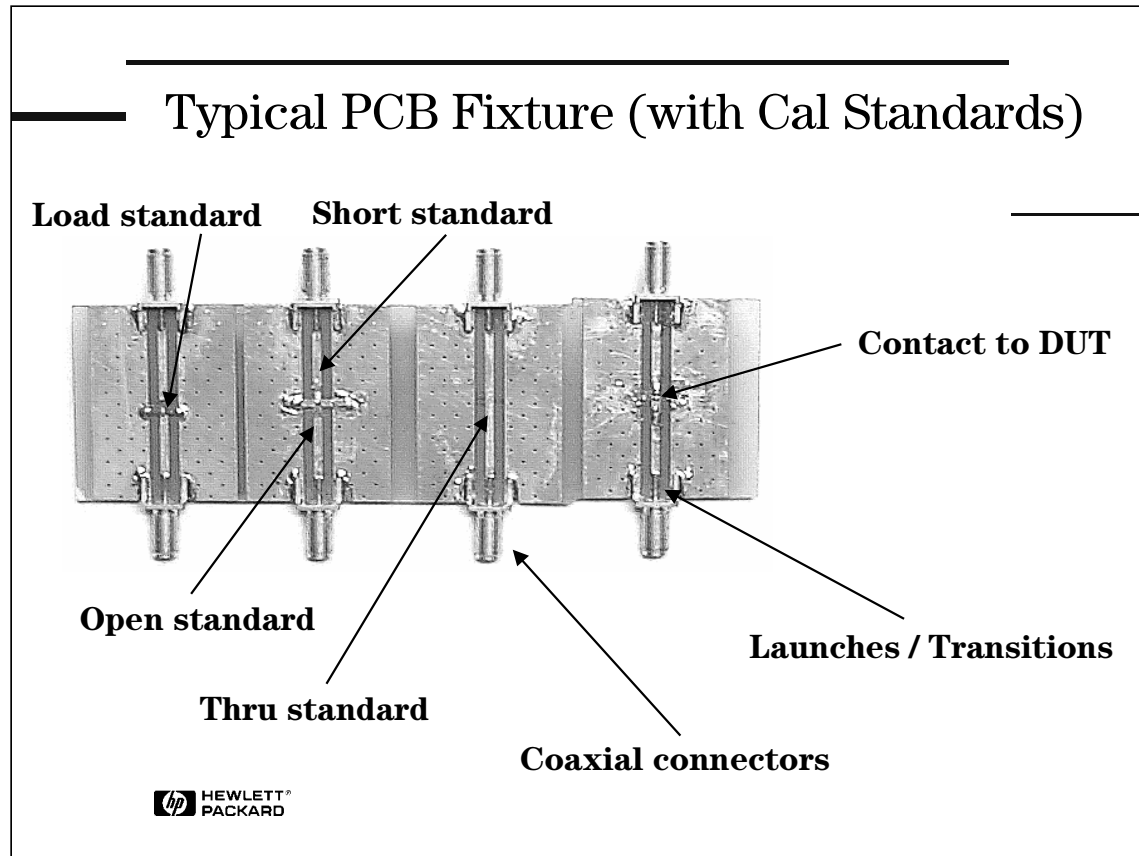
Pwr = 10.8 dBm
ACPR < -70 dBc



We simulated and measured the amplifier performance using digital modulation.

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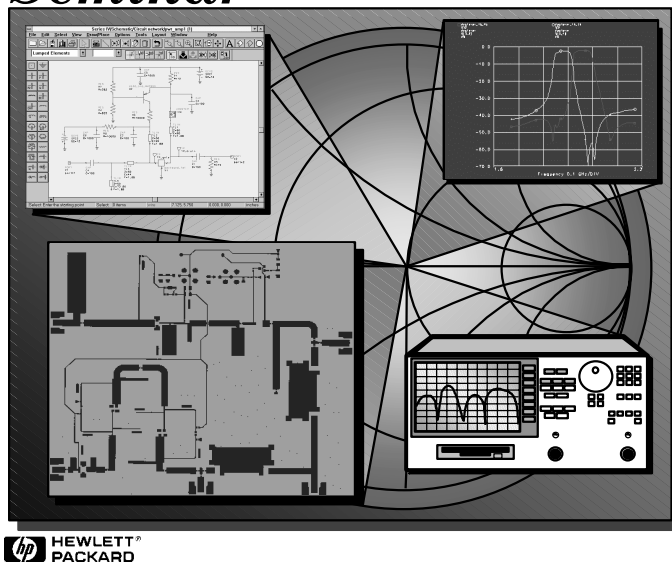


The final section covered fixturing and calibration issues which arise when characterizing devices.

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***Thank You for Attending -
HP RF Design and Measurement
Seminar***



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