N-Channel Enhancement MOS Silicon FET



2SK536

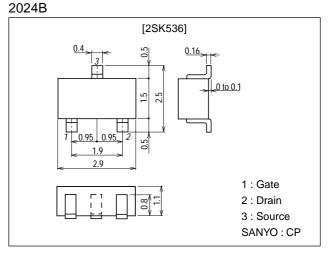
Analog Switch Applications

Features

- · Large $|y_{fs}|$.
- · Enhancement type.
- · Low ON-state resistance.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

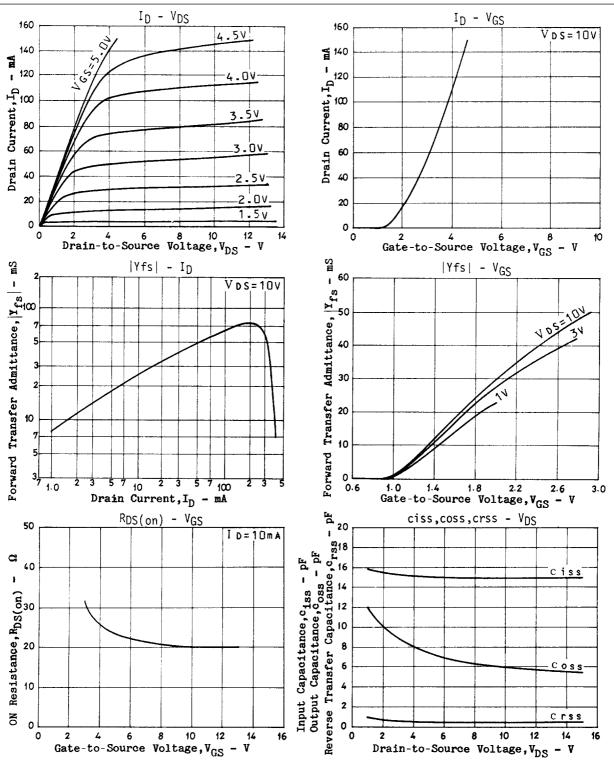
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DS}		50	V
Gate-to-Source Voltage	V _{GS}		±12	V
Drain Current	۱ _D		100	mA
Drain Current(Pulse)	I _{DP}		300	mA
Allowable Power Dissipation	PD		200	mW
Channel Temperature	Tch		125	°C
Storage Temperature	Tstg		-55 to +125	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
Falance			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DS	I _D =10µA, V _{GS} =0	50			V
Gate-to-Source Leakage Current	IGSS	V _{GS} =10V, V _{DS} =0		0.01	10	nA
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V			1	μΑ
Cutoff Voltage	IGS(off)	V _{DS} =10V, I _D =100µA	0.3	0.9	1.5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =50mA, f=1kHz	25	40		mS
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0, f=1MHz		15		pF
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0, f=1MHz		6		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, V _{GS} =0, f=1MHz		0.5		pF
Drain-to-Source ON Resistance	R _{DS(on)}	V _{GS} =10V, I _D =10mA		20		Ω

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