

Switching Applications (with Bias Resistances)

Applications

· Switching circuits, inverter circuits, interface circuits, driver circuits.

Features

- · On-chip bias resistances (R1=10k Ω , R2=47k Ω).
- · Compact package (SPA).

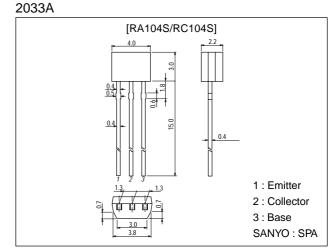
(): RA104S

Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(–)50	V
Collector-to-Emitter Voltage	VCEO		(–)50	V
Emitter-to-Base Voltage	V _{EBO}		(-)6	V
Input Voltage	VIN		(–)30	V
Collector Current	lС		(–)100	mA
Collector Current (Pulse)	I _{CP}		(–)200	mA
Collector Dissipation	PC		300	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0			(-)0.1	μΑ
	ICEO	V _{CE} =(-)40V, I _B =0			(-)0.5	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =(-)5V, I _C =0	(-)67	(–)88	(–)125	μΑ
DC Current Gain	hFE	V _{CE} =(-)5V, I _C =(-)5mA	70			
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)5mA		250		MHz
				(200)		MHz
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		3.5		pF
				(5.3)		pF

Marking: RA104S: A104, RC104S: C104

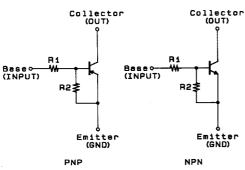
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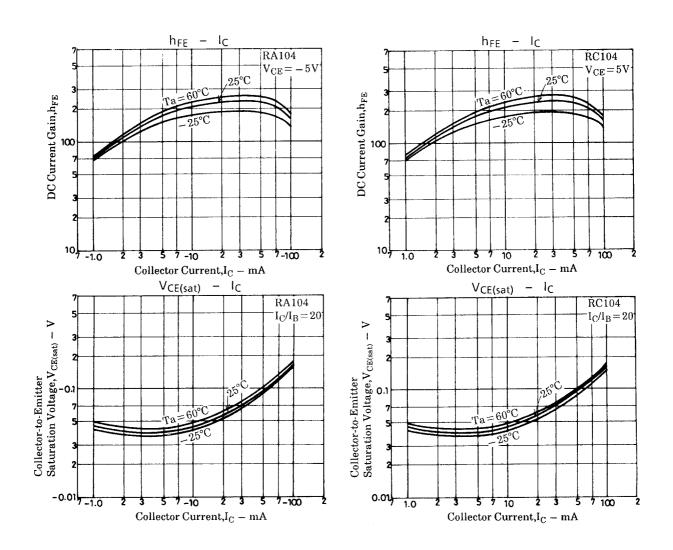
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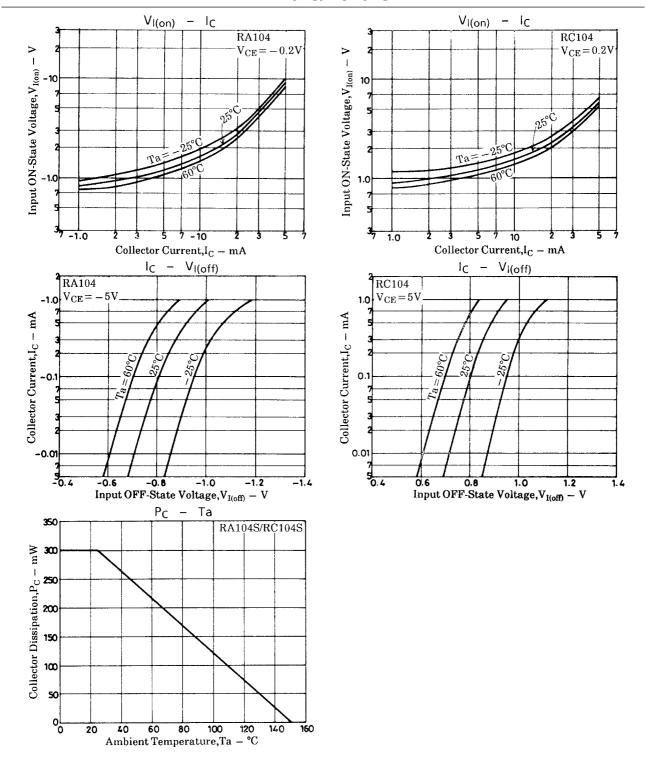
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)10mA, I _B =(-)0.5mA		(-)0.1	(-)0.3	V
Collector-to-Base Breakdown Voltage	V _(BR) CBO	$I_{C}=(-)10\mu A, I_{E}=0$	(-)50			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =(−)100μA, R _{BE} =∞	(-)50			V
Input OFF-State Voltage	V _{IN(off)}	V _{CE} =(-)5V, I _C =(-)100μA	(-)0.6	(-)0.8	(-)1.0	V
Input ON-State Voltage	V _{IN(on)}	V _{CE} =(-)0.2V, I _C =(-)5mA	(-)0.7	(-)1.0	(-)2.0	V
Input Resistance	R1		7	10	13	kΩ
Resistance Ratio	R1/R2			0.213		

Electrical Connection







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