

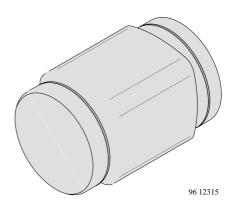
# Silicon Epitaxial Planar Diode

#### Features

- Electrical data identical with the device 1N4154
- Micro Melf package

### Applications

Extreme fast switches



#### **Absolute Maximum Ratings**

 $T_j = 25^{\circ}C$ 

Parameter	Test Conditions	Туре	Symbol	Value	Unit
Repetitive peak reverse voltage			V <sub>RRM</sub>	35	V
Reverse voltage			VR	25	V
Peak forward surge current	t <sub>p</sub> =1µs		I <sub>FSM</sub>	2	Α
Repetitive peak forward current			I <sub>FRM</sub>	450	mA
Forward current			IF	200	mA
Average forward current	V <sub>R</sub> =0		I <sub>FAV</sub>	150	mA
Power dissipation			P <sub>V</sub>	500	mW
Junction temperature			Tj	175	°C
Storage temperature range			T <sub>stg</sub>	-65+175	°C

# **Maximum Thermal Resistance**

 $T_j = 25^{\circ}C$ 

Parameter	Test Conditions	Symbol	Value	Unit
	mounted on epoxy–glass hard issue, Fig. 1, 35µm copper clad, 0.9 mm <sup>2</sup> copper area per electrode	R <sub>thJA</sub>	500	K/W

## Characteristics

#### $T_j = 25^{\circ}C$

Parameter	Test Conditions	Туре	Symbol	Min	Тур	Max	Unit
Forward voltage	I <sub>F</sub> =30mA		V <sub>F</sub>			1	V
Reverse current	V <sub>R</sub> =25V		IR			100	nA
	V <sub>R</sub> =25V, T <sub>j</sub> =150°C		I <sub>R</sub>			100	μΑ
Breakdown voltage	$I_R=5\mu A, t_p/T=0.01, t_p=0.3ms$		V <sub>(BR)</sub>	35			V
Diode capacitance	$V_R=0$ , f=1MHz, $V_{HF}=50mV$		CD			4	pF
Reverse recovery time	I <sub>F</sub> =I <sub>R</sub> =10mA, i <sub>R</sub> =1mA		t <sub>rr</sub>			4	ns
	$I_{F}=10\text{mA}, V_{R}=6\text{V}, \\ i_{R}=0.1\text{xI}_{R}, R_{L}=100\Omega$		t <sub>rr</sub>			2	ns

**Typical Characteristics** ( $T_j = 25^{\circ}C$  unless otherwise specified)

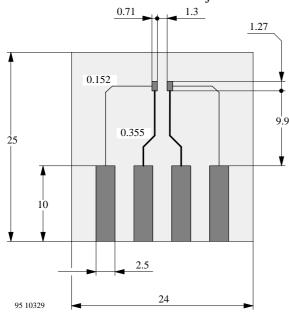


Figure 1. Board for RthJA definition (in mm)



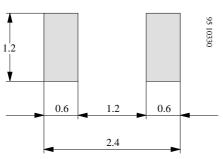


Figure 2. Recommended foot pads (in mm)

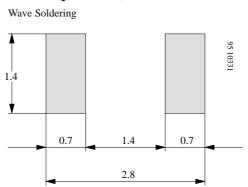


Figure 3. Recommended foot pads (in mm)

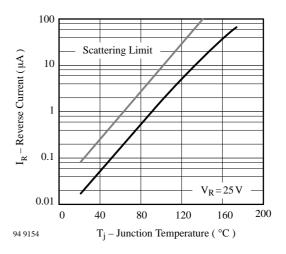


Figure 4. Reverse Current vs. Junction Temperature



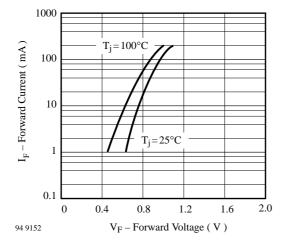


Figure 5. Forward Current vs. Forward Voltage

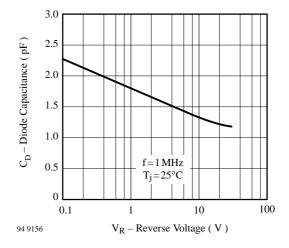
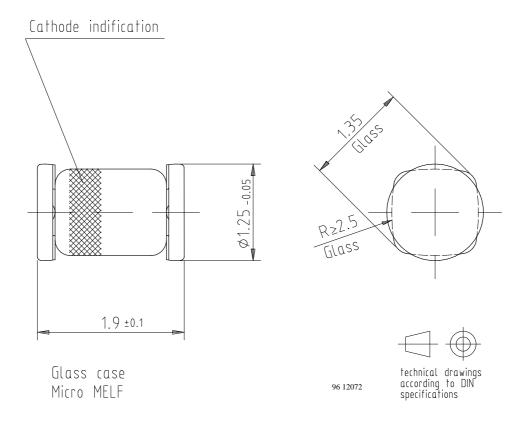


Figure 6. Diode Capacitance vs. Reverse Voltage

#### **Dimensions in mm**



### **Ozone Depleting Substances Policy Statement**

#### It is the policy of TEMIC TELEFUNKEN microelectronic GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**TEMIC TELEFUNKEN microelectronic GmbH** semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**TEMIC** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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