

# LA6458M, 6458S

# High-Performance Dual Operational Amplifiers

### Overview

The LA6458 consists of two independent, internally phase compensated operational amplifiers. Application areas include active filters, audio preamplifiers, and various electronic circuits.

## **Features**

 LA6458M: 8-pin MFP package, LA6458S: 9-pin SIP package
 Phase compensation circuit built in.

High gain, low noise.
Slew rate: 1.1V/µs typ.

# **Specifications**

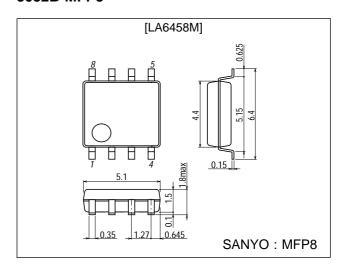
### Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit	
Maximum supply voltage	V <sub>CC</sub> /V <sub>EE</sub>		±18	V	
Differential input voltage	V <sub>ID</sub>		±30	V	
Common-mode input voltage	V <sub>IN</sub>		±15	V	
Allowable power dissipation	Pd max	LA6458M	300	mW	
		LA6458S	500	mW	
Operating temperature	Topr		-20 to +75	°C	
Storage temperature	Tstg		-40 to +125	°C	

## **Package Dimensions**

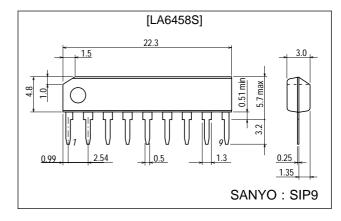
unit: mm

#### 3032B-MFP8



unit: mm

#### 3017C-SIP9



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## SANYO Electric Co., Ltd. Semiconductor Company

## LA6458M, 6458S

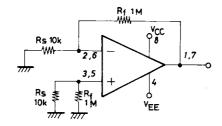
# Operating Characteristics at Ta = 25°C, $V_{CC}$ = 15 V, $V_{EE}$ = -15 V

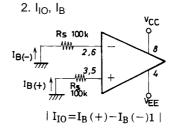
Parameter	Symbol	Conditions	min	typ	max	Unit
Input offset voltage	V <sub>IO</sub>	$R_S = 10 \text{ k}\Omega$		0.5	6	mV
Input offset current	I <sub>IO</sub>			5	200	nA
Input bias current	I <sub>B</sub>			60	500	nA
Common-mode input voltage	V <sub>ICM</sub>		±12	±14		V
Common-mode rejection ratio	CMR		70	90		dB
Voltage gain	VGO	$R_L \ge 2 k\Omega, V_O = \pm 10 V$	86	100		dB
Maximum output voltage	V <sub>O</sub> (1)	$R_L \ge 10 \text{ k}\Omega$	±12	±14		V
	V <sub>O</sub> (2)	$R_L \ge 2 k\Omega$	±10	±13		V
Slew rate	SR	LA6458M: VG = 0, $R_L \ge 2 \text{ k}\Omega$		1.0		V/µs
		LA6458S: VG = 0, R <sub>L</sub> $\geq$ 2 k $\Omega$		1.1		V/µs
Equivalent input noise voltage	V <sub>NI</sub>	LA6458M: $R_S = 1 \text{ k}\Omega$ , B.P.F. = 10 Hz to 30 kHz		1.6		μV
		LA6458S: $R_S = 1 \text{ k}\Omega$ , B.P.F. = 10 Hz to 30 kHz		1.7		μV
Current drain	Icc			3.5	6	mA
Supply voltage rejection	SVR	$R_S \le 10 \text{ k}\Omega$		30	150	μV/V

## **Test Circuits**

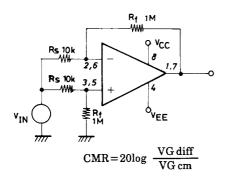
(Pin assignment : SIP/MFP package)

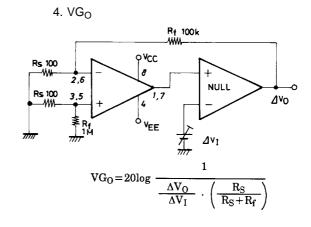
1.  $V_{IO}$ , SVR

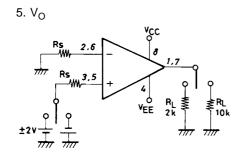


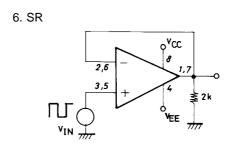


3.  $V_{ICM}$ , CMR



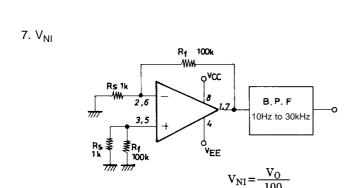


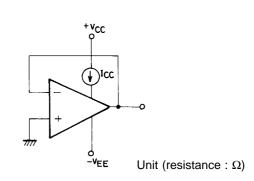


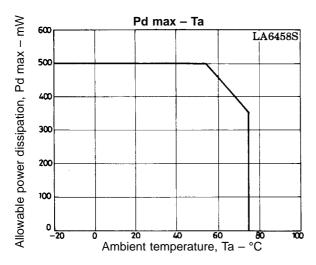


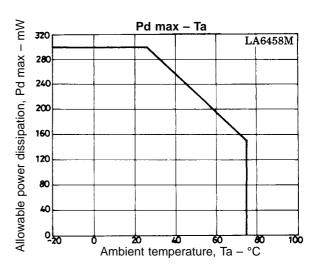
Unit (resistance:  $\Omega$ )

8. I<sub>CO</sub>



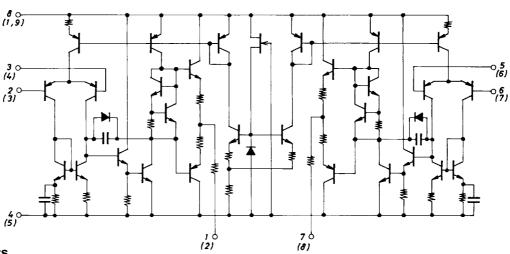




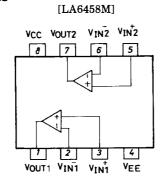


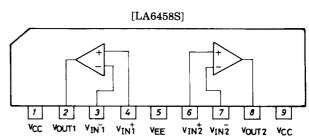
## **Equivalent Circuit**

Pin No.: LA6458M, ( ) of pin No.: LA6458S



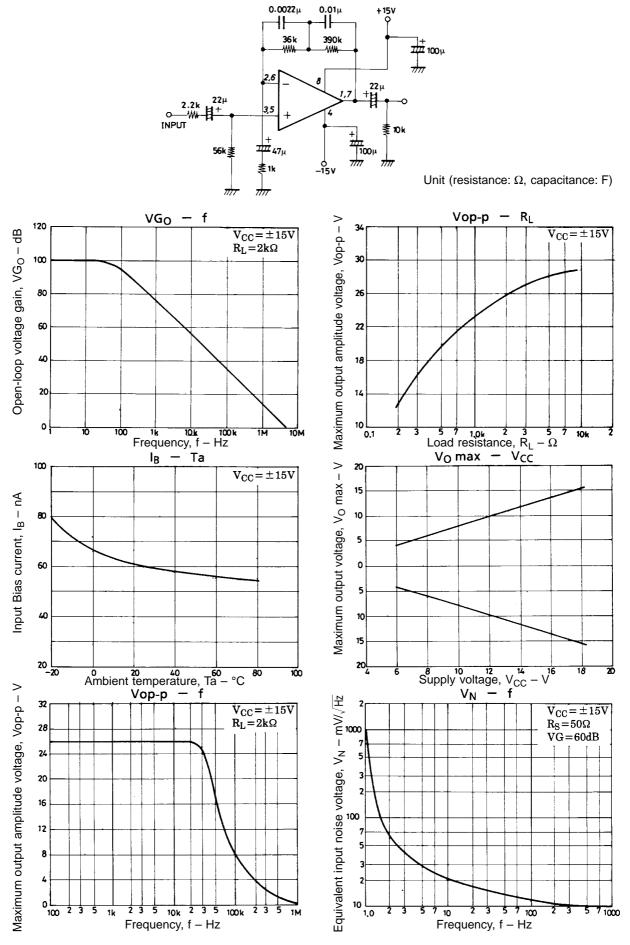
## **Pin Assignments**





Top view

## Sample Application Circuit RIAA preamplifier (VG = 32.5 dB)



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