

L3100B L3100B1

Application Specific Discretes A.S.D.TM

OVERVOLTAGE AND OVERCURRENT PROTECTION FOR TELECOM LINE

FEATURES

- UNIDIRECTIONAL FUNCTION
- PROGRAMMABLE BREAKDOWN VOLTAGE UP TO 265 V
- PROGRAMMABLE CURRENT LIMITATION FROM 50 mA TO 550 mA
- HIGH SURGE CURRENT CAPABILITY IPP = 100A 10/1000 μs

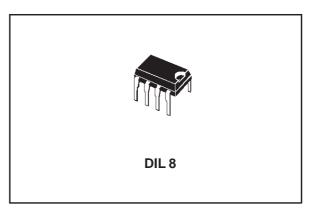
DESCRIPTION

Dedicated to sensitive telecom equipment protection, this device can provide both voltage protection and current limitation with a very tight tolerance.

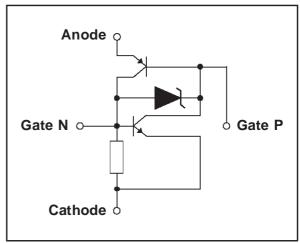
Its high surge current capability makes the L3100B a reliable protection device for very exposed equipment, or when series resistors are very low. The breakdown voltage can be easily programmed by using an external zener diode.

A multiple protection mode can also be performed when using several zener diodes, providing each line interface with an optimized protection level.

The current limiting function is achieved with the use of a resistor between the gate N and the cathode. The value of the resistor will determine the level of the desired current.



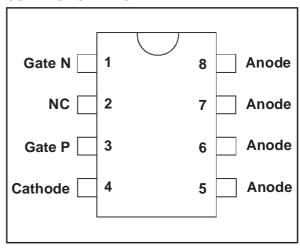
SCHEMATIC DIAGRAM



COMPLIES WITH THE FOLLOWING STANDARDS:

CCITT K17 - K20	10/700 μs	1.5	kV
	5/310 μs	38	Α
VDE 0433	10/700 μs	2	kV
	5/200 μs	50	Α
CNET	0.5/700 μs	1.5	kV
	0.2/310 us	38	Α

CONNECTION DIAGRAM



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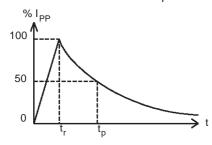
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ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C)

Symbol	Parameter	Value	Unit	
Ірр	Peak pulse current (see note 1)	10/1000 μs 8/20 μs	100 250	А
Ітѕм	Non repetitive surge peak on-state current	tp = 10 ms	50	А
T _{stg} T _j	Storage temperature range Maximum operating junction temperature		- 40 to + 150 + 150	°C °C
TL	Maximum lead temperature for soldering	during 10s	230	°C

Note 1 : Pulse waveform 10/1000 μs



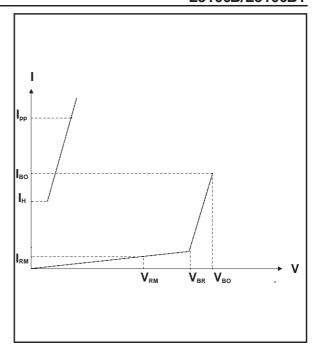
THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{th (j-a)}	Junction-to-ambient	80	°C/W

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C)

Symbol	Parameter	
VRM	Stand-off voltage	
I _{RM}	Reverse leakage current	
V_{BR}	Breakdown voltage	
V _{BO}	Breakovervoltage	
lн	Holding current	
I _{BO}	Breakover current	
I _{PP}	Peak pulse current	
V _{GN}	Gate voltage	
I _{GN} , I _{GP}	Triggering gate current	
V _{RGN}	Reverse gate voltage	
С	Capacitance	



OPERATION WITHOUT GATE

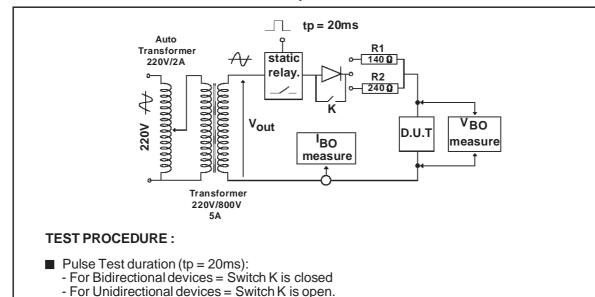
	I _{RM} @	V _{RM}	V _{BR} @ I _R		V _{BO}	@	во	lμ	С
Type	max.		min.		max.	min.	max.	min.	max.
,			note 1		note 1	note 2			
	μ Α	V	٧	mA	٧	mA	mA	mA	pF
L3100B	6 40	60 250	265	1	350	200	500	280	100
L3100B1	6 40	60 250	255	1	350	200	500	210	100

OPERATION WITH GATES

	Vgn @ lgn	V _{GN} @ l _{GN} = 200 mA		AC = 100V	VRGN @ IG = 1mA	IGP @ VAC = 100V	
Туре	min.	max.	min. max.		min.	max.	
	V	V	mA	mA	V	mA	
L3100B/B1	0.6	1.8	30	200	0.7	150	

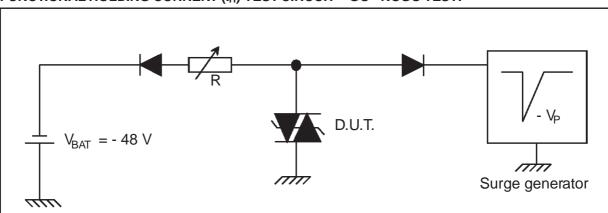
Note 1: See the reference test circuits for I_H , I_{BO} and V_{BO} parameters. Note 2: $V_R = 5 \text{ V}$, F = 1 MHz.

REFERENCE TEST CIRCUIT FOR IBO and VBO parameters:



- Vout Selection
 - Device with VBo < 200 Volt
 - Vout = 250 VRMs, R_1 = 140 Ω .
 - Device with $V_{BO} \ge 200 \text{ Volt}$
 - $V_{\text{OUT}} = 480 V_{\text{RMS}}$, $R_2 = 240 \Omega$.

FUNCTIONAL HOLDING CURRENT (IH) TEST CIRCUIT = GO - NOGO TEST.



This is a GO-NOGO Test which allows to confirm the holding current (I_H) level in a functional test circuit.

This test can be performed if the reference test circuit can't be implemented.

TEST PROCEDURE:

- 1) Adjust the current level at the I_H value by short circuiting the AK of the D.U.T.
 - 2) Fire the D.U.T with a surge Current : Ipp = 10A, $10/1000 \,\mu s$.
 - 3) The D.U.T will come back to the OFF-State within a duration of 50 ms max.

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Figure 1 : Surge peak current versus overload duration.

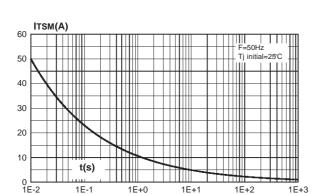


Figure 2: Relative variation of holding current versus junction temperature.

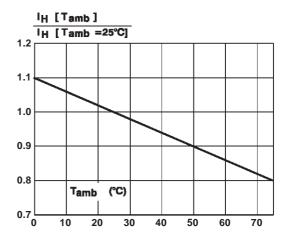


Figure 3: Relative variation of breakdown voltage versus ambient temperature.

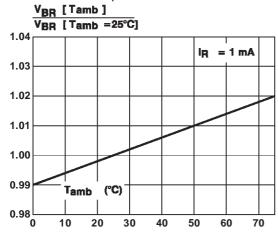
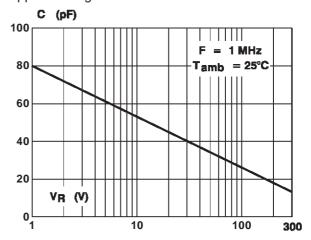


Figure 4: Junction capacitance versus reverse applied voltage.



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APPLICATION CIRCUIT

Overvoltage Protection and Current limitation

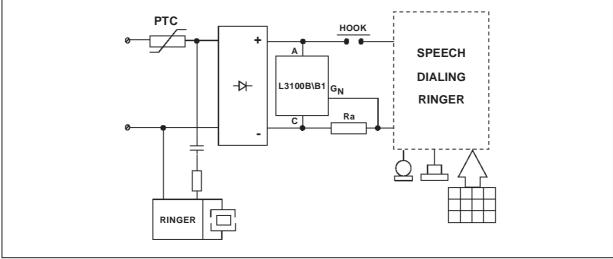
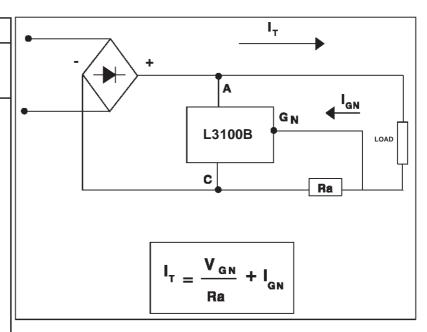


Table below gives the tolerance of the limited current I_T for each standardized resistor value. The formula (1) has been used with V_{GN} values specified at the typical gate current level I_{GN} .

CURR	ENT TOLER	RANCE
R Ω (± 5%)	IT mA min	I _T mA max
3.00 3.30 3.60 3.90 4.30 4.70 5.10 5.60 6.20 6.80 7.50 8.20 9.10 10.10 11.00 12.00 13.00 15.00 16.00 18.00 20.00 22.00 24.00 27.00 30.00	268 246 228 213 196 181 170 158 145 135 152 117 108 101 95 90 85 78 75 70 66 62 60 56	533 503 478 456 433 413 396 379 361 347 333 322 310 299 291 283 277 266 263 256 250 245 242 237 233

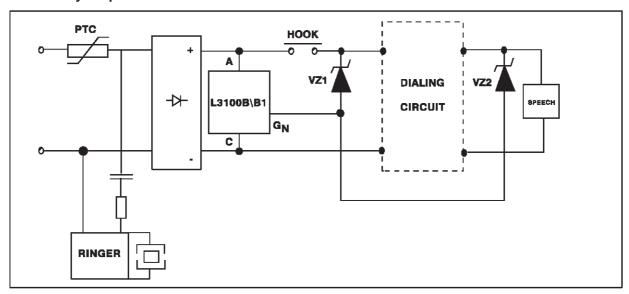


	V _{GN} @	I _{GN}	
Min.	Max.		Тур.
V	V		mA
0.75	0.95		100

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Ground key telephone set Protection

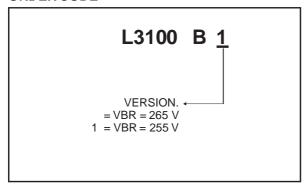


PROTECTION MODES:

ON HOOK = Ringer circuit protection is ensured with breakdown voltage at 265 V.

OFF HOOK = In dialing mode and in speech mode, the breakdown voltage of L3100B can be adapted to different levels with zener diodes.

ORDER CODE



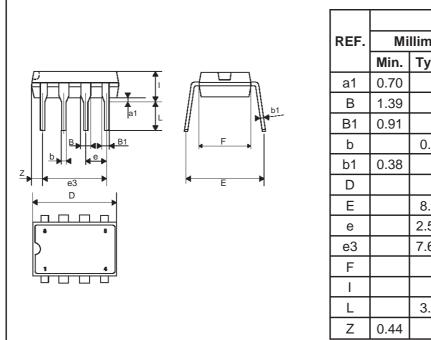
MARKING: Logo, Date Code,part Number.

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PACKAGE MECHANICAL DATA.

DIL 8 (Plastic)



	DIMENSIONS					
REF.	Mi	Millimetres Inche		Inches	;	
	Min.	Тур.	Max.	Min.	Тур.	Max.
a1	0.70			0.027		
В	1.39		1.65	0.055		0.065
B1	0.91		1.04	0.036		0.041
b		0.5			0.020	
b1	0.38		0.50	0.015		0.020
D			9.80			0.385
Е		8.8			0.346	
е		2.54			0.100	
еЗ		7.62			0.300	
F			7.1			0.280
I			4.8			0.189
L		3.3			0.130	
Z	0.44		1.60	0.017		0.063

Weight: 0.59 g

Packaging: Product supplied in antistatic tubes.

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