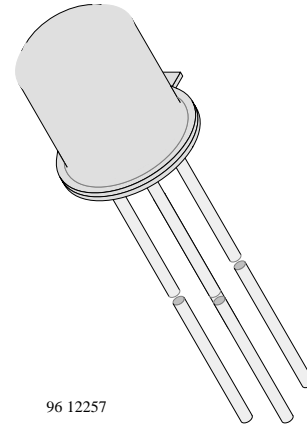


Optocoupler with Phototransistor Output

Description

The K120P consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a hermetically sealed 4-lead TO72 metal can package for high reliability requirements.



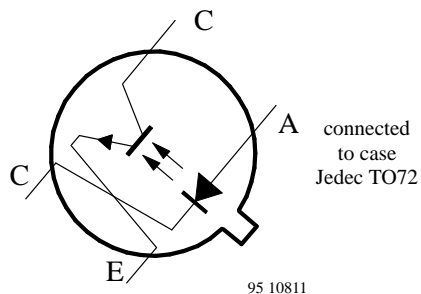
Applications

Galvanically separated circuits

Features

- Hermetically-sealed case
- High isolation resistance
- DC isolation test voltage $V_{IO} = 1000\text{ V}$
- Current Transfer Ratio, (CTR) of typical 50%
- Coupling capacitance of typical 1.5 pF
- Low temperature coefficient of CTR
- Large ambient temperature range

Pin Connection



Absolute Maximum Ratings

Input (Emitter)

Parameters	Test Conditions	Symbol	Value	Unit
Reverse voltage		V_R	7	V
Forward current		I_F	60	mA
Forward surge current	$t \leq 10 \mu\text{s}$	I_{FSM}	1.5	A
Power dissipation	$T_{amb} \leq 25^\circ\text{C}$	P_V	100	mW
Junction temperature		T_j	125	$^\circ\text{C}$

Output (Detector)

Parameters	Test Conditions	Symbol	Value	Unit
Collector emitter voltage		V_{CEO}	35	V
Emitter collector voltage		V_{ECO}	7	V
Collector current		I_C	100	mA
Power dissipation	$T_{amb} \leq 25^\circ\text{C}$	P_V	200	mW
Junction temperature		T_j	125	$^\circ\text{C}$

Coupler

Parameters	Test Conditions	Symbol	Value	Unit
DC isolation test voltage		$V_{IO}^{1)}$	1000	V
Power dissipation	$T_{amb} \leq 25^\circ\text{C}$	P_{tot}	300	mW
Ambient temperature range		T_{amb}	-55 to +100	$^\circ\text{C}$
Storage temperature range		T_{stg}	-55 to +125	$^\circ\text{C}$
Soldering temperature	2 mm from case, $t \leq 10 \text{ s}$	T_{sd}	260	$^\circ\text{C}$

Electrical Characteristics

$T_{amb} = 25^{\circ}\text{C}$

Input (Emitter)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Forward voltage	$I_F = 50 \text{ mA}$	V_F		1.25	1.5	V
Reserve voltage	$I_R = 100 \mu\text{A}$	$V_{(BR)}$	7			V
Junction capacitance	$V_R = 0, f = 1 \text{ MHz}$	C_j		50		pF
Reserve current	$V_R = 3 \text{ V}$	I_R		0.35	10	μA

Output (Detector)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Collector emitter breakdown voltage	$I_C = 1 \text{ mA}$	$V_{(BR)CEO}$	35			V
Emitter -collector breakdown voltage	$I_E = 100 \mu\text{A}$	$V_{(BR)ECO}$	7			V
Collector dark current	$V_{CE} = 20 \text{ V},$ $I_F = 0, E = 0$	I_{CEO}			50	nA

Coupler

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
DC isolation test voltage	$t = 1 \text{ min}$	$V_{IO}^{1)}$	1000			V
Isolation resistance	$V_{IO} = 1 \text{ kV},$ 40% relative humidity	$R_{IO}^{1)}$	10^{10}	10^{12}		Ω
I_C/I_F	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	CTR	0.25	0.50		
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 2.5 \text{ mA}$ $I_F = 10 \text{ mA}, I_C = 0.5 \text{ mA}$	V_{CEsat} V_{CEsat}		0.1	0.3	V V
Cut-off frequency	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA},$ $R_L = 100 \Omega$	f_g		110		kHz
Coupling capacitance	$f = 1 \text{ MHz}$	C_k		1.5		pF

¹⁾ Related to standard climate 23/50 DIN 50 014

Switching Characteristics

$V_S = 5\text{ V}$

Type	$R_L = 100\ \Omega$ (see figure 1)							$R_L = 1\text{ k}\Omega$ (see figure 2)		
	$t_d[\mu\text{s}]$	$t_r[\mu\text{s}]$	$t_{on}[\mu\text{s}]$	$t_s[\mu\text{s}]$	$t_f[\mu\text{s}]$	$t_{off}[\mu\text{s}]$	$I_C[\text{mA}]$	$t_{on}[\mu\text{s}]$	$t_{off}[\mu\text{s}]$	$I_F[\text{mA}]$
K120P			5.0			3	5	11	13.8	20

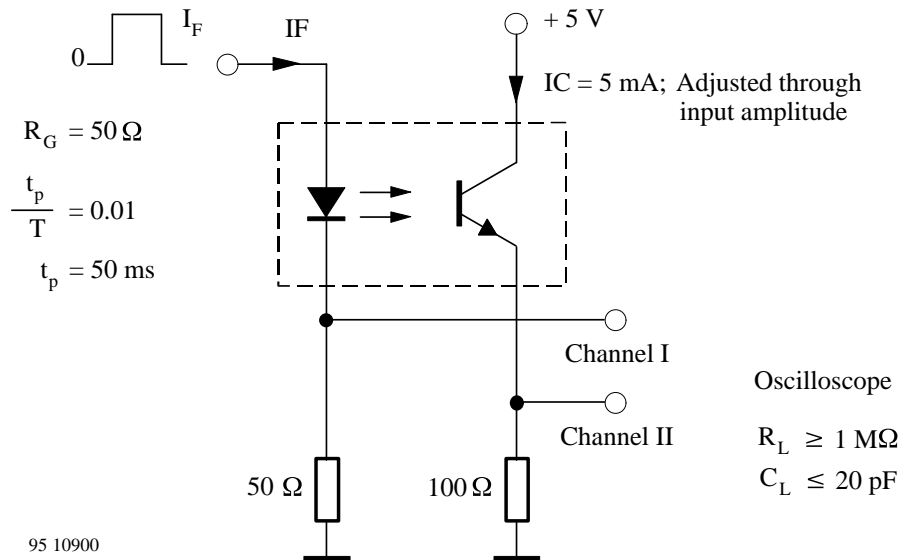


Figure 1. Test circuit, non-saturated operation

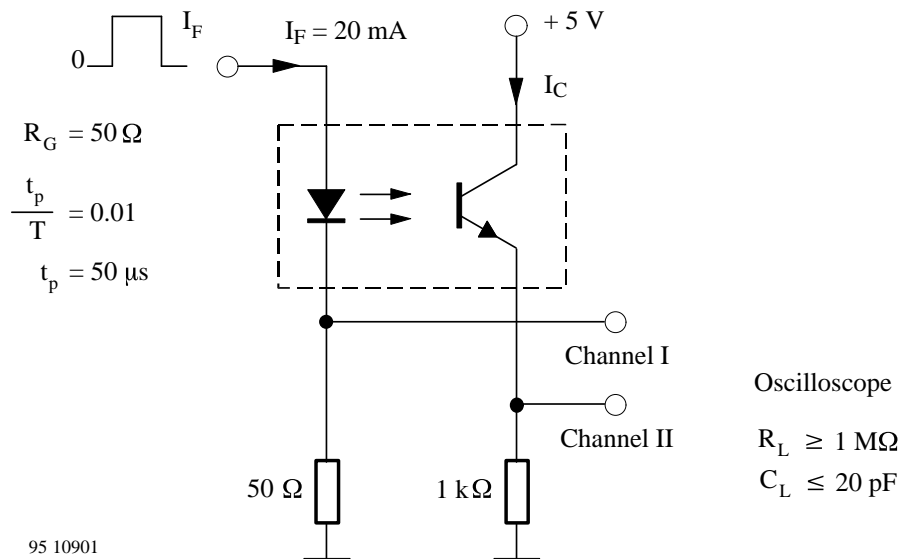


Figure 2. Test circuit, saturated operation

Typical Characteristics ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

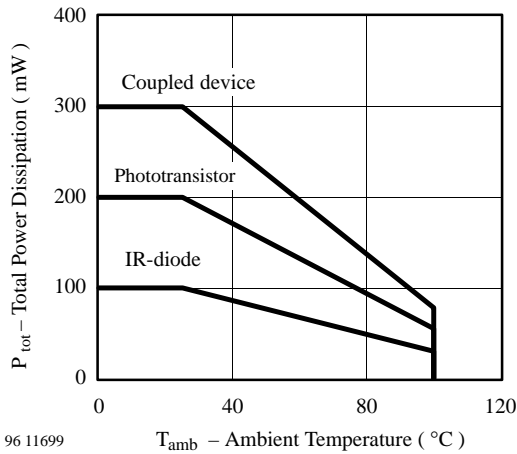


Figure 3. Total Power Dissipation vs. Ambient Temperature

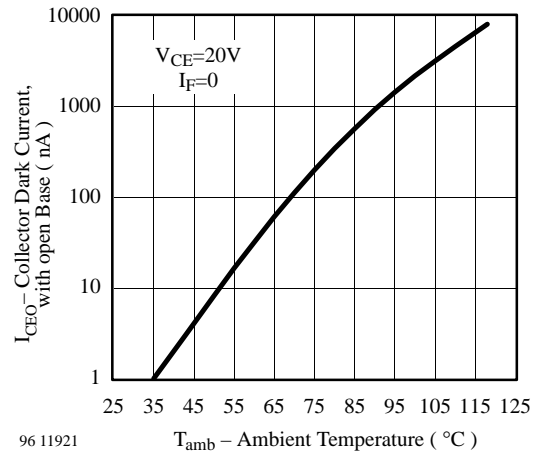


Figure 6. Collector Dark Current vs. Ambient Temperature

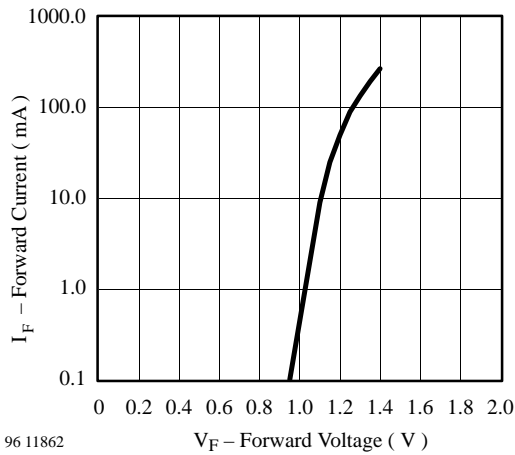


Figure 4. Forward Current vs. Forward Voltage

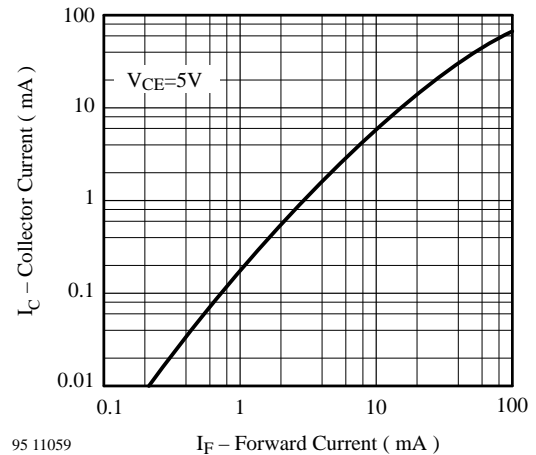


Figure 7. Collector Current vs. Forward Current

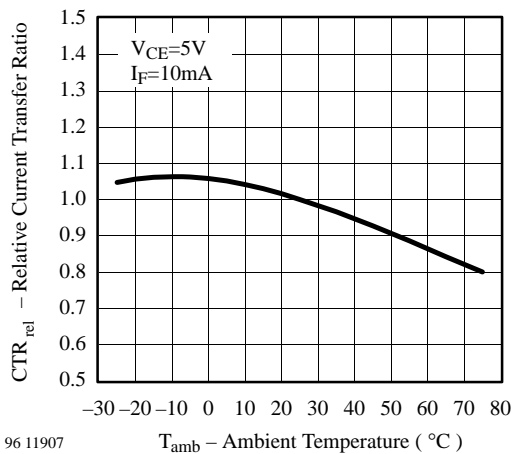


Figure 5. Rel. Current Transfer Ratio vs. Ambient Temperature

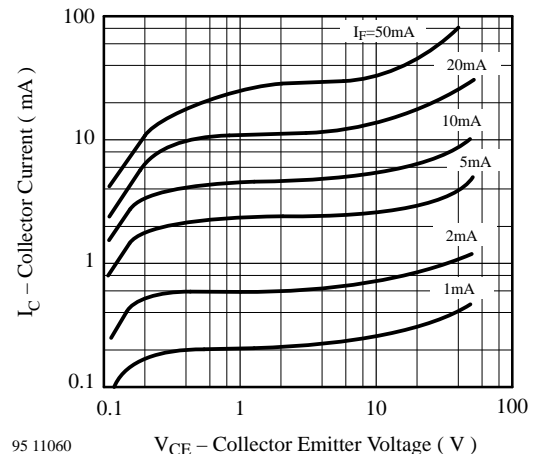


Figure 8. Collector Current vs. Collector Emitter Voltage

Typical Characteristics ($T_{amb} = 25^{\circ}\text{C}$, unless otherwise specified)

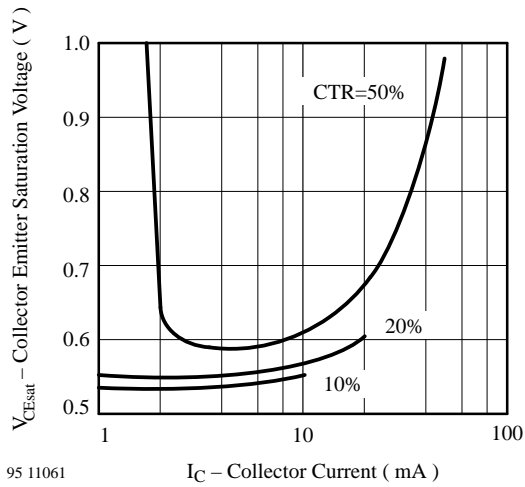


Figure 9. Collector Emitter Sat. Voltage vs. Collector Current

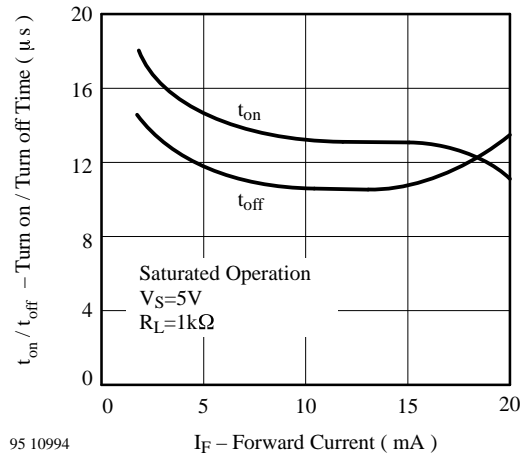


Figure 11. Turn on / off Time vs. Forward Current

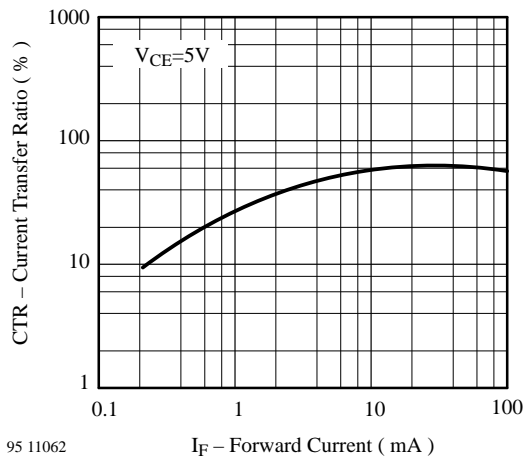


Figure 10. Current Transfer Ratio vs. Forward Current

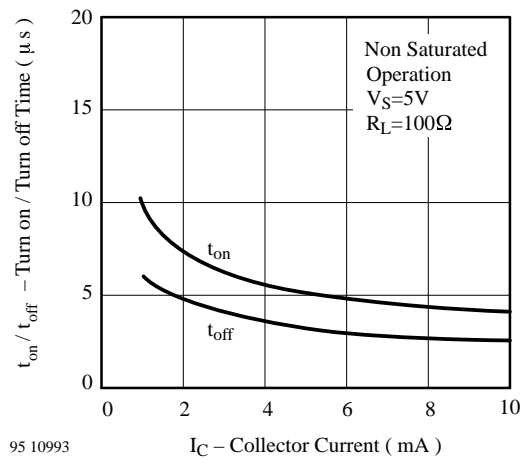
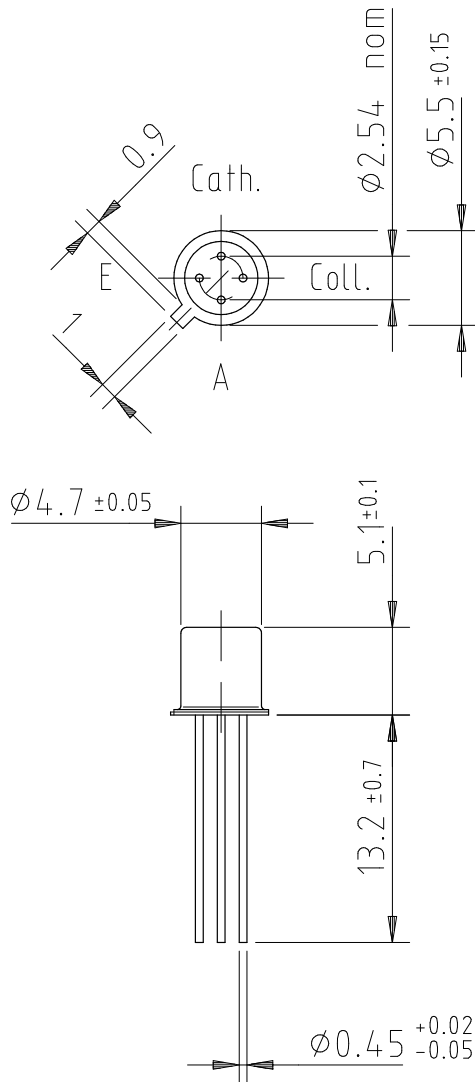
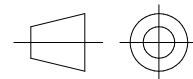


Figure 12. Turn on / off Time vs. Collector Current

Dimensions in mm



96 12078



technical drawings
according to DIN
specifications

Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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