

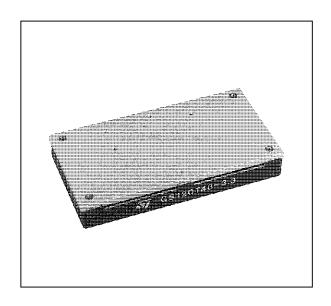
GS120/175T48 Family

120W/175W DC-DC CONVERTERS FAMILY

Туре	V _i	Vo	lo
GS120T48-3.3 GS120T48-3.3E	38 to 60 V	3,35 V	35 A
GS175T48-5 GS175T48-5E	38 to 60 V	5,075 V	35 A
GS175T48-12 GS175T48-12E	38 to 60 V	12,0 V	15 A
GS175T48-15 GS175T48-15E	38 to 60 V	15,0 V	12 A

FEATURES

- UL, CSA, TUV approved
- High output power (up to 175W)
- High efficiency (82% typ. on GS175T48-5 mod-
- Parallel operation with equal current sharing
- Synchronization pinRemote ON/OFF
- Remote load voltage sense compensation
- Output short-circuit protection
- Undervoltage lock-out
- Minimal overshoot during load transients
- Output overvoltage protection
- 500V_{DC} input to output isolation voltage
- Internal input and output filtering
- Softstart
- PCB or chassis mountable
- Optional additional finned heatsink
- Mechanical dimensions 125 66,5 19 (4,92 $2,62 \bullet 0,75)$



DESCRIPTION

The GS120/175T48family includes 120/175W DC-DC converters used to generate fixed isolated output voltages with an output current up to 35A from a wide range input voltage (38 to 60V).

The suffix E Identifies the metric threading on the planar heatsink (see fig. 1).

OPTION

Type Ordering Number	Description	Thermal Resistance	Dimensions L • W • H mm (inches)
HS01	Additional finned heatsink (See fig. 7)	2.8°C/W	125 • 66.5 • 15 (4.92 • 2.62 • 0.59)

June 1994 1/10

 $\textbf{GS120T48-3.3ELECTRICAL CHARACTERISTICS} \ (T_{amb} = 25^{\circ}\text{C unless otherwise specified})$

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Vi	Input Voltage	$V_0 = 3.35V$ $I_0 = 0$ to 35A (Operating Conditions)	38	48	60	VDC
Viuv	Input Undervoltage Lockout	Vo = 3.35V I _O = 0 to 35A	32	34	36	VDC
lį	Average Input Current	Vi = 0 to 60V Io = 35A			4.2	А
lipk	Inrush Transient Peak Current	Vi = 60V I _O = 35A			0.2	A ² s
lir	Reflected Input Current	Vi = 38 to 60V BW = 5Hz to 20MHz Io = 35A (See fig. 2)			20	mApp
Vien	Enable Input Voltage	Vi = 38 to 60V Io = 0 to 35A	0		1.2	V
lien	Enable Input Current	Vi = 38 to 60V I _O = 0 to 35A Vien = 0V			-1	mA
Viinh	Inhibit Voltage	Vi = 38 to 60V Io = 0 to 35A Vien = open	8		18	V
Pi	Input Power	$V_i = 38 \text{ to } 60V \text{ I}_0 = 0A \text{ (No Load)}$		1.5	2	W
V_{0}	Total Output Voltage Regulation	Vi = 38 to 60V lo = 0 to 35A	3.25	3.35	3.45	V
Vost	Short-term Output Voltage Regulation	Vi = 38 to 60V Io = 0 to 35A	3.30	3.35	3.40	٧
Vots	Total Static Tolerance	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 35A$	3.28	3.35	3.42	V
Vol	Output Overvoltage Limit Initiation	ut Overvoltage V _i = 38 to 60V I _O = 0 to 35A Initiation		4.5	5.2	VDC
Vor	Output Ripple Voltage	$V_i = 38 \text{ to } 60V I_0 = 35A$		20	30	mVpp
Von	Output Noise Voltage	Vi = 38 to 60V Io = 35A		50	80	mVpp
ΔV_0	Total Remote Sense Compensation	Vi = 38 to 60V			0.6	>
δVο	Peak Load Transient Response	Vi = 48V δIo = 5A slope = 0.1A/μs			60	mVp
lo	Output Current	$V_i = 38 \text{ to } 60V \ V_0 = 3.35V$	0		35	Α
lol	Overcurrent Limit Initiation	Vi = 48V	36		39	Α
losc	Shortcircuit Output Current	Vi = 48V V ₀ = 0.2 to 0.5V			51	Α
ts	Load Transient Settling Time	Vi = 48V δIo = 5A slope = 0.1A/μs			200	μs
ton	Turn-on Time	Vi = 48V Io = 35A Vien = from high to low			5	ms
		Vi = 0 to 60V Io = 35A Vien = low	3		10	
Vis	Isolation Voltage		500			V
fs	Switching Frequency	V _i = 38 to 60V I ₀ = 0 to 35A	160	175	200	kHz
η	Efficiency V _i = 38 to 60V I _O = 35A		76	77		%
Rth	Thermal Resistance	al Resistance Case to Ambient		5.2		°C/W
T _{cop}	Operating Case Temperature Range	perating Case emperature Range			+85	°C
Tstg	Storage Temperature Range		- 40		+105	°C

GS175T48-5 ELECTRICAL CHARACTERISTICS ($T_{amb} = 25$ °C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Vi	Input Voltage $V_0 = 5.075V$ $I_0 = 0$ to 35A (Operating Conditions)		38	48	60	VDC
Viuv	Input Undervoltage Lockout	V ₀ = 5.075V I ₀ = 0 to 35A		34	36	VDC
lį	Average Input Current	V _i = 0 to 60V I _O = 35A			6.1	А
lipk	Inrush Transient Peak Current	Vi = 60V Io = 35A			0.2	A ² s
lir	Reflected Input Current	Vi = 38 to 60V BW = 5Hz to 20MHz I _O = 35A (See fig. 2)			30	mApp
Vien	Enable Input Voltage	V _i = 38 to 60V I _O = 0 to 35A	0		1.2	V
lien	Enable Input Current	Vi = 38 to 60V Io = 0 to 35A Vien = 0V			-1	mA
Viinh	Inhibit Voltage	V_i = 38 to 60V I_0 = 0 to 35A Vien = open	8		18	V
Pi	Input Power	$V_i = 38 \text{ to } 60V I_0 = 0A \text{ (No Load)}$		1.5	2	W
Vo	Total Output Voltage Regulation	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 35A$	4.94	5.075	5.21	V
Vost	Short-term Output Voltage Regulation	Vi = 38 to 60V Io = 0 to 35A	5.002	5.075	5.148	٧
Vots	Total Static Tolerance	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 35A$	4.97	5.075	5.18	V
Vol	Output Overvoltage Limit Initiation	V _i = 38 to 60V I _O = 0 to 35A		6.3	7	VDC
Vor	Output Ripple Voltage	$V_i = 38 \text{ to } 60V I_0 = 35A$		20	30	mVpp
Von	Output Noise Voltage	$V_i = 38 \text{ to } 60V I_0 = 35A$		50	80	mVpp
ΔV_{0}	Total Remote Sense Compensation	Vi = 38 to 60V			0.6	V
δV_0	Peak Load Transient Response	$V_i = 48V \delta I_0 = 5A$ slope = 0.1A/ μ s			100	mVp
lo	Output Current	Vi = 38 to 60V Vo = 5.075V	0		35	Α
lol	Overcurrent Limit Initiation	Vi = 48V	36		39	Α
losc	Shortcircuit Output Current	$V_i = 48V$ $V_0 = 0.2 \text{ to } 0.5V$			51	Α
ts	Load Transient Settling Time	$V_i = 48V \delta I_0 = 5A$ slope = 0.1A/ μ s			250	μs
ton	Turn-on Time	Vi = 48V Io = 35A Vien = from high to low			5	ms
		$V_i = 0$ to 60V $I_0 = 35A$ Vien = low	3		10	
Vis	Isolation Voltage		500			V
fs	Switching Frequency	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 35A$	160	175	200	kHz
η	Efficiency	$V_i = 38 \text{ to } 60V I_0 = 35A$	81	82		%
Rth	Thermal Resistance	Case to Ambient		5.2	°C/W	
T _{cop}	Operating Case Temperature Range		– 10		+85	°C
T _{stg}	Storage Temperature Range -40 +105		+105	°C		



GS175T48-12 ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^{\circ}C$ unless otherwise specified)

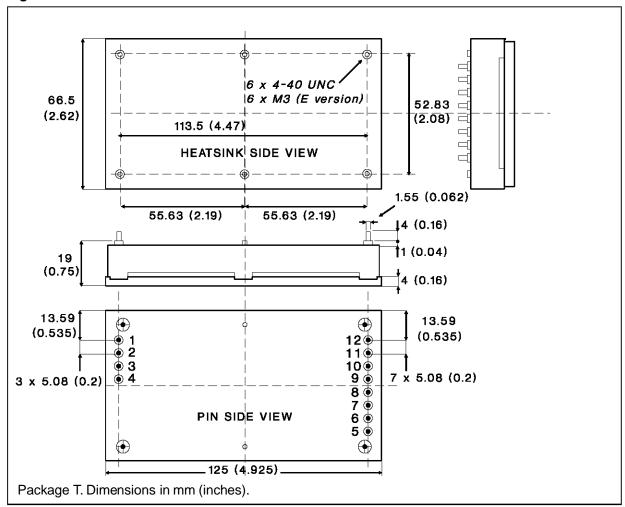
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Vi	Input Voltage	V _O = 12V I _O = 0 to 15A (Operating Conditions)	38	48	60	VDC
Viuv	Input Undervoltage Lockout	V _O = 12V I _O = 0 to 15A		34	36	VDC
lį	Average Input Current	V _i = 0 to 60V I _O = 15A			5.5	Α
lipk	Inrush Transient Peak Current	Vi = 60V Io = 15A			0.2	A ² s
lir	Reflected Input Current	V _i = 38 to 60V BW = 5Hz to 20MHz I ₀ = 15A (See fig. 2)			20	mApp
Vien	Enable Input Voltage	V _i = 38 to 60V I _O = 0 to 15A	0		1.2	V
lien	Enable Input Current	Vi = 38 to 60V I _O = 0 to 15A Vien = 0V			-1	mA
Viinh	Inhibit Voltage	V _i = 38 to 60V I _O = 0 to 15A Vien = open	8		18	٧
Pi	Input Power	V _i = 38 to 60V I _O = 0A (No Load)		1.5	2	W
Vo	Total Output Voltage Regulation	V _i = 38 to 60V I _O = 0 to 15A	11.4	12.0	12.6	V
Vost	Short-term Output Voltage Regulation	Vi = 38 to 60V Io = 0 to 15A		12.0	12.24	٧
Vots	Total Static Tolerance	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 15A$	11.64	12.0	12.36	V
Vol	Output Overvoltage Limit Initiation	V _i = 38 to 60V I ₀ = 0 to 15A		14	15	VDC
Vor	Output Ripple Voltage	V _i = 38 to 60V I _O = 15A		35	70	mVpp
Von	Output Noise Voltage	V _i = 38 to 60V I _O = 15A 60		60	120	mVpp
ΔV_0	Total Remote Sense Compensation	Vi = 38 to 60V			0.6	V
δVο	Peak Load Transient Response	$V_i = 48V \delta I_0 = 3A$ slope = 0.2A/ μ s			200	mVp
lo	Output Current	Vi = 38 to 60V Vo = 12V	0		15	Α
lol	Overcurrent Limit Initiation	V _i = 48V	16		19	Α
losc	Shortcircuit Output Current	V _i = 48V			25	Α
ts	Load Transient Setting Time	$V_i = 48V \delta I_0 = 3A$ slope = 0.2A/ μ s			300	μs
ton	Turn-on Time	Vi = 48V lo = 15A Vien = from high to low			5	ms
		V _i = 0 to 60V I _O = 15A V _{ien} = low	3		10	
Vis	Isolation Voltage		500			V
fs	Switching Frequency	V _i = 38 to 60V I ₀ = 0 to 15A	160	175	200	kHz
η	Efficiency	Vi = 38 to 60V I ₀ = 15A	84	86		%
Rth	Thermal Resistance	Case to Ambient		5.2	°C/W	
T _{cop}	Operating Case Temperature Range	erating Case - 10 +8 herature Range		+85	°C	
Tstg	Storage Temperature Range -40 +105		+105	°C		

GS175T48-15 ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Vi	Input Voltage	V _O = 15V I _O = 0 to 12A (Operating Conditions)	38	48	60	VDC
Viuv	Input Undervoltage Lockout	V _O = 15V I _O = 0 to 12A		34	36	VDC
lį	Average Input Current	V _i = 0 to 60V I _O = 12A			5.5	Α
lipk	Inrush Transient Peak Current	Vi = 60V Io = 12A			0.2	A ² s
lir	Reflected Input Current	Vi = 38 to 60V I _O = 12A			20	mApp
Vien	Enable Input Voltage	Vi = 38 to 60V lo = 0 to 12A	0		1.2	V
lien	Enable Input Current	Vi = 38 to 60V lo = 0 to 12A Vien = 0V			-1	mA
Viinh	Inhibit Voltage	V _i = 38 to 60V I _O = 0 to 12A Vien = open	8		18	٧
Pi	Input Power	$V_i = 38 \text{ to } 60V I_0 = 0A \text{ (No Load)}$		1.5	2	W
Vo	Total Output Voltage Regulation	Vi = 38 to 60V lo = 0 to 12A	14.25	15.0	15.75	V
Vost	Short-term Output Voltage Regulation	Vi = 38 to 60V lo = 0 to 12A	14.7	15.0	15.3	V
Vots	Total Static Tolerance	Vi = 38 to 60V lo = 0 to 12A	14.55	15.0	15.45	٧
Vol	Output Overvoltage Limit Initiation			17	18	VDC
Vor	Output Ripple Voltage	V _i = 38 to 60V I _O = 12A		45	90	mVpp
Von	Output Noise Voltage	Vi = 38 to 60V lo = 12A		75	150	mVpp
ΔV_{0}	Total Remote Sense Compensation	V _i = 38 to 60V			0.6	V
δV_0	Peak Load Transient Response	$V_i = 48V \delta I_0 = 3A$ $slope = 0.2A/\mu s$			200	mVp
lo	Output Current	Vi = 38 to 60V Vo = 15V	0		12	Α
lol	Overcurrent Limit Initiation	Vi = 48V	13		16	Α
losc	Shortcircuit Output Current	$V_i = 48V$ $V_0 = 0.2 \text{ to } 0.5V$			21	Α
ts	Load Transient Settling Time	$V_i = 48V \delta I_0 = 3A$ $slope = 0.2A/\mu s$			300	μs
ton	Turn-on Time	Vi = 48V Io = 12A Vien = from high to low			5	ms
		V _i = 0 to 60V I ₀ = 12A Vien = low	3		10	
Vis	Isolation Voltage		500			V
fs	Switching Frequency	$V_i = 38 \text{ to } 60V I_0 = 0 \text{ to } 12A$	160	175	200	kHz
η	Efficiency	Vi = 38 to 60V I ₀ = 12A	86	88		%
Rth	Thermal Resistance	Case to Ambient		5.2		°C/W
T _{cop}	Operating Case Temperature Range		- 10		+85	°C
Tstg	Storage Temperature Range		- 40		+105	°C



CONNECTION DIAGRAM AND MECHANICAL DATA Figure 1.



PIN DESCRIPTION

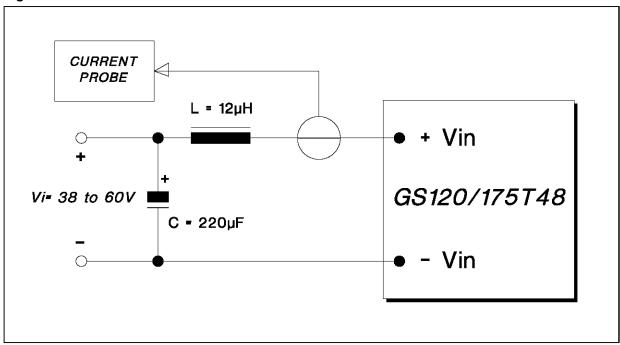
Pin	Function	Description
1	- IN	Negative input voltage.
2	+ IN	Positive input voltage. Unregulated input voltage (typically 48V) must be applied between pin 1-2.
3	ON/OFF	The converter is ON (Enable) when the voltage applied to this pin with reference to pin 1 is lower than 1.2V (see Vien). The converter is OFF (Inhibit) for a control voltage in the range of 8 to 18V. When the pin is unconnected the converter is OFF (Inhibit).
4	CASE	Case connection pin.
5	SYNC	Synchronization pin. See figures 3, 4, 5, 6. Open when not used.
6	PARALLEL	Parallel output. See figures 3, 4, 5, 6. Open when not used.
7	+ SENSE	Senses the remote load high side. To be connected to pin 11,12 when remote sense is not used.
8	- SENSE	Senses the remote load return. To be connected to pin 9,10 when remote sense is not used. In parallel configuration, take care to connect all -S pins together (see figures 3,4,5,6).
9,10	- OUT	Fixed output voltage return.
11,12	+ OUT	Fixed output voltage.

USER NOTES

Reflected Input Current

The reflected input current measurement (lir, see Electrical Characteristics) is performed according to the test set-up of fig. 2.

Figure 2.



Softstart

To avoid heavy inrush current the output voltage rise time is 10ms maximum in any condition of load.

Remote Sensing

The remote voltage sense compensation range is for a total drop of 0.6V equally shared between the load connecting wires.

It is a good practice to shield the sensing wires to avoid oscillations.

See the connection diagram on figures 3, 4, 5, 6.

Remote ON/OFF

The module is controlled by the voltage applied between the ON/OFF pin and -IN pin.

The converter is ON (Enable) when the voltage applied is lower than 1.2 V (see Vien on Electrical Characteristics).

The converter is OFF (Inhibit) for a control voltage in the range of 8 to 18V (see Viinh).

When the pin is unconnected the converter is OFF. Maximum sinking current is 1mA.

Module Protection

The module is protected against occasional and permanent shortcircuits of the output pins to ground, as well as against output current overload. It uses a current limiting protection circuitry, avoiding latch-up problems with certain type of loads.

A crowbar output overvoltage protection is activated when the output voltage exceeds the specified values (see Electrical Characteristics).

Parallel Operation

To increase available output regulated power, the module features the parallel connection possibility with equal current sharing and maximum deviation of 10% (two modules in parallel).

See the connection diagram on figures 3, 4, 5, 6.

Figure 3.

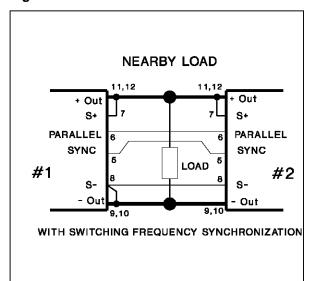


Figure 4.

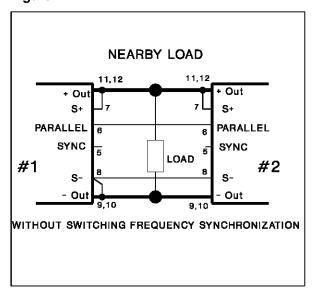


Figure 5.

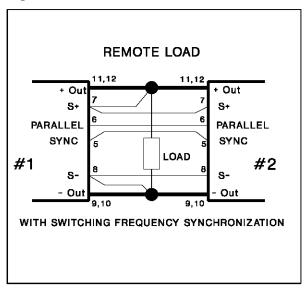
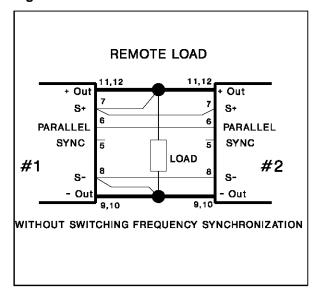


Figure 6.

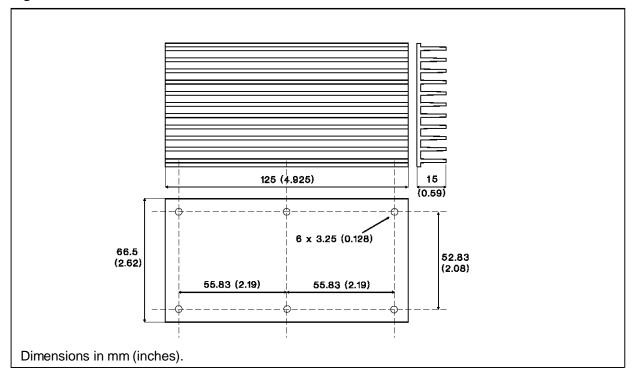


Finned heatsink option

An additional finned heatsink is available (type ordering number HS01) to allow the user to decrease the total thermal resistance of the module to a

typical value of 2.8 $^{\circ}$ C/W. The heatsink is suitable both for standard (4-40 UNC threading) and E version (M3 threading); screw length in the range of 6 to 8 mm (0.24 to 0.32"). See fig. 7.

Figure 7. - HS01 Heatsink.

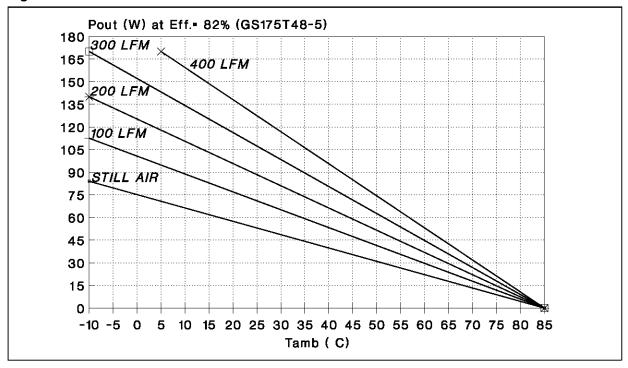


Thermal Characteristics

Following figures show the behaviour at still air and forced ventilation operation of the GS175T48-5 module (typical efficiency 82%) without

(fig. 8) and with the additional finned HS01 heatsink (fig. 9)

Figure 8. - GS175T48-5 with stadard flat heatsink.



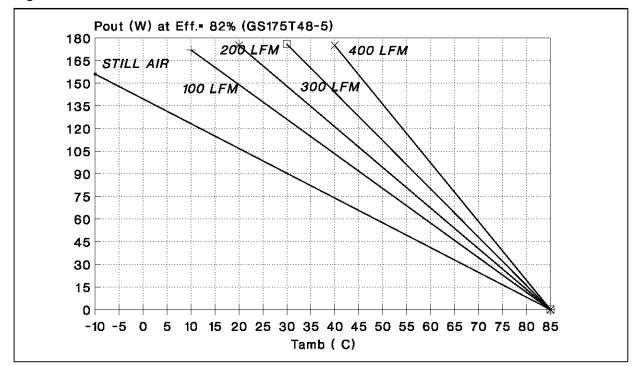


Figure 9. - GS175T48-5 with additional HS01 finned heatsink

Safety approvals

The converter is agency certified to the following safety requirements.

Agency	Requirements	License Number
UL	UL-STD-1950	E141284
CSA	CSA-STD-C22.2 No.234 (level 3)	LR 99794-2
TUV	EN 60950 DIN VDE 0805	R 9272137

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