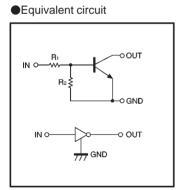
## Transistors

# Digital transistors (built-in resistors) DTC143ZE / DTC143ZUA / DTC143ZKA DTC143ZCA / DTC143ZSA

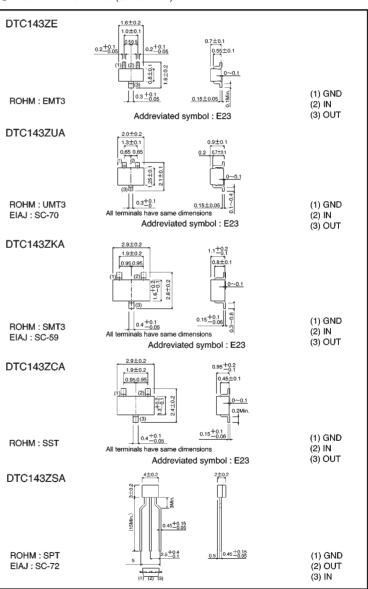
#### Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.

Structure
NPN digital transistor
(Built-in resistor type)



External dimensions (Units: mm)



(96-334-C143Z)

ROHM

### •Absolute maximum ratings (Ta = $25^{\circ}$ C)

Devementer	O week at	Lim	nits (E	DTC1	l lucià		
Parameter	Symbol	Е	UA	KA	CA	SA	Unit
Supply voltage	Vcc	50					V
Input voltage	Vin		_	5~+	V		
Output current	lo			100	mA		
	IC(Max.)	100					
Power dissipation	Pd	150		200		300	mW
Junction temperature	Tj	150				Ĉ	
Storage temperature	Tstg	-55~+150					ç

### •Electrical characteristics (Ta = $25^{\circ}$ C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Input voltage	VI (off)	—	—	0.5		Vcc=5V, lo=100 μ A		
	VI (on)	1.3	—	—	V	Vo=0.3V, Io=5mA		
Output voltage	Vo(on)	—	0.1	0.3	V	lo/li=5mA/0.25mA		
Input current	lı	—	_	1.8	mA	Vi=5V		
Output current	lo(off)	_	_	0.5	μA	Vcc=50V, VI=0V		
DC current gain	Gi	80	_	_	_	Vo=5V, lo=10mA		
Input resistance	Rı	3.29	4.7	6.11	kΩ	—		
Resistance ratio	R₂/R1	8	10	12	—	_		
Transition frequency	fт		250	—	MHz	Vc==10V, I==-5mA, f=100MHz *		

\* Transition frequency of the device

#### Packaging specifications

	Package	EMT3	UMT3	SMT3	SST3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	TL	T106	T146	T116	TP
Part No.	Basic ordering unit (pieces)	3000	3000	3000	3000	5000
DTC143ZE		0	-	-	_	-
DTC143ZU/	A	-	0	_	_	-
DTC143ZK/	٩	-	-	0	_	_
DTC143ZC/	A	-	_	—	0	_
DTC143ZS/	٩	-	-	-	-	0

11

Electrical characteristic curves

