

# 2SK2171

# **High-Frequency, Low-Frequency Amplifier Analog Switch Applications**

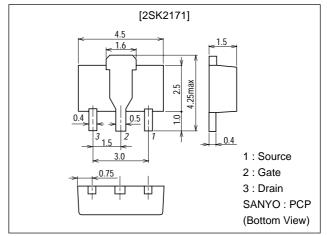
#### **Features**

- · Adoption of FBET process.
- · Large | y<sub>fs</sub> |.
- · Small Ciss.
- · High P<sub>D</sub> allowable power dissipation.

## **Package Dimensions**

unit:mm

2125



# **Specifications**

### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSX</sub>		40	V
Gate-to-Drain Voltage	V <sub>GDS</sub>		-40	V
Gate Current	IG		10	mA
Drain Current	ID		100	mA
Allowable Power Dissipation	PD		400	mW
		Mounted on ceramic board (250mm <sup>2</sup> ×0.8mm)	800	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	01111
Gate-to-Drain Breakdown Voltage	V(BR)GDS	$I_G=-10\mu A, V_{DS}=0$	-40			V
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0			-1.0	nA
Zero-Gate Voltage Drain Current	IDSS**	V <sub>DS</sub> =10V, V <sub>GS</sub> =0	40*		75*	mA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =100μA	-2.0	-3.0	-5.0	V
Forward Transfer Admittance	yfs  1	V <sub>DS</sub> =10V, I <sub>D</sub> =10mA, f=1kHz	10	15		mS
	yfs  2	$V_{DS}$ =10V, $V_{GS}$ =0, f=1kHz	22	30		mS

\*\* : Pulse Test Pulse Width≤2mS

48 63 57 75

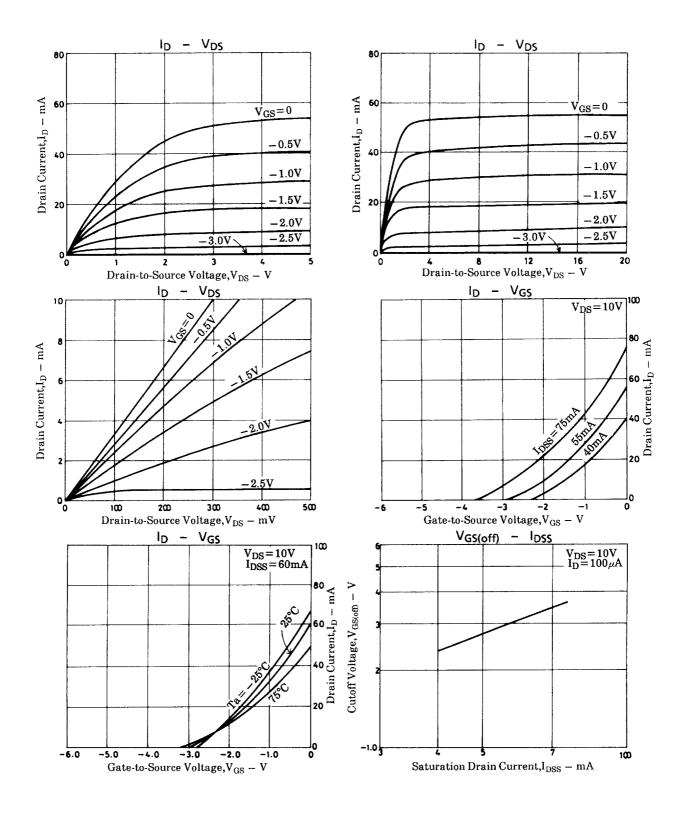
\*: The 2SK2171 is classified by  $I_{DSS}$  as follows: (unit: mA) Marking: KM I<sub>DSS</sub> rank: 3, 4, 5

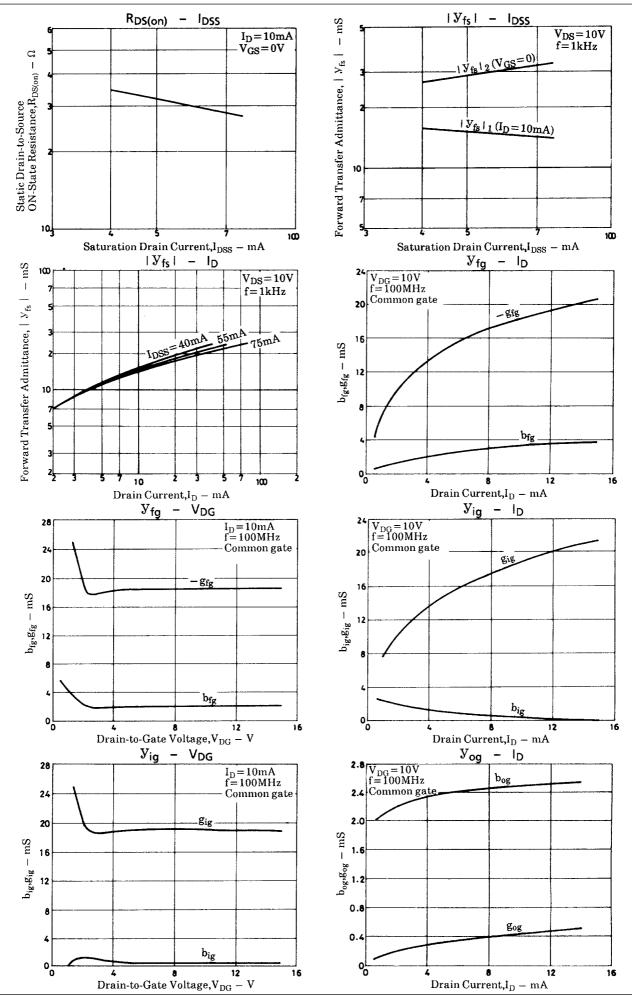
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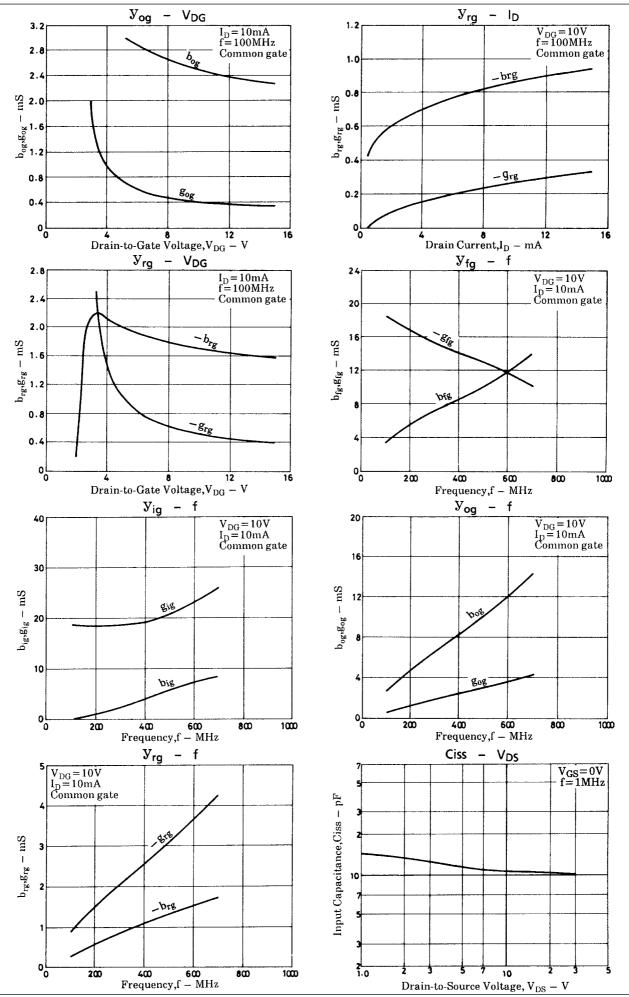
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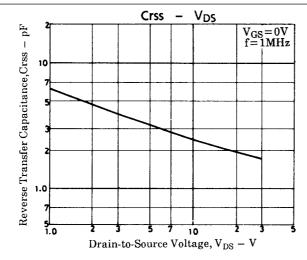
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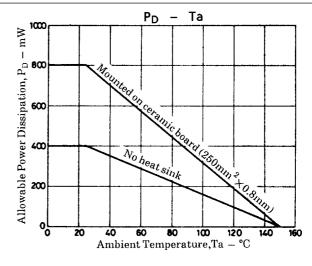
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0, f=1MHz		11		pF
Reverse Transfer Capacitance	Crss	$V_{DS}$ =10V, $V_{GS}$ =0, f=1MHz		2.5		pF
Noise Figure	NF	$V_{DS}$ =10V, Rg=1k $\Omega$ , I $_{D}$ =1mA, f=1kHz		1.5		dB
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub>	V <sub>DS</sub> =10mV, V <sub>GS</sub> =0		30		Ω











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