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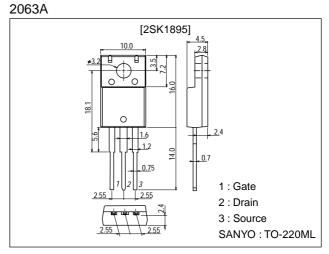
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		60	V
Gate-to-Source Voltage	V _{GSS}		±15	V
Drain Current (DC)	۱D		12	A
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	48	A
Allowable Power Dissipation	PD		2.0	W
	F D	Tc=25°C	25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	60			V
Gate-to-Source Breakdown Voltage	V(BR)GSS	I _G =±100µA, V _{DS} =0	±15			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±12V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.0	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =8A	6.5	10.5		S
Static Drain-to-Source ON-State Resistance	R _{DS(on)}	I _D =8A, V _{GS} =10V		60	80	mΩ
	R _{DS(on)}	ID=8A, VGS=4V		80	110	mΩ

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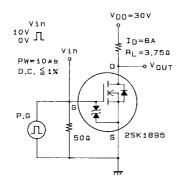
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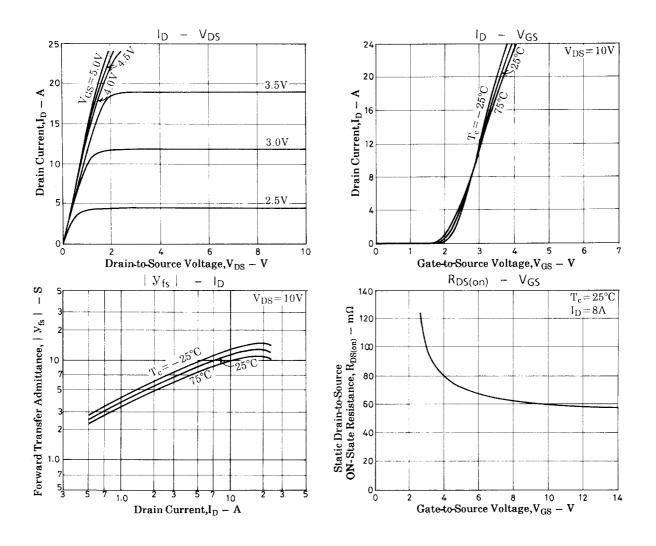
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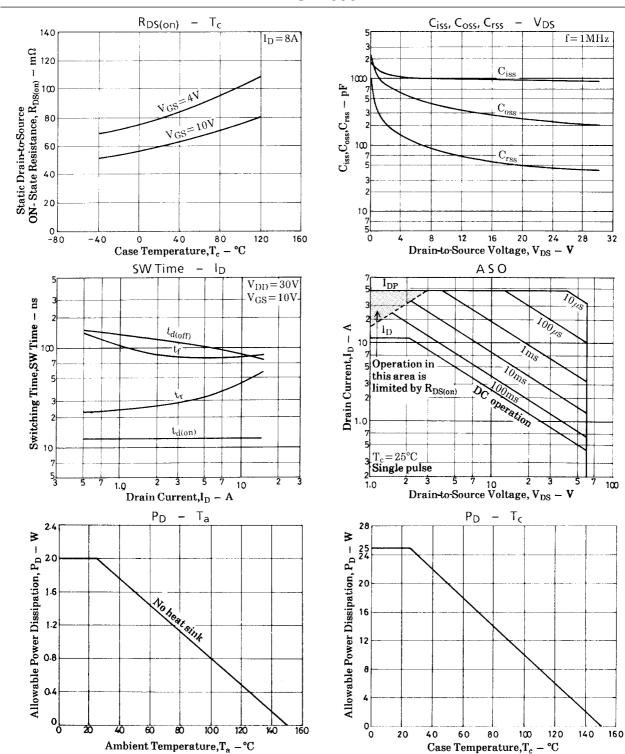
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		950		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		250		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		50		pF
Turn-ON Delay Time	^t d(on)	See specified Test Circuit		13		ns
Rise Time	tr	See specified Test Circuit		40		ns
Turn-OFF Delay Time	^t d(off)	See specified Test Circuit		95		ns
Fall Time	tf	See specified Test Circuit		80		ns
Diode Forward Voltage	V _{SD}	I _S =12A, V _{GS} =0		1.0	1.5	V

Switching Time Test Circuit







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