N-Channel Enhancement Silicon MOSFET



2SK1839

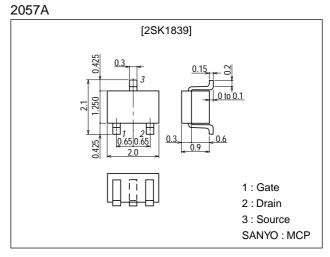
Analog Switch Applications

Features

- Ultrasmall-sized package permitting 2SK1839applied sets to be made small and slim.
- \cdot Large $|y_{fs}|$.
- · Enhancement type.
- · Low ON resistance.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±12	V
Drain Current (DC)	۱ _D		100	mA
Drain Current (pulse)	I _{DP}		300	mA
Allowable Power Dissipation	PD		150	mW
Channel Temperature	Tch		125	°C
Storage Temperature	Tstg		-55 to +125	°C

Electrical Characteristics at Ta = 25°C

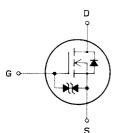
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V _(BR) DSS	I _D =10µA, V _{GS} =0	30			V
Zero-Gate Votlage Drain Current	IDSS	V _{DS} =15V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±10V, V _{DS} =0		0.01	±10	nA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =100µA	0.3	0.9	1.5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =50mA, f=1kHz	25	50		mS
Static Drain-to-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =10mA		15	25	Ω
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0, f=1MHz		12		pF
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0, f=1MHz		4		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, V _{GS} =0, f=1MHz		0.4		pF

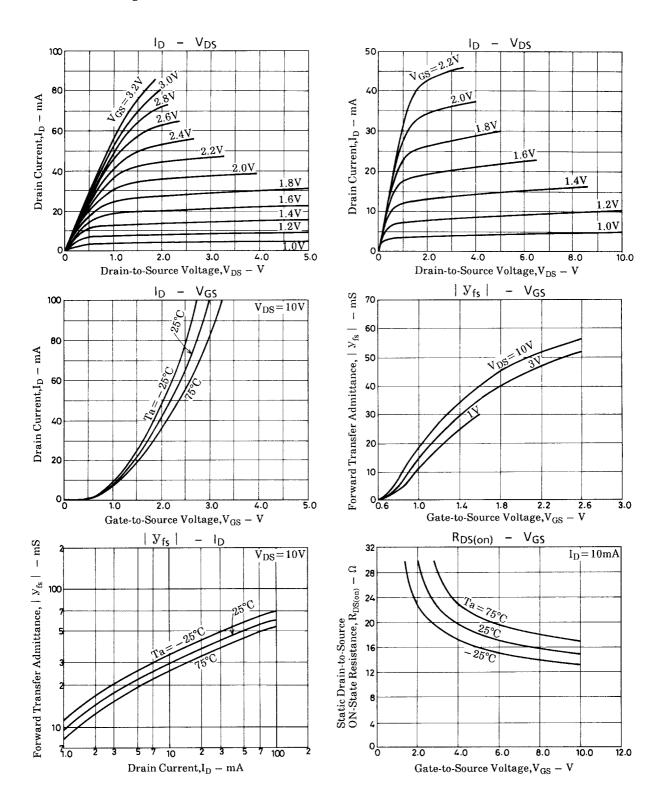
Marking : JJ

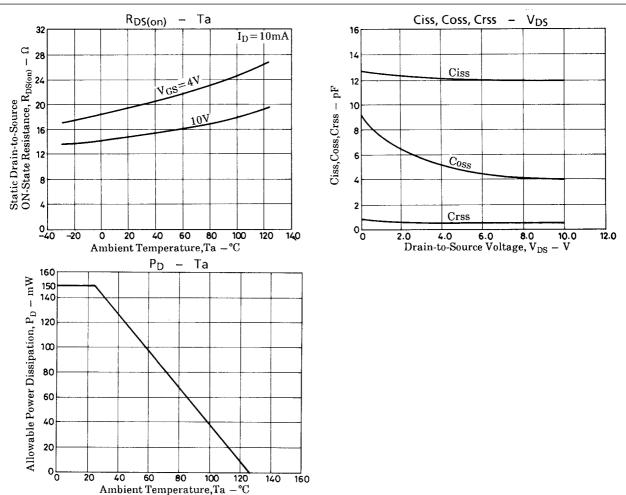
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Electrical Connection







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