

2SK1413

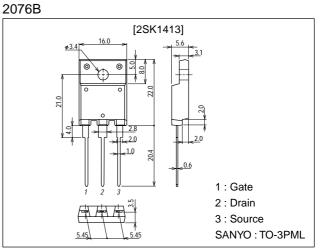
Ultrahigh-Speed Switching Applications

Features

- Low ON resistance, low input capacitance, Ultrahigh-speed switching.
- · High reliability (Adoption of HVP process).
- · Micaless package facilitating mounting.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		1500	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	۱ _D		2	A
Drain Current (Pulse)	I _{DP}	PW≤10µs, duty cycle≤1%	4	A
Allowable Power Dissipation	D-		3.0	W
	PD	Tc=25°C	60	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Symbol	Conditions	Ratings			Unit
		min	typ	max	Unit
V(BR)DSS	I _D =1mA, V _{GS} =0	1500			V
IDSS	V _{DS} =1200V, V _{GS} =0			100	μΑ
IGSS	V _{GS} =±20V, V _{DS} =0			±100	nA
VGS(off)	V _{DS} =10V, I _D =1mA	1.5		3.5	V
yfs	V _{DS} =20V, I _D =1A	1.0	1.5		S
R _{DS(on)}	I _D =1A, V _{GS} =10V		8.0	11.0	Ω
	V _{(BR)DSS} I _{DSS} I _{GSS} V _{GS(off)} yfs	V(BR)DSS ID=1mA, VGS=0 IDSS VDS=1200V, VGS=0 IGSS VGS=±20V, VDS=0 VGS(off) VDS=10V, ID=1mA yfs VDS=20V, ID=1A	V(BR)DSS ID=1mA, VGS=0 1500 IDSS VDS=1200V, VGS=0 1 IGSS VGS=±20V, VDS=0 1 VGS(off) VDS=10V, ID=1mA 1.5 I yfs VDS=20V, ID=1A 1.0	Symbol Conditions min typ V(BR)DSS ID=1mA, VGS=0 1500 1500 IDSS VDS=1200V, VGS=0 1 1 IGSS VGS=±20V, VDS=0 1 1 VGS(off) VDS=10V, ID=1mA 1.5 1 I yfs VDS=20V, ID=1A 1.0 1.5	Symbol Conditions min typ max V(BR)DSS ID=1mA, VGS=0 1500 1 1 1 1 1 100 1 100 1 1 1 100 1 <td< td=""></td<>

(Note) Be careful in handling the 2SK1413 because it has no protection diode between gate and source.

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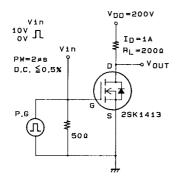
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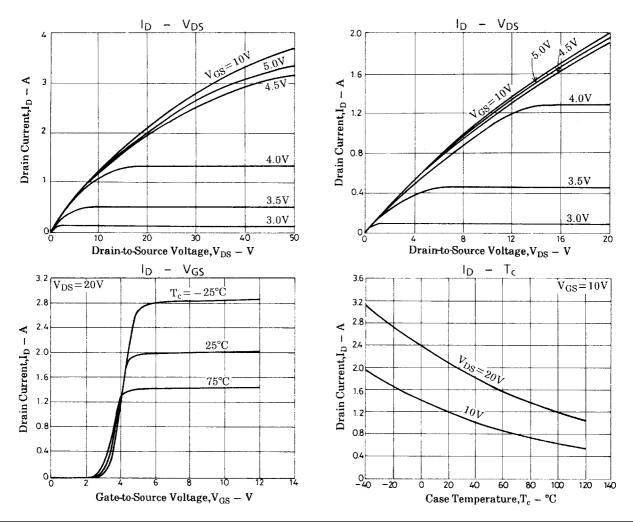
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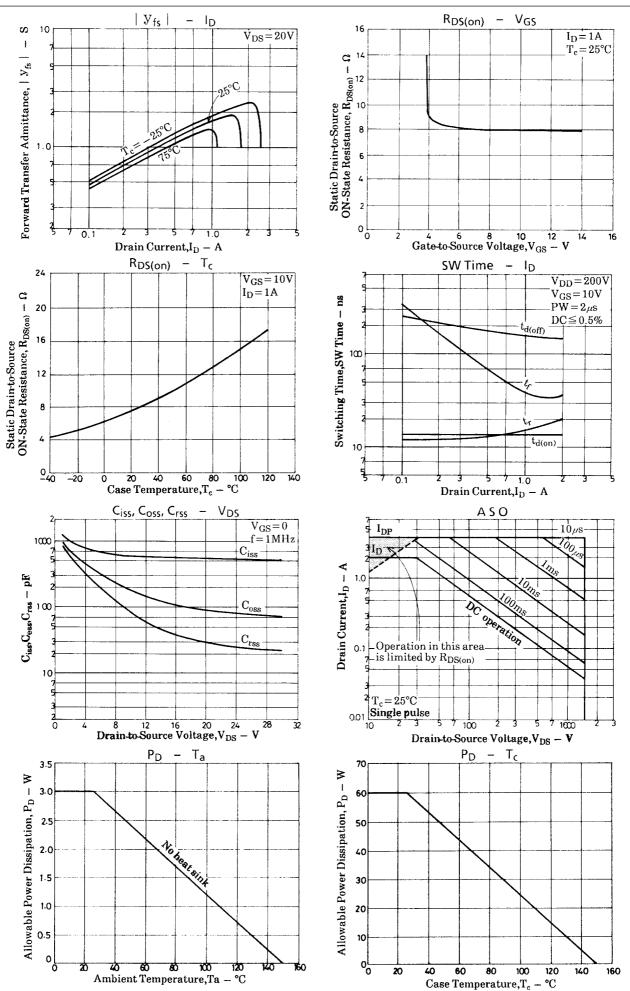
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		550		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		90		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		30		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		14		ns
Rise Time	tr	See specified Test Circuit		16		ns
Turn-OFF Delay Time	^t d(off)	See specified Test Circuit		160		ns
Fall Time	t _f	See specified Test Circuit		40		ns
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0		1.0	1.5	V

Switching Time Test Circuit







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