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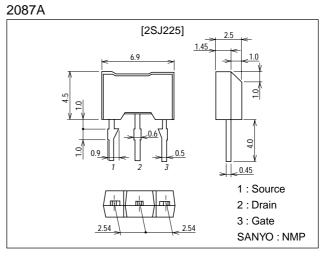
# **Ultrahigh-Speed Switching Applications**

### Features

- $\cdot$  Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.
- · Meets radial taping.

## **Package Dimensions**

unit:mm



# **Specifications**

### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±15	V
Drain Current (DC)	۱ <sub>D</sub>		-1	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10µs, duty cycle≤1%	-4	A
Allowable Power Dissipation	PD		1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0			-100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-500mA	0.6	1.0		S
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =-500mA, V <sub>GS</sub> =-10V		0.5	0.75	Ω
	R <sub>DS(on)</sub>	I <sub>D</sub> =-500mA, V <sub>GS</sub> =-4V		0.75	1.1	Ω

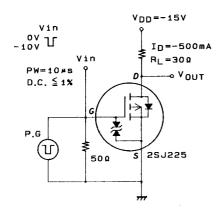
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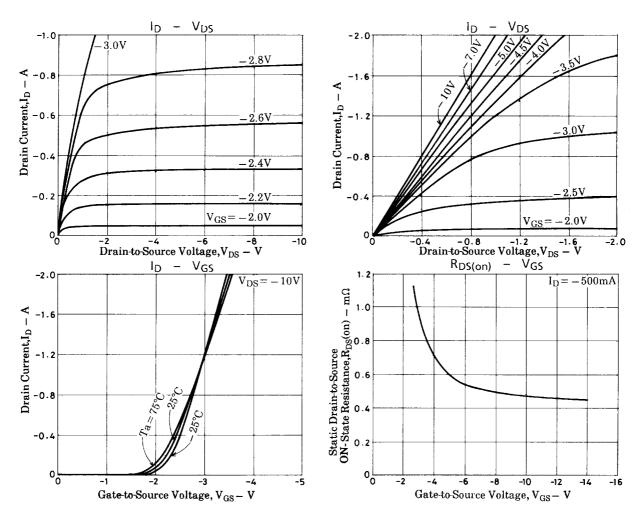
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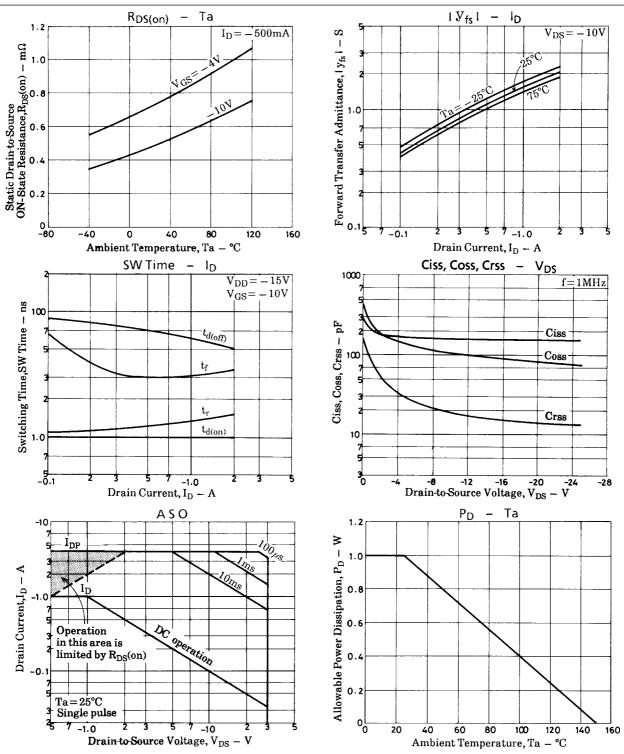
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		170		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		110		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		20		pF
Turn-ON Delay Time	<sup>t</sup> d(on)	See specified Test Circuit		10		ns
Rise Time	tr	See specified Test Circuit		13		ns
Turn-OFF Delay Time	<sup>t</sup> d(off)	See specified Test Circuit		70		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		30		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0		-0.9		V

### **Switching Time Test Circuit**







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