2SB776 : PNP Epitaxial Planar Silicon Transistor 2SD896 : NPN Triple Diffused Planar Silicon Transistor



2SB776/2SD896

# 100V/7A, AF 40W Output Applications

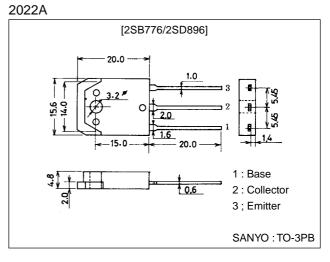
### **Features**

- Capable of being mounted easily because of onepoint fixing type plastic molded package (Interchangeable with TO-3).
- · Wide ASO because of on-chip ballast resistance.
- $\cdot$  Goode dependence of  $f_T$  on current and excellent high frequency responce.

The descriptions in parentheses are for the 2SB776 only ; other descriptions than those in parentheses are common to the 2SB776 and 2SD896.

# Package Dimensions

unit:mm



# **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		(–)120	V
Collector-to-Emitter Voltage	VCEO		(–)100	V
Emitter-to-Base Voltage	VEBO		(–)6	V
Collector Current	۱ <sub>C</sub>		(-)7	A
Collector Current (Pulse)	ICP		()11	A
Collector Dissipation	PC	Tc=25°C	70	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Electrical Characteristics at Ta = 25°C**

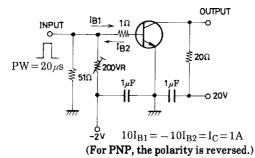
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =(-)80V, I <sub>E</sub> =0			(–)0.1	mA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =(-)4V, I <sub>C</sub> =0			(–)0.1	mA
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)1A	60*		200*	
	h <sub>FE</sub> 2	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)4A	20			
Gain-Bandwidth Product	fT	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)1A		15		MHz
Output Capacitance	Cob	V <sub>CB</sub> =(-)10V, f=1MHz		(200)		pF
				140		pF
Base-to-Emitter Voltage	VBE	V <sub>CE</sub> =(-)5V, I <sub>C</sub> =(-)1A			(–)1.5	V
* : The 2SB776/2SD896 are classified by 1A $h_{FF}$	as follows :	60 D 120 100 E 200				

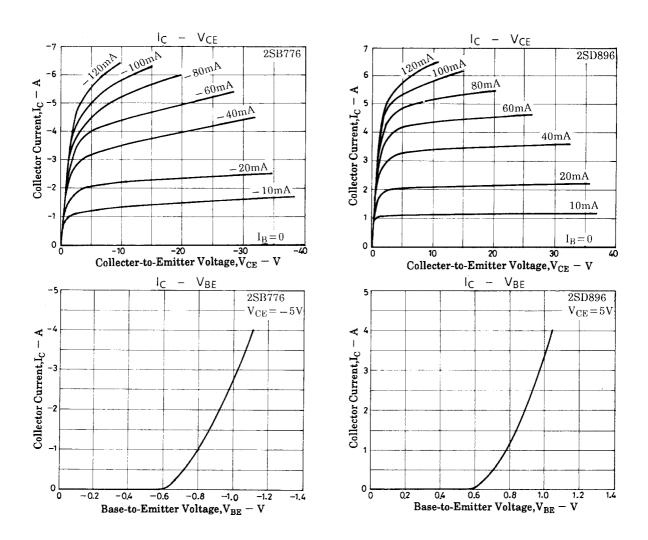
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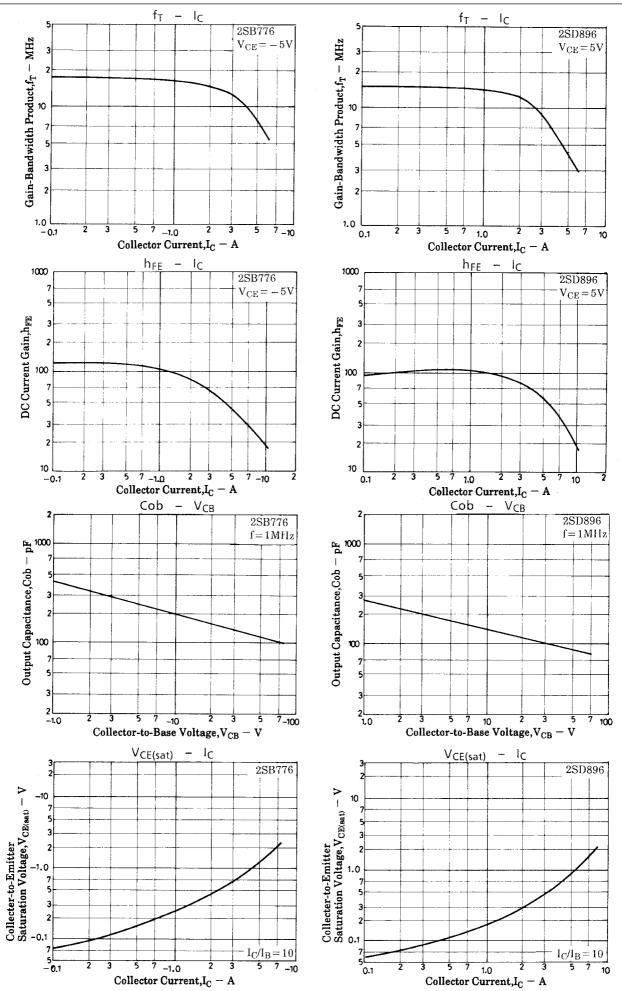
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =(-)4A, I <sub>B</sub> =(-)0.4A		(-0.9)	2.0	V
				0.6		V
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =(-)5mA, I <sub>E</sub> =0	(–)120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =(–)5mA, R <sub>BE</sub> =∞	(–)100			V
		I <sub>C</sub> =(–)50mA, R <sub>BE</sub> =∞	(–)100			V
Emitter-to-Base Breakdown Votage	V <sub>(BR)EBO</sub>	I <sub>E</sub> =(-)5mA, I <sub>C</sub> =0	(–)6			V
Turn-ON TIme	ton	See specified Test Circuit		(0.2)		μs
				0.2		μs
Storage Time	t <sub>stg</sub>	See specified Test Circuit		(0.3)		μs
				0.6		μs
Fall Time	t <sub>f</sub>	See specified Test Circuit		(1.2)		μs
				6.0		μs

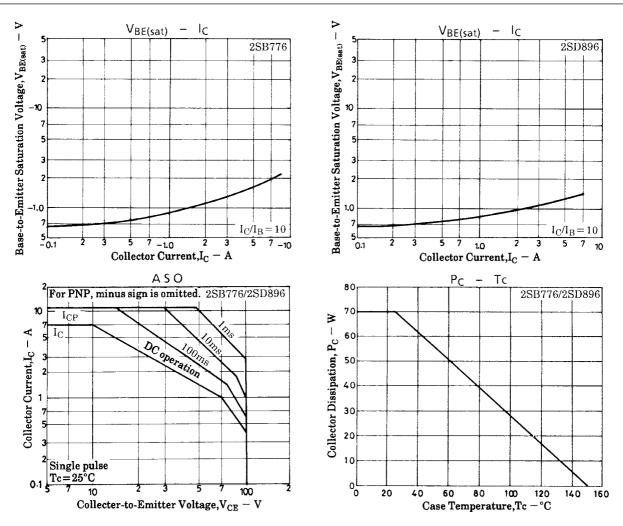
## Switching Time Test Circuit





# 2SB776/2SD896





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