NPN Triple Diffused Planar Silicon Transistor



2SD2252

Color TV Horizontal Deflection Output Applications

Applications

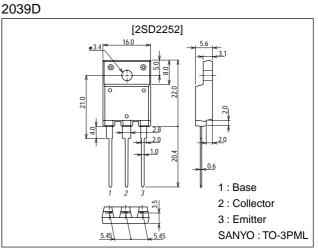
- \cdot Color TV horizontal diflection output.
- · Color display horizontal deflection output.

Features

- · High-speed : $t_f=100$ ns.
- · High breakdown voltage : V_{CBO} =1500V.
- · High reliability : Adoption of HVP process.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	ι _C		7	A
Collector Current (Pulse)	I _{CP}		20	A
Collector Dissipation	PC		60	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

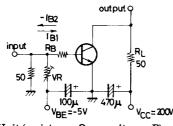
Parameter	Symbol	Conditions	Ratings			Unit
Falanielei			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =800V, I _E =0			10	μΑ
	ICES	V _{CE} =1500V, R _{BE} =0			1.0	mA
Collector-to-Emitter Sustain Voltage	V _{CEO(sus)}	I _C =100mA, I _B =0	800			V
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0			1.0	mA
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =5A, I _B =1A			5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =5A, I _B =1A			1.5	V
DC Current Gain	hFE1	V _{CE} =5V, I _C =1A	8			
	h _{FE} 2	V _{CE} =5V, I _C =5A	5		8	
Fall Time	t _f	I _C =4A, I _B 1=0.8A, I _B 2=-1.6A		0.1	0.3	μs

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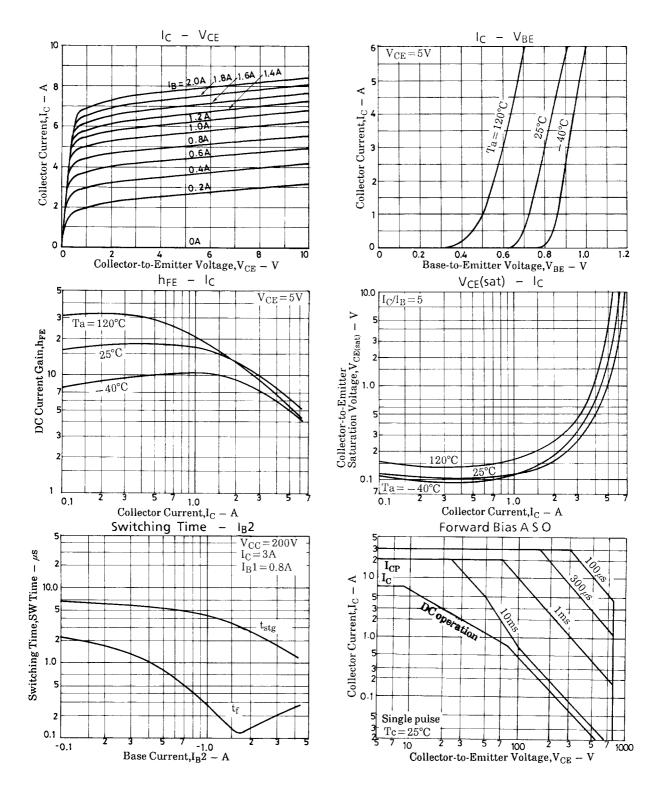
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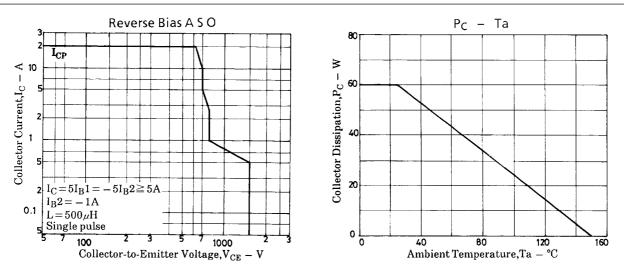
Switching Time Test Circuit

PW = $20\mu s$, duty $\leq 1\%$



Unit (resistance: Ω , capacitance:F)





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