NPN Triple Diffused Planar Silicon Transistor

2SD1958



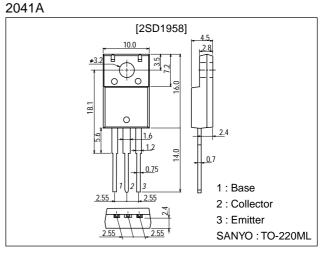
TV Horizontal Deflection Output High-Current Switching Applications

Features

 \cdot Excellent $t_{\rm f}$ permitting efficient drive with less internal dissipation.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		200	V
Collector-to-Emitter Voltage	VCEO		60	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		4.5	A
Collector Current (Pulse)	ICP		10	A
Collector Dissipation	PC	Tc=25°C	30	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0			0.1	mA
Emitter Cutoff Current	IEBO	V _{EB} =5V, I _C =0			0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =5V, I _C =1A	30		160	
	h _{FE} 2	$V_{CE}=5V, I_{C}=4A$	25			
Gain-Bandwidth Product	fT	V _{CE} =5V, I _C =1A		10		MHz
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =4A, I _B =0.4A		0.5	1.0	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =4A, I _B =0.4A			1.5	V

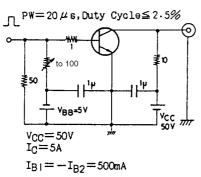
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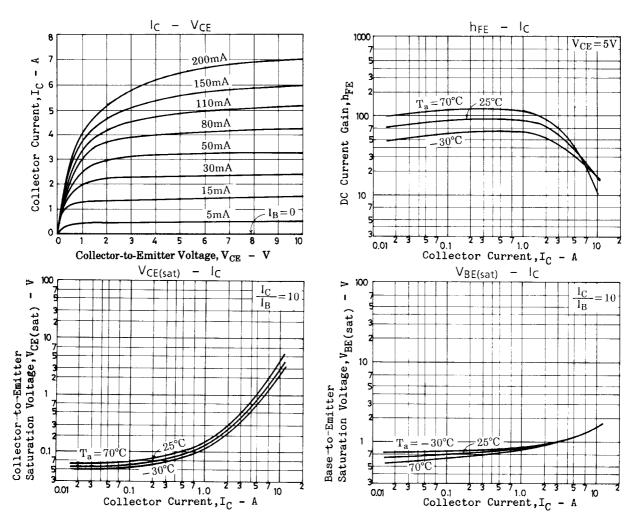
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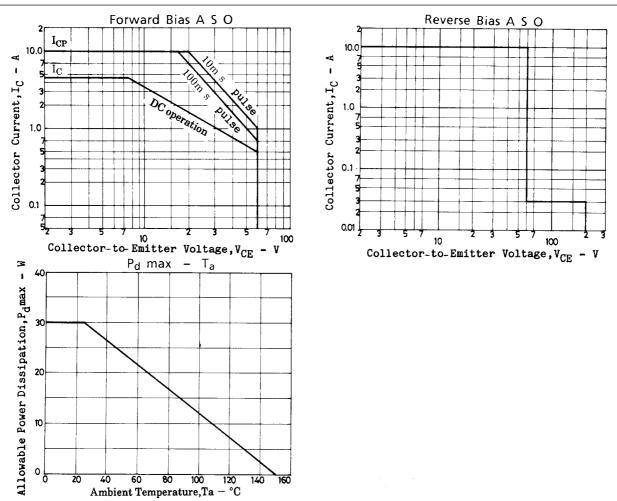
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =5mA, I _E =0	200			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =5mA, R _{BE} =∞	60			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =5mA, I _C =0	6			V
Fall Time	t _f	See specified Test Circuit.		0.2	0.5	μs

Switching Time Test Circuit







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