NPN Triple Diffused Planar Silicon Transistor



2SD1886

Color TV Horizontal Deflection Output Applications

Applications

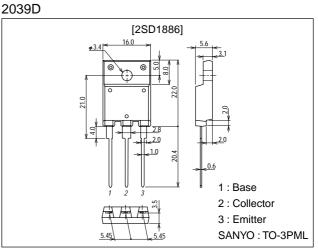
- \cdot Color TV horizontal diflection output.
- · Color display horizontal deflection output.

Features

- · High speed ($t_f=100ns$).
- · High breakdown voltage (V_{CBO} =1500V).
- · High reliability (adoption of HVP process).

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		8	A
Collector Current (Pulse)	I _{CP}		30	A
Collector Dissipation	PC		70	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

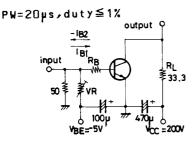
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
Falanetei			min	typ	max	
Collector Cutoff Current	ICES	V _{CE} =1500V			1.0	mA
	ICBO	V _{CB} =800V			10	μΑ
Collector-to-Emitter Sustain Voltage	VCEO(sus)	I _C =100mA, I _B =0	800			V
Emitter Cutoff Current	IEBO	V _{EB} =4V			1.0	mA
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =6A, I _B =1.2A			5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =6A, I _B =1.2A			1.5	V
DC Current Gain	h _{FE} 1	V _{CE} =5V, I _C =1A	8			
	h _{FE} 2	V _{CE} =5V, I _C =6A	5		10	
Fall Time	t _f	I _C =6A, I _{B1} =1.2A, I _{B2} =-2.4A		0.1	0.3	μs

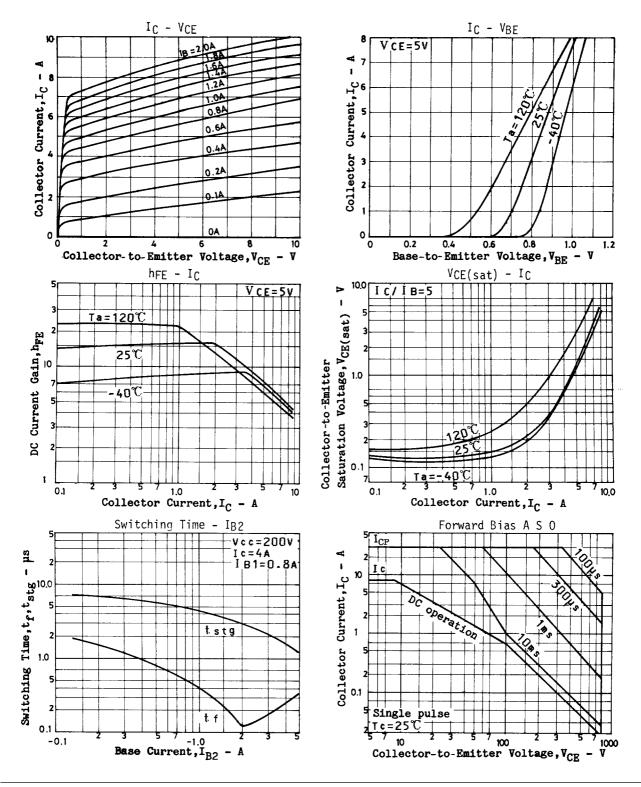
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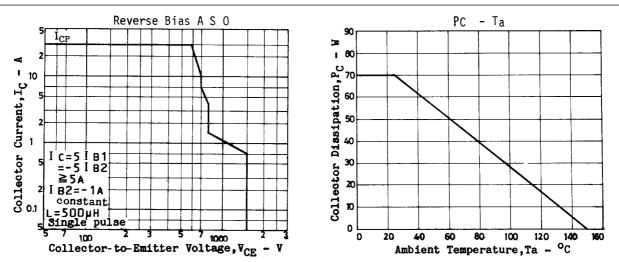
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Switching Time Test Circuit



Unit (resistance: Ω , capacitance:F)





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