

2SB1166/2SD1723

50V/8A Switching Applications

Applications

· Relay drivers, high-speed inverters, converters.

Features

- · Low collector-to-emitter saturation voltage.
- · High f_T.
- · Excellent linearity of hFE.
- · Fast switchint time.

(): 2SB1166

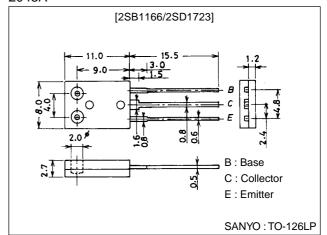
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2043A



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(–)60	V
Collector-to-Emitter Voltage	V _{CEO}		(-)50	V
Emitter-to-Base Voltage	V _{EBO}		(-)6	V
Collector Current	Ic		(–)8	Α
Collector Current (Pulse)	I _{CP}		(–)12	Α
Collector Dissipation	PC		1.2	W
		Tc=25°C	20	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =(-)40V, I _E =0			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)0.5A	70*		400*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)6A	35			
Gain-Bandwidth Product	fΤ	V _{CE} =(-)5V, I _C =(-)1A		180		MHz
				(130)		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		65(95)		pF

^{*}: The 2SB1166/2SD1723 are classified by 0.5A h_{FE} as follows:

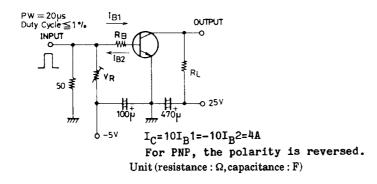
70 Q 140	100 R 200	140 S 280	200 T 400
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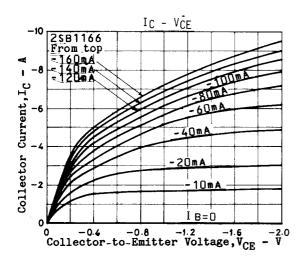
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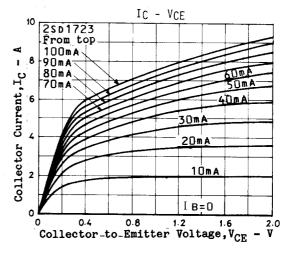
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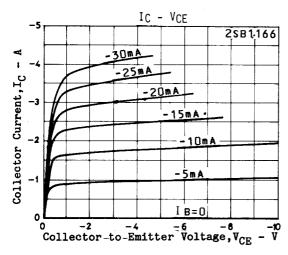
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)4A, I _B =(-)0.2A		200	400	mV
				(-250)	(-500)	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)4A, I _B =(-)0.2A		(-)0.95	(-)1.3	V
Collector-to-Base Breakdown Voltage	V _(BR) CBO	$I_{C}=(-)10\mu A, I_{E}=0$	(–)60			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	$I_C=(-)1mA$, $R_{BE}=\infty$	(–)50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)10μA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified Test Circuit		(50)50		ns
Storage Time	t _{stg}	See specified Test Circuit		500		ns
				(450)		ns
Fall Time	t _f	See specified Test Circuit		20(20)		ns

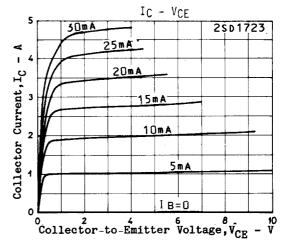
Switching Time Test Circuit

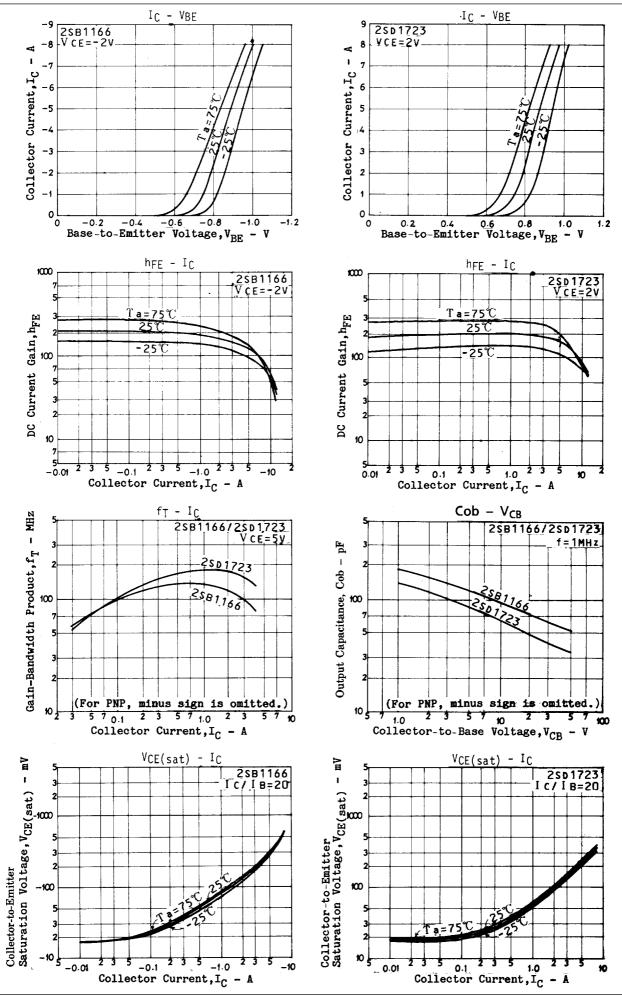




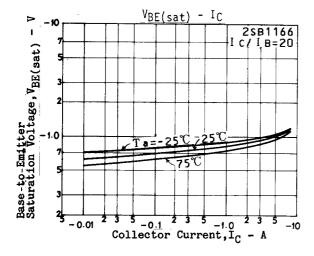


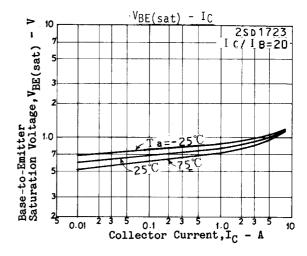


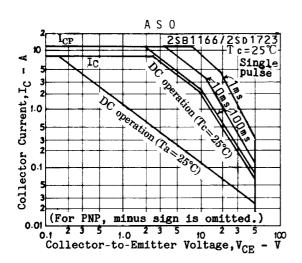


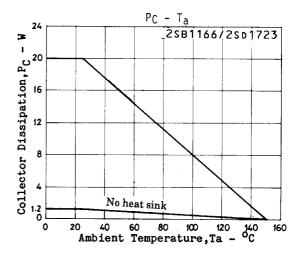


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