NPN Epitaxial Planar Silicon Transistor



2SC4909

Muting Circuits, Drivers

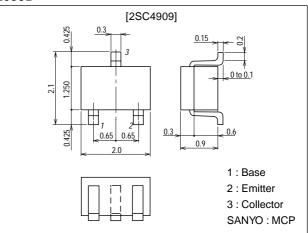
Features

- \cdot High DC current gain.
- \cdot On-chip bias resistance (R1=47k\Omega, R2=47k\Omega).
- Very small-sized package permitting 2SC4909applied sets to be made smaller and slimmer.
- · Small ON resistance.

Package Dimensions

unit:mm

2059B



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		25	V
Collector-to-Emitter Voltage	VCEO		20	V
Emitter-to-Base Voltage	VEBO		10	V
Input Voltage	VIN		18	V
Collector Current	IC		100	mA
Collector Current (Pulse)	ICP		200	mA
Base Current	IB		20	mA
Collector Dissipation	PC		200	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Collector Cutoff Current	ICBO	V _{CB} =20V, I _E =0			0.1	μΑ
	ICEO	V _{CE} =15V, I _B =0			0.5	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0	30	53	80	μΑ
DC Current Gain	h _{FE}	V _{CE} =2V, I _C =5mA	200			
Gain-Bandwidth Product	fT	V _{CE} =5V, I _C =10mA		240		MHz

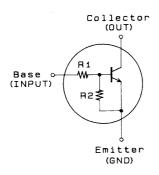
Marking : JN

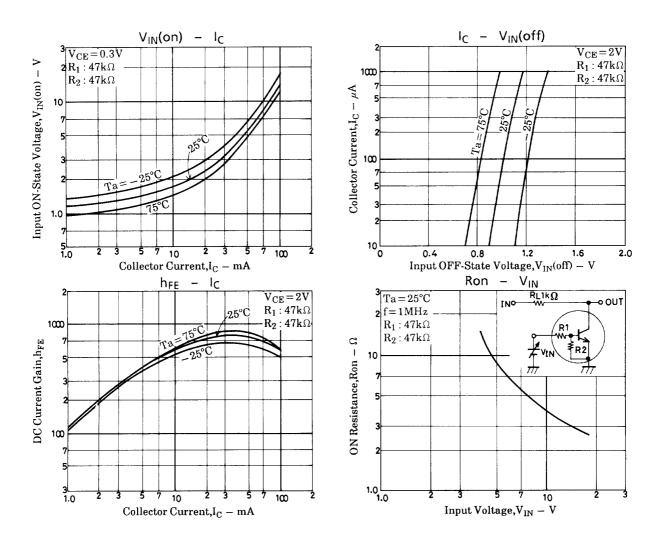
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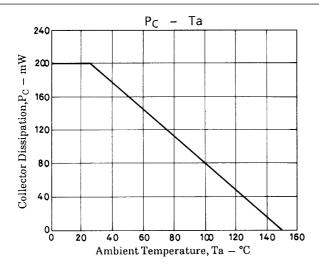
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =2mA, I _B =0.2mA		10	30	mV
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =10μA, I _E =0	25			V
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =1mA, R _{BE} =∞	20			V
Input OFF-State Voltage	V _{IN} (off)	V _{CE} =2V, I _C =100µA	0.7	1.0	1.4	V
Input ON-State Voltage	V _{IN} (on)	V _{CE} =0.3V, I _C =5mA	1.0	1.5	3.0	V
Input Resistance	R ₁		32	47	62	kΩ
Resistance Ratio	R ₁ /R ₂		0.9	1.0	1.1	
ON Resistance	Ron	V _{IN} =10V, f=1MHz		4.0		Ω

Electrical Connection







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