NPN Triple Diffused Planar Silicon Transistor



# 2SC3636

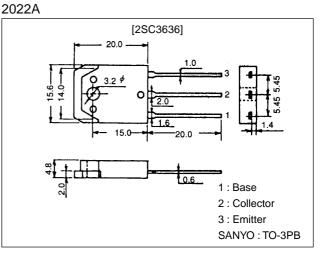
# Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

### **Features**

- · High reliability (Adoption of HVP process).
- · Fast speed.
- · High breakdown voltage.
- · Adoption of MBIT process.

## **Package Dimensions**

unit:mm



## **Specifications**

### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		900	V
Collector-to-Emitter Voltage	VCEO		500	V
Emitter-to-Base Voltage	VEBO		7	V
Collector Current	ι <sub>C</sub>		7	A
Collector Current (Pulse)	I <sub>CP</sub>		14	A
Collector Dissipation	PC	Tc=25°C	80	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

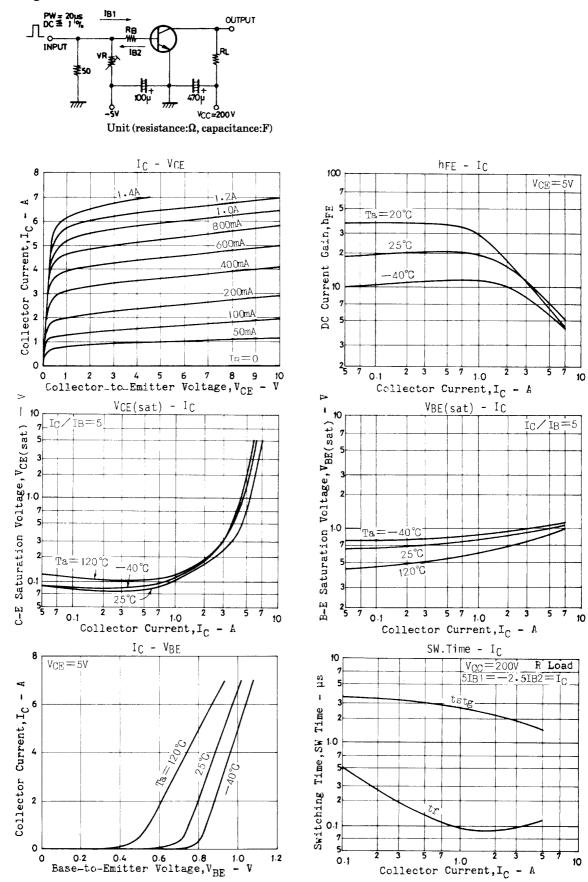
### **Electrical Characteristics at Ta = 25°C**

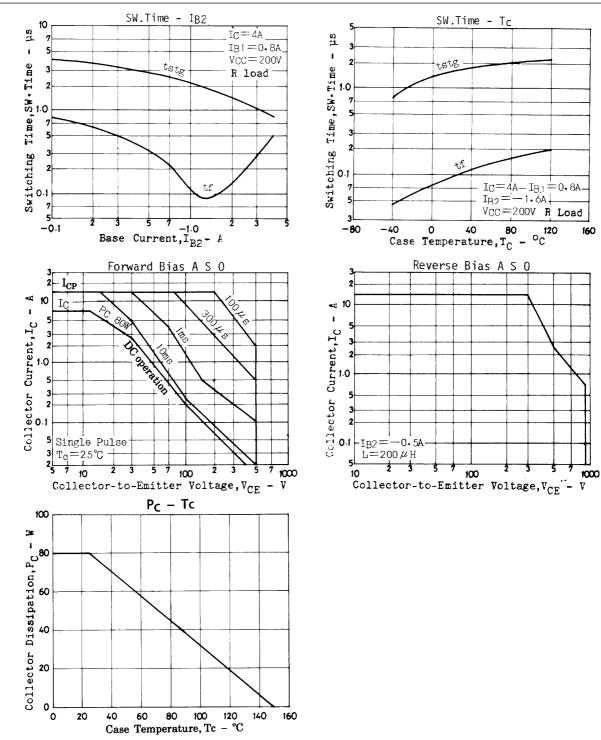
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =500V, I <sub>E</sub> =0			10	μΑ
	ICES	V <sub>CE</sub> =900V, R <sub>BE</sub> =0			0.5	mA
Collector-to-Emitter Sastain Voltage	V <sub>CEO(sus)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =0	500			V
Emitter Cutoff Current	IEBO	$V_{EB}=5V, I_{C}=0$			1	mA
Collector-to-Emitter Saturation Voltage	VCE(sat)	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			2	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =4A, I <sub>B</sub> =0.8A			1.5	V
DC Current Gain	hFE	V <sub>CE</sub> =5V, I <sub>C</sub> =0.8A	8			
Storage Time	<sup>t</sup> stg	I <sub>C</sub> =4A, I <sub>B1</sub> =0.8A, I <sub>B2</sub> =-1.6A			3.0	μs
Fall Time	t <sub>f</sub>	I <sub>C</sub> =4A, I <sub>B1</sub> =0.8A, I <sub>B2</sub> =-1.6A		0.1	0.2	μs

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SANYO Electric Co., Ltd. Semiconductor Bussiness Headquaters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

### **Switching Time Test Circuit**





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