



Driver Applications

Applications

· Suitable for use in switching of L load (motor drivers, printer hammer drivers, relay drivers).

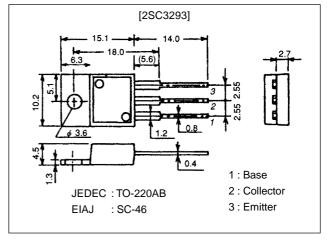
Features

- · High DC current gain.
- · Large current capacity and wide ASO.
- · On-chip Zener diode of 60±10V between collector and base.
- · Uniformity in collector-to-base breakdown voltage due to the adoption of an accurate impurity diffusion process.
- · High inductive load handling capability.

Package Dimensions

unit:mm

2010C



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		50*	V
Collector-to-Emitter Voltage	VCEO		50*	V
Emitter-to-Base Voltage	V _{EBO}		6	V
Collector Current	IC		2	Α
Collector Current (Pulse)	I _{CP}		4	Α
Base Current	I _B		0.4	Α
Collector Dissipation	PC	Tc=25°C	20	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

^{*:} With Zener diode (60±10V)

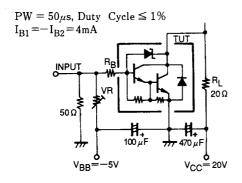
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Office
Collector Cutoff Current	I _{CBO}	V _{CB} =40V, I _E =0			10	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0			2	mA
DC Current Gain	hFE	V _{CE} =5V, I _C =1A	1000	4000		
Gain-Bandwidth Product	fT	V _{CE} =5V, I _C =1A		180		MHz
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =1A, I _B =4mA		1.0	1.5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =1A, I _B =4mA			2.0	V

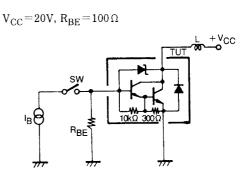
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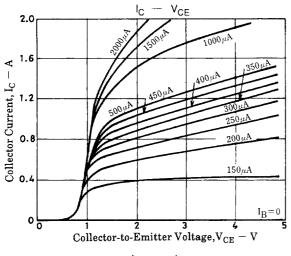
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =0.1mA, I _E =0	50	60	70	V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =1mA, R _{BE} =∞	50	60	70	V
Inductive Load Handling Capability	Es/b	L=100mH, R_{BE} =100 Ω	25			mJ
Turn-ON Time	ton	V _{CC} =20V, I _C =1A, I _{B1} =-I _{B2} =4mA		0.2		μs
Storage Time	t _{stg}	V _{CC} =20V, I _C =1A, I _{B1} =-I _{B2} =4mA		3.5		μs
Fall Time	t _f	V _{CC} =20V, I _C =1A, I _{B1} =-I _{B2} =4mA		0.5		μs

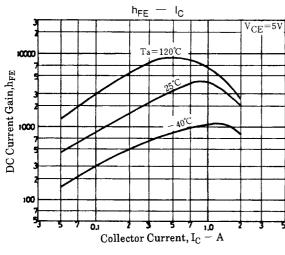
Switching Time Test Circuit

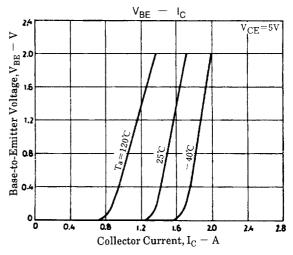


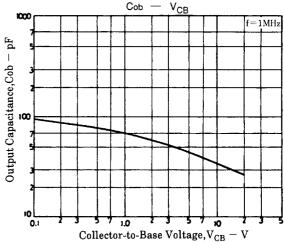
Es/b Test Circuit

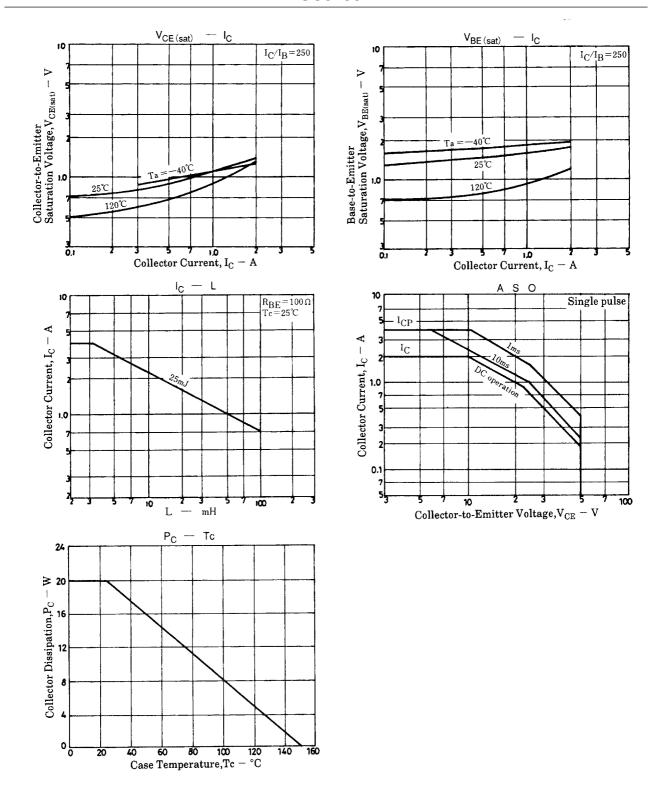












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