

80V/5A Switching Applications

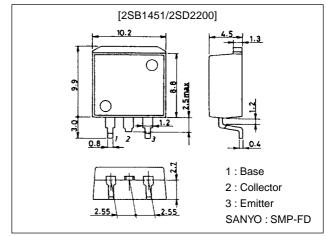
Features

- · Surface mount type device making the following possible.
 - -Reduction in the number of manufacturing processes for 2SB1451/2SD2200-applied equipment.
 - -High density surface mount applications.
 - -Small size of 2SB1451/2SD2200-applied equipment
- · Low collector-to-emitter saturation voltage.
- · Large current capacity.

Package Dimensions

unit:mm

2069B



(): 2SB1451

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)90	V
Collector-to-Emitter Voltage	V _{CEO}		(-)80	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	IC		(–)5	Α
Collector Current (Pulse)	ICP		(-)9	Α
Collector Dissipation	PC		1.65	W
		Tc=25°C	30	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Unit		
i aranetei		Conditions	min	typ	max	Oliit
Collector Cutoff Current	ICBO	V _{CB} =(-)80V, I _E =0			(–)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(–)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)1A	70*		280*	
De Guiteit Gairi	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)3A	30			
Gain-Bandwidth Product	fT	V _{CE} =(-)5V, I _C =(-)1A		20		MHz
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)3A, I _B =(-)0.3A			0.4	V
					(-0.5)	V

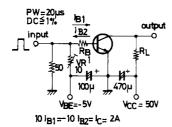
^{* :} The 2SB14512SD2200 are classified by 1A $h_{\mbox{\scriptsize FE}}$ as follows :

	70	Q	140	100	R	200	140	S	280	
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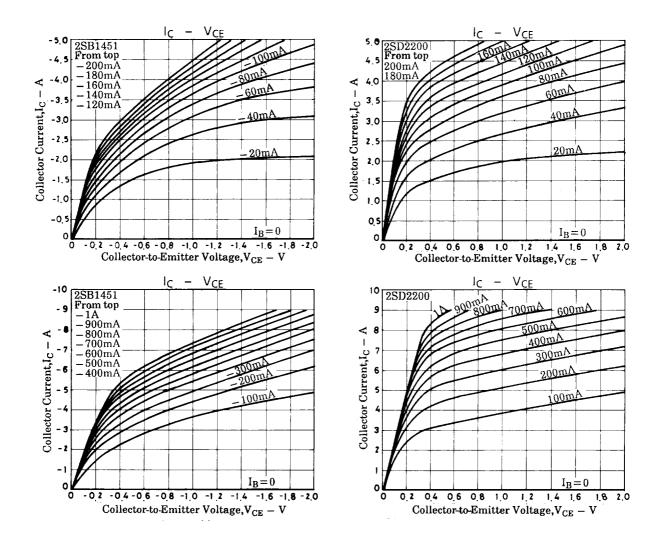
Parameter	Symbol	Conditions	Ratings			Unit
Farameter	Symbol	Conditions		typ	max	Offic
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(-)1mA, I _E =0	(–)90			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(–)1mA, R _{BE} =∞	(–)80			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)1mA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified test circuit.		(0.2)		μs
				0.1		μs
Storage Time	t _{stg}	See specified test circuit.		(0.7)		μs
				1.2		μs
Fall Time	t _f	See specified test circuit.		(0.2)		μs
				0.4		μs

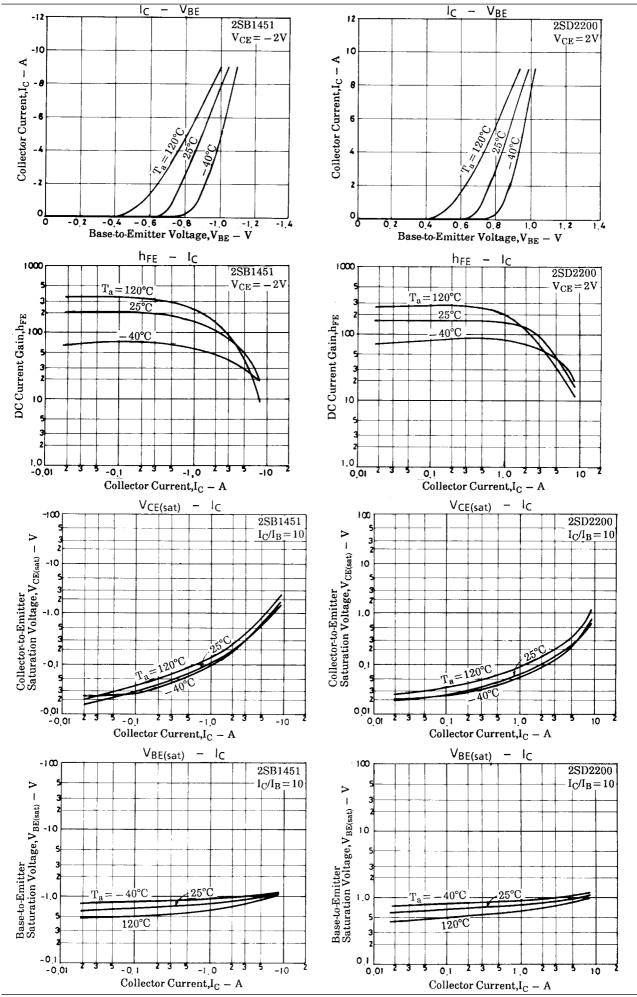
Switching Time Test Circuit

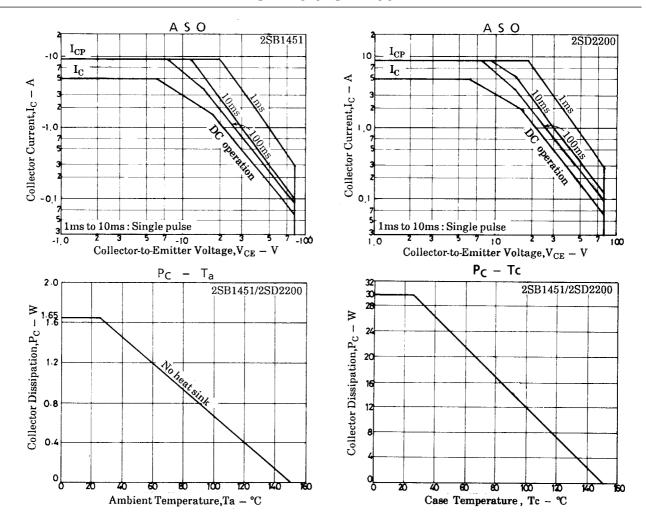


For PNP, the polarity is reversed.

Unit (resistance : Ω , capacitance : F)







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