

2SB1124/2SD1624

High Current Switching Applications

Applications

· Voltage regulators, relay drivers, lamp drivers, electrical equipment.

Features

- · Adoption of FBET, MBIT processes.
- · Low collector-to-emitter saturation voltage.
- · Fast switching speed.
- · Large current capacity and wide ASO.

(): 2SB1124

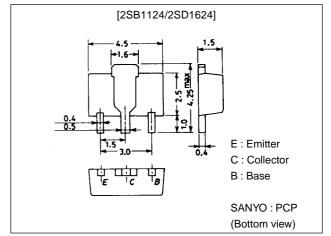
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2038



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{СВО}		(–)60	V
Collector-to-Emitter Voltage	VCEO		(–)50	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	I _C		(–)3	Α
Collector Current (Pulse)	ICP		(–)6	Α
Collector Dissipation	PC		500	mW
		Mounted on ceramic board (250mm²×0.8mm)	1.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =(-)40V, I _E =0			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)100mA	100*		560*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)3A	35			
Gain-Bandwidth Product	fT	V _{CE} =(-)10V, I _C =(-)50mA		150		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(39)		pF
				25		pF

^{*;} The 2SB1124/2SD1624 are classified by 100mA h_{FE} as follows: 100 R 200 140

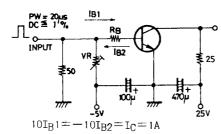
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Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)2A, I _B =(-)100mA		(-0.35)	(-0.7)	V
				0.19	0.5	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)2A, I _B =(-)100mA		(-0.94)	(–)1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(–)1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μA, I _C =0	(-)6			V
Turn-ON Time	ton	See specified Test Circuit.		70		ns
				(70)		ns
Storage Time	t _{stg}	See specified Test Circuit.		650		ns
				(450)		ns
Fall Time	t _f	See specified Test Circuit.		35		ns
				(35)		ns

Switching Time Test Circuit

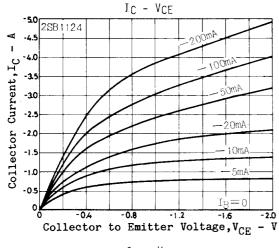


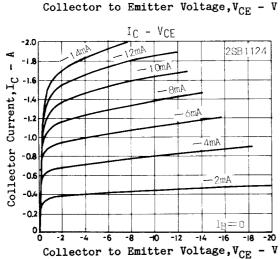
Marking 2SB1124:BG

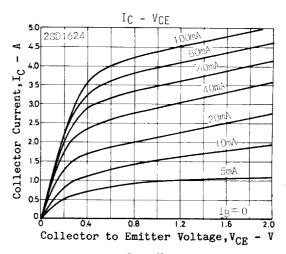
2SD1624:DG

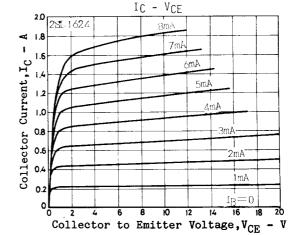
h_{FE} rank :R,S,T,U

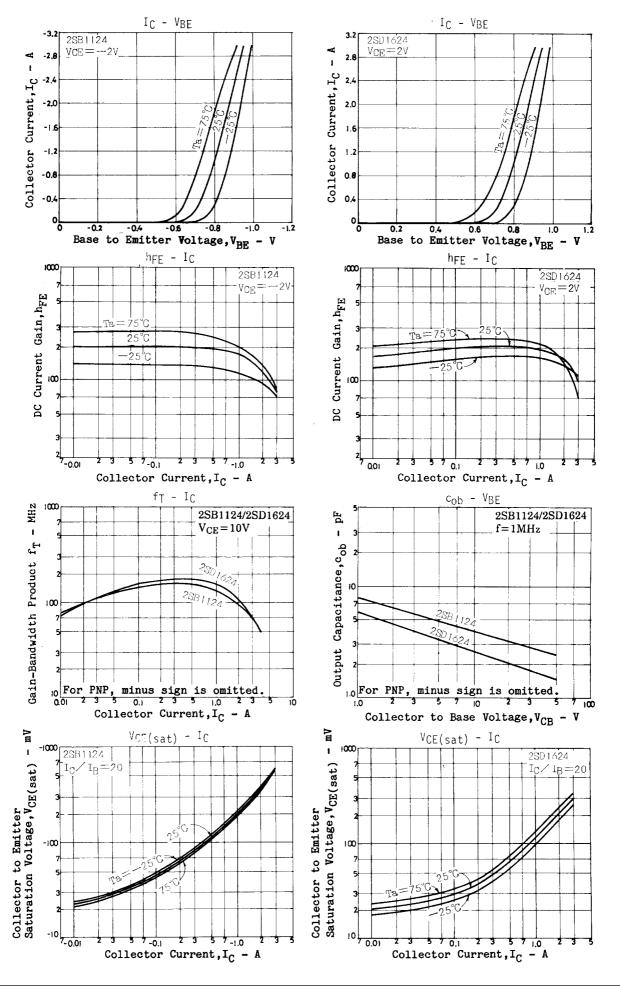
(For PNP, the polarity is reversed.) $Unit (resistance: \Omega, capacitance: F)$



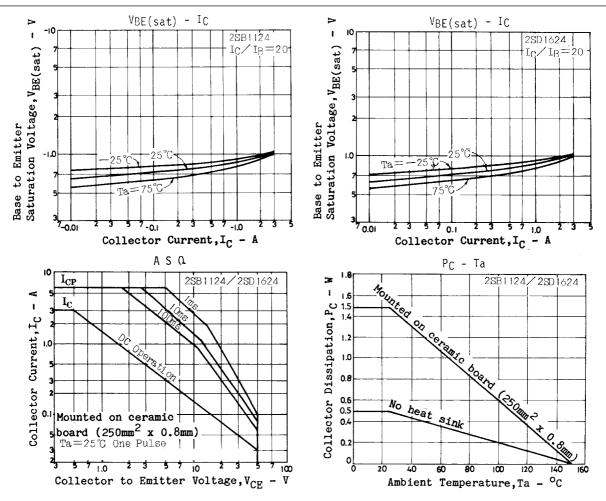








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