PNP/NPN Epitaxial Planar Silicon Transistors



2SA1706/2SC4486

High-Current Switching Applications

Applications

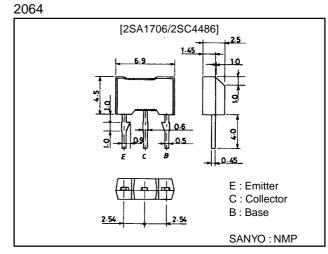
· Voltage regulators, relay drivers, lamp drivers.

Features

- · Adoption of FBET, MBIT processes.
- · Large current capacity and wide ASO.
- · Fast switching speed.

Package Dimensions

unit:mm



():2SA1706

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		(–)60	V
Collector-to-Emitter Voltage	VCEO		(–)50	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	ι _C		(–)2	A
Collector Current (Pulse)	I _{CP}		(–)4	A
Collector Dissipation	PC		1	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)50V, I _E =0			(–)100	nA
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0			(–)100	nA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)100mA	100*		400*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)1.5A	40			
Gain-Bandwidth Product	fT	V _{CE} =(-)10V, I _C =(-)50mA		150		MHz

 \ast : The 2SA1706/2SC4486 are classified by 100mA h_{FE} as follows :

100 R 200 140 S 280 200 T 400

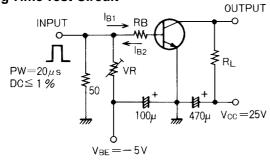
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)1A, I _B =(-)50mA		(-0.3)	(-0.7)	V
				0.15	0.4	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)1A, I _B =(-)50mA		(–)0.9	(–)1.2	V
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(22)12		pF
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10µA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	(–)50			V
Emitter-to-Base Breakdown Votage	V _{(BR)EBO}	I _E =(-)10μΑ, I _C =0	(–)6			V
Turn-ON TIme	ton	See specified Test Circuit		60		ns
Storage Time	tstg	See specified Test Circuit		(450)		ns
				550		ns
Fall Time	t _f	See specified Test Circuit		30		ns

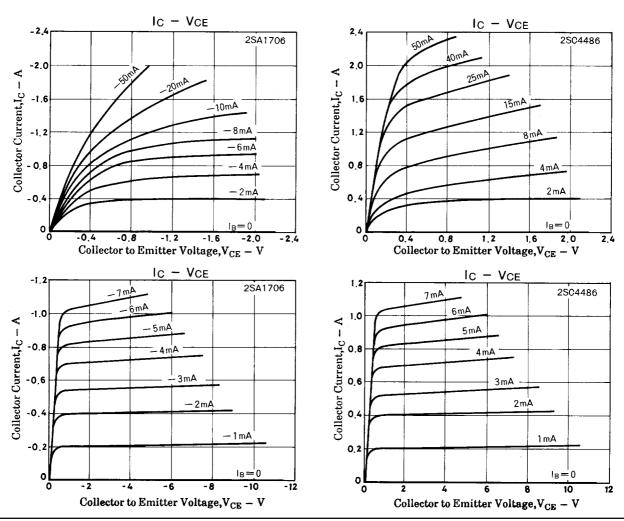
Switching Time Test Circuit



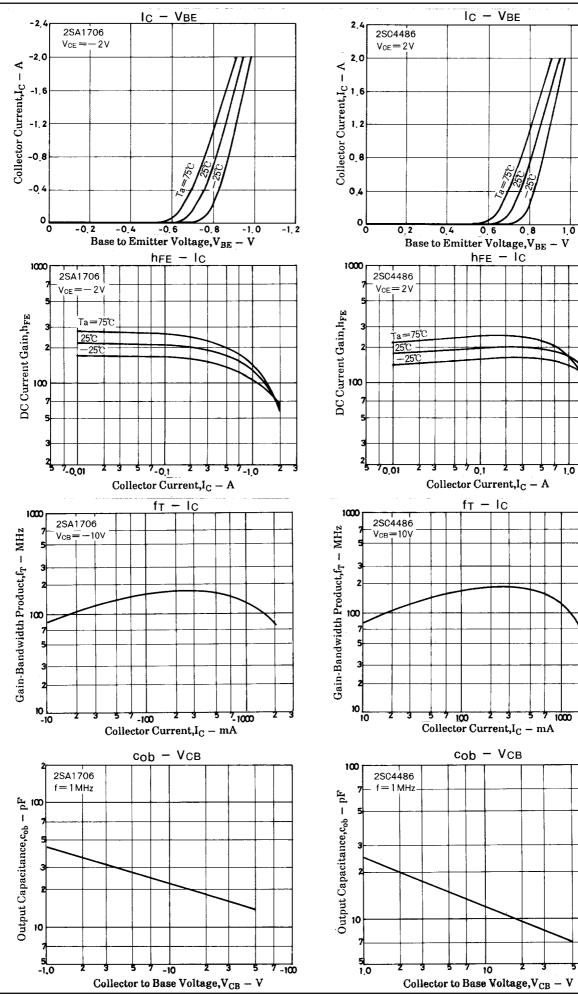
 $10I_{B1} = -10I_{B2} = I_C = 500 \text{mA}$

(For PNP, the polarity is reversed.)

Unit (resistance : Ω , capacitance : F)



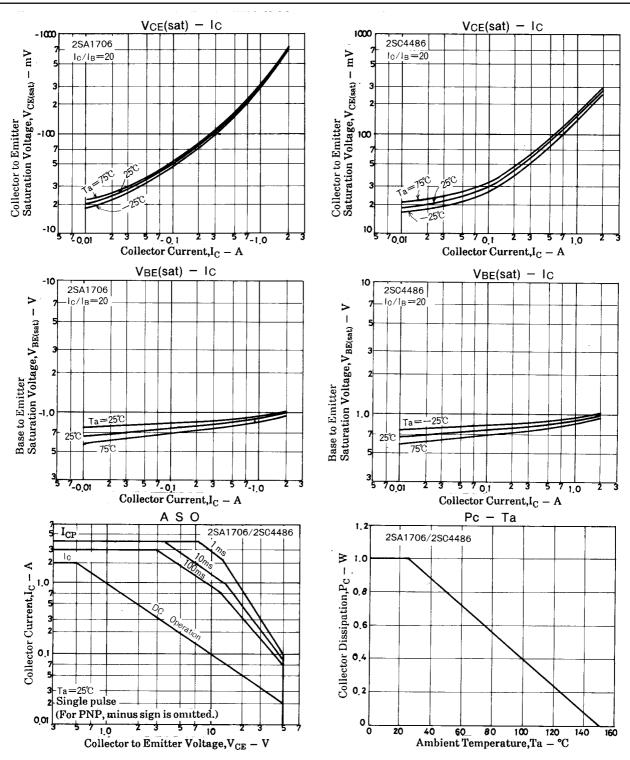
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