

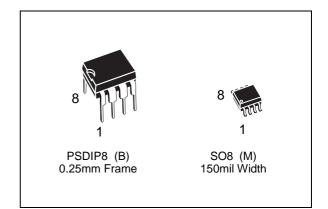
ST24E16 ST25E16

DATA BRIEFING

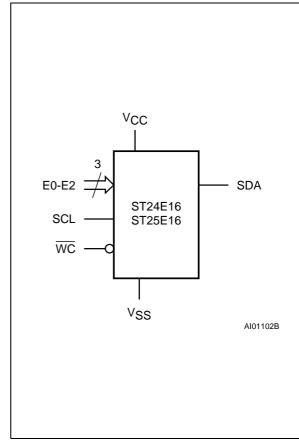
16K SERIAL I²C EEPROM with EXTENDED ADDRESSING

COMPATIBLE with I²C EXTENDED ADDRESSING

- TWO WIRE SERIAL INTERFACE, SUPPORTS 400kHz PROTOCOL
- 1 MILLION ERASE/WRITE CYCLES, OVER the FULL SUPPLY VOLTAGE RANGE
- 40 YEARS DATA RETENTION
- SINGLE SUPPLY VOLTAGE
 - 4.5V to 5.5V for ST24E16 version
- 2.5V to 5.5V for ST25E16 version
- WRITE CONTROL FEATURE
- BYTE and PAGE WRITE (up to 16 BYTES)
- BYTE, RANDOM and SEQUENTIAL READ MODES
- SELF TIMED PROGRAMING CYCLE
- AUTOMATIC ADDRESS INCREMENTING
- ENHANCED ESD/LATCH UP PERFORMANCES



Logic Diagram



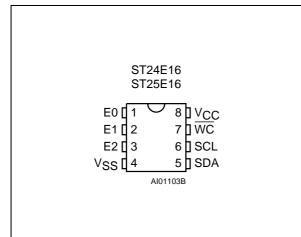
DESCRIPTION

The ST24/25E16 are 16K bit electrically erasable programmable memories (EEPROM), organized as 8 blocks of 256 x 8 bits. It is manufactured in SGS-THOMSON's Hi-Endurance Advanced CMOS technology which guarantees an endurance of one million erase/write cycles over the full supply voltage range, and a data retention of over 40 years. The ST25E16 operates with a power supply value as low as 2.5V.

B24E16/611

Complete data available on DATA-on-DISC CD-ROM or at www.st.com

DIP Pin Connections

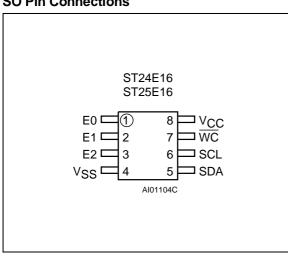


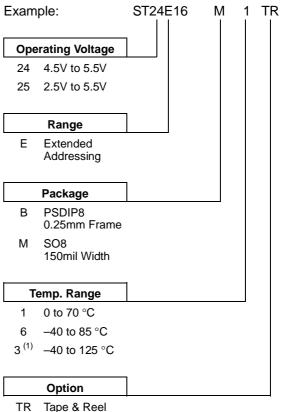
Signal Names

E0 - E2	Chip Enable Inputs
SDA	Serial Data Address Input/Output
SCL	Serial Clock
WC	Write Control
Vcc	Supply Voltage
V _{SS}	Ground

Ordering Information Scheme

For a list of available options or for further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.







Notes: 1. Temperature range on special request only.

Devices are shipped from the factory with the memory content set at all "1's" (FFh).

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SO Pin Connections