

**N-Channel JFETs****Product Summary**

Part Number	$V_{GS(off)}$ (V)	$V_{(BR)GSS}$ Min (V)	$g_{fs}$ Min (mS)	$I_{DSS}$ Min (mA)
J210	-1 to -3	-25	4	2
J211	-2.5 to -4.5	-25	6	7
J212	-4 to -6	-25	7	15

*J211, For applications information see AN104, page 21.*

**Features**

- Excellent High Frequency Gain: J211/212, Gps 12 dB (typ) @ 400 MHz
- Very Low Noise: 3 dB (typ) @ 400 MHz
- Very Low Distortion
- High ac/dc Switch Off-Isolation
- High Gain:  $A_V = 35$  @ 100  $\mu$ A

**Benefits**

- Wideband High Gain
- Very High System Sensitivity
- High Quality of Amplification
- High-Speed Switching Capability
- High-Quality Low-Level Signal Amplification

**Applications**

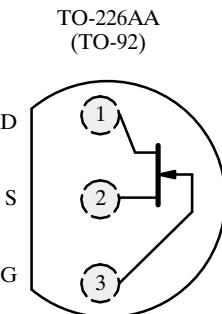
- High-Frequency Amplifier/Mixer
- Oscillator
- Sample-and-Hold
- Very Low Capacitance Switches

**Description**

The J210/211/212 n-channel JFETs are general-purpose and high-frequency amplifiers for a wide range of applications. These devices feature low leakage ( $I_{GSS} < 100$  pA).

The TO-226AA (TO-92) plastic package, provides low cost and is available in tape-and-reel for automated assembly (see Packaging Information).

For similar dual products, see the 2N5911/5912 and U440/441 data sheets.



Top View

**Absolute Maximum Ratings**

Gate-Drain, Gate-Source Voltage .....	-25 V
Gate Current .....	10 mA
Lead Temperature (1/16" from case for 10 sec.) .....	300°C
Storage Temperature .....	-55 to 150°C

Operating Junction Temperature .....	-55 to 150°C
Power Dissipation <sup>a</sup> .....	350 mW

Notes

a. Derate 2.8 mW/°C above 25°C

### Specifications<sup>a</sup>

Parameter	Symbol	Test Conditions	Typ <sup>b</sup>	Limits						Unit
				J210		J211		J212		
Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Unit
<b>Static</b>										
Gate-Source Breakdown Voltage	V <sub>(BR)GSS</sub>	I <sub>G</sub> = -1 μA, V <sub>DS</sub> = 0 V	-35	-25		-25		-25		V
Gate-Source Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1 nA		-1	-3	-2.5	-4.5	-4	-6	
Saturation Drain Current <sup>c</sup>	I <sub>DSS</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V		2	15	7	20	15	40	mA
Gate Reverse Current	I <sub>GSS</sub>	V <sub>GS</sub> = -15 V, V <sub>DS</sub> = 0 V T <sub>A</sub> = 125°C	-1 -0.5	-100		-100		-100		pA
Gate Operating Current <sup>b</sup>	I <sub>G</sub>	V <sub>DG</sub> = 10 V, I <sub>D</sub> = 1 mA	-1							pA
Drain Cutoff Current	I <sub>D(off)</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = -8 V	1							
Gate-Source Forward Voltage	V <sub>GS(F)</sub>	I <sub>G</sub> = 1 mA, V <sub>DS</sub> = 0 V	0.7							V
<b>Dynamic</b>										
Common-Source Forward Transconductance <sup>c</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V f = 1 kHz		4	12	6	12	7	12	mS
Common-Source Output Conductance	g <sub>os</sub>				150		200		200	μS
Common-Source Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V f = 1 MHz	4							pF
Common-Source Reverse Transfer Capacitance	C <sub>rss</sub>		1.5							
Equivalent Input Noise Voltage	ē <sub>n</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V f = 1 kHz	5							nV/√Hz

Notes

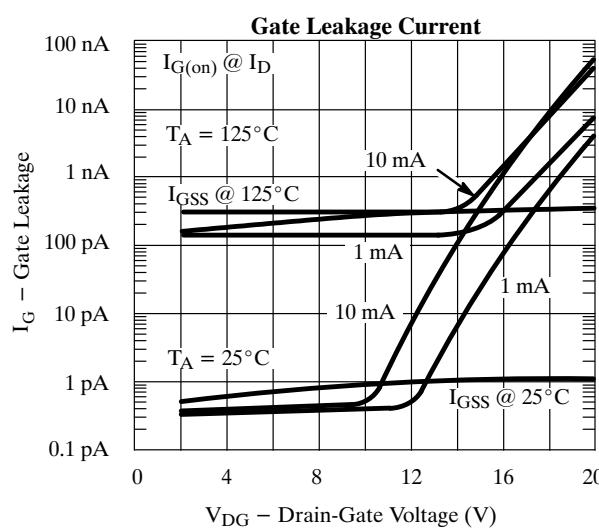
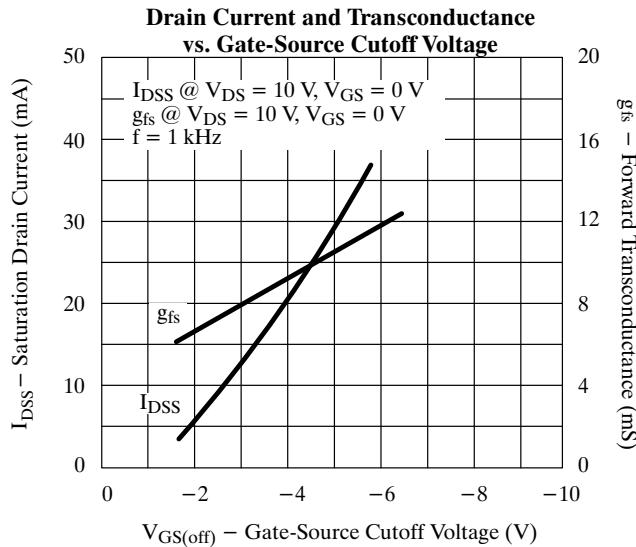
a. T<sub>A</sub> = 25°C unless otherwise noted.

NZF

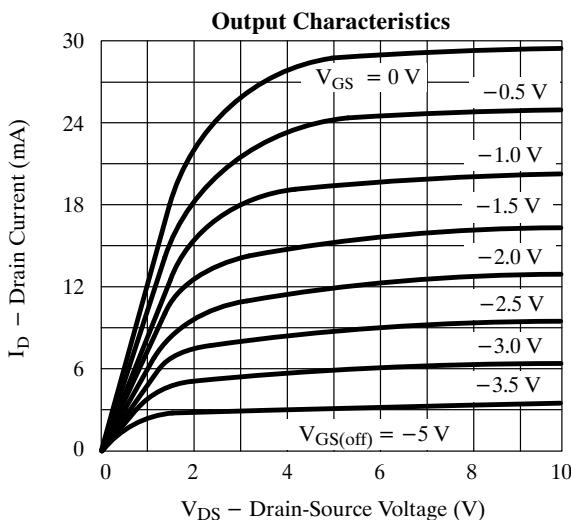
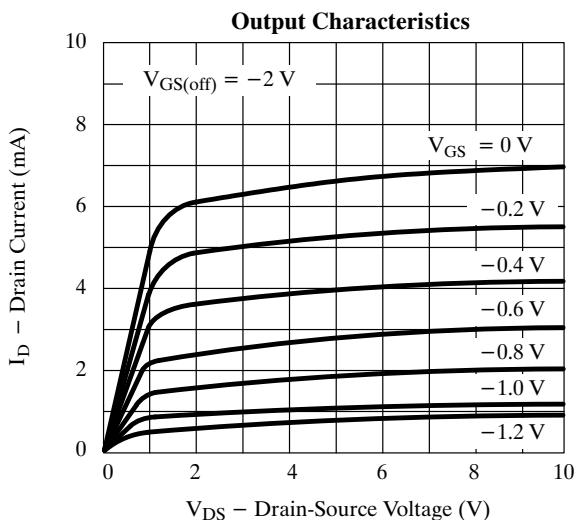
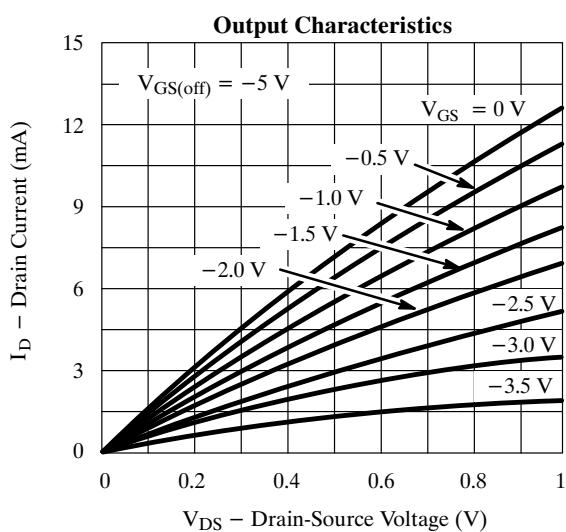
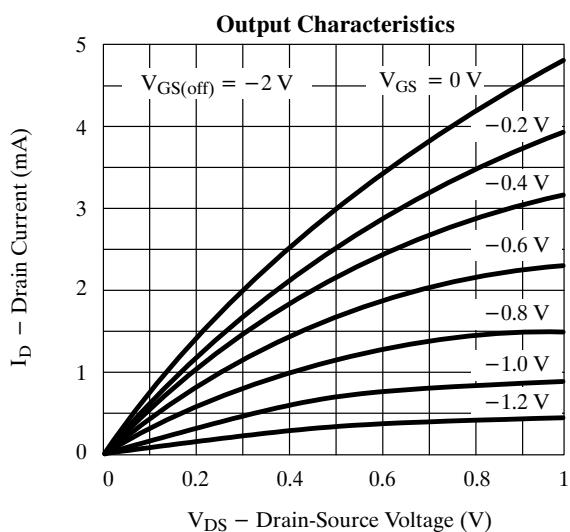
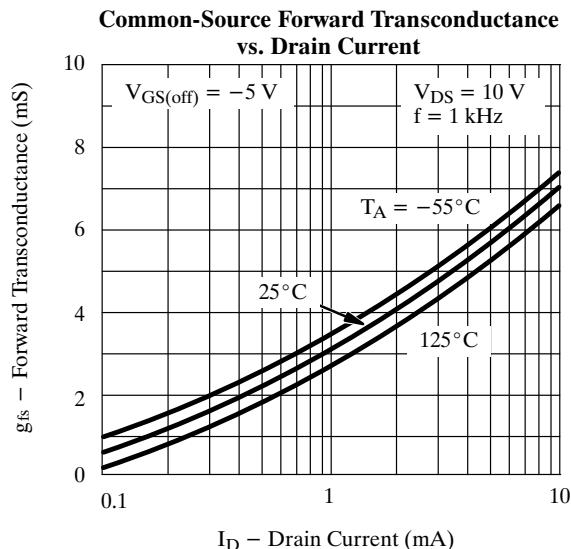
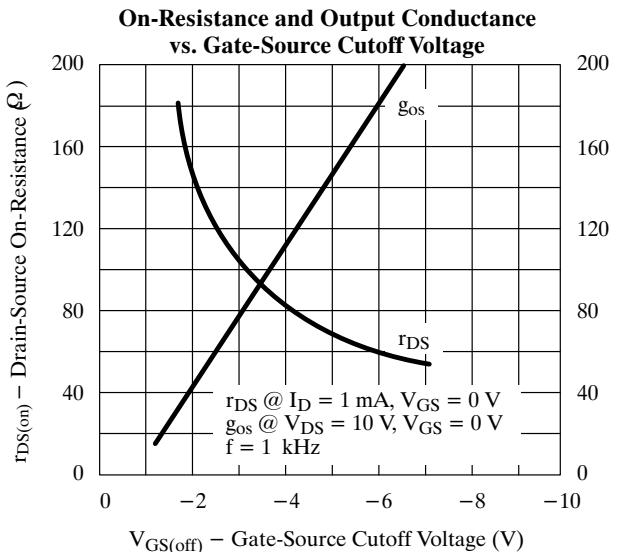
b. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

c. Pulse test: PW ≤ 300 μs duty cycle ≤ 3%.

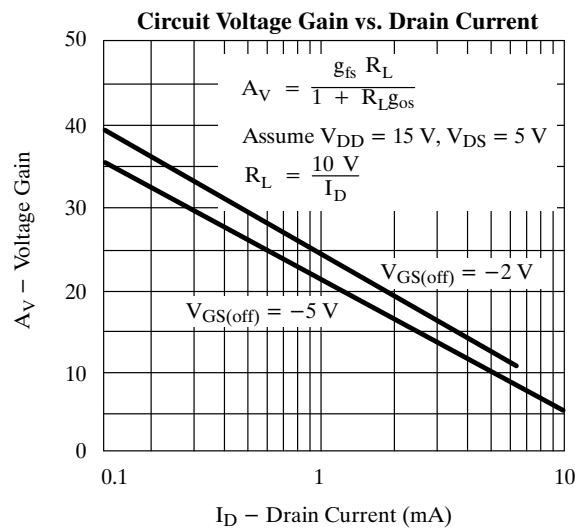
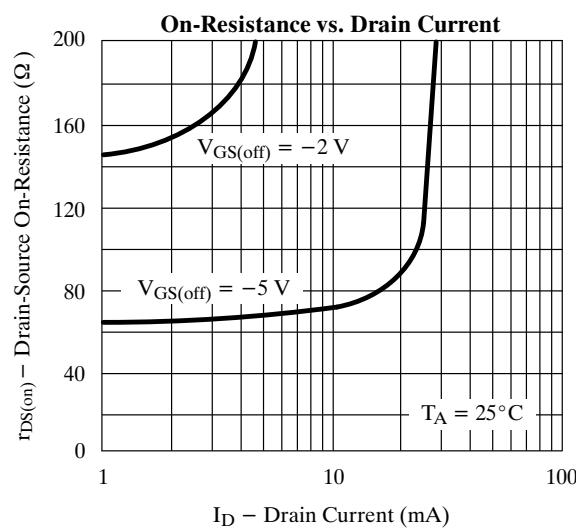
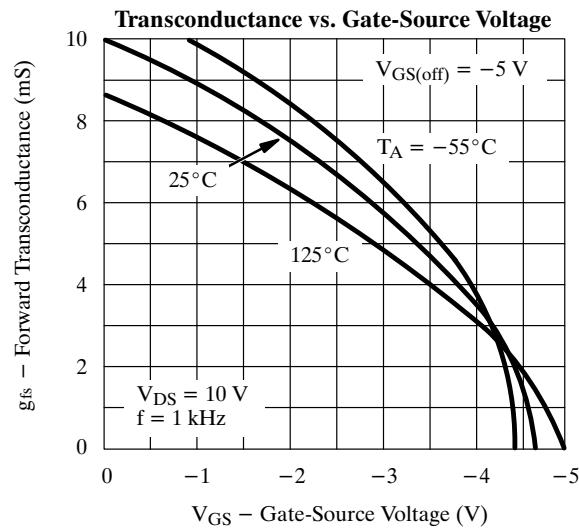
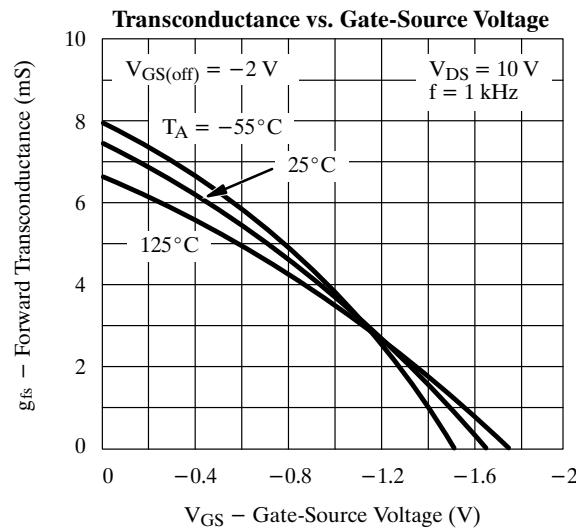
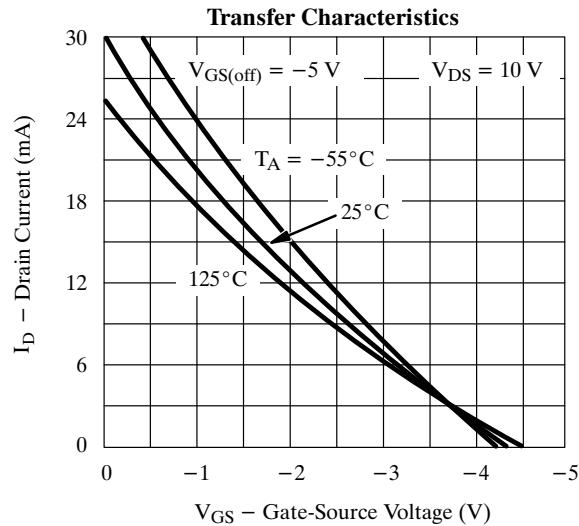
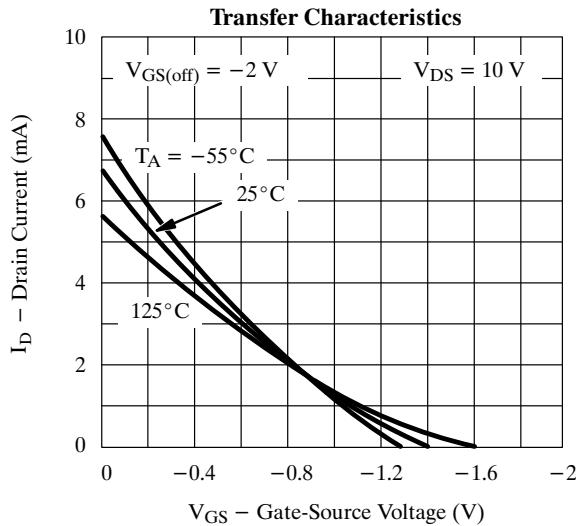
### Typical Characteristics



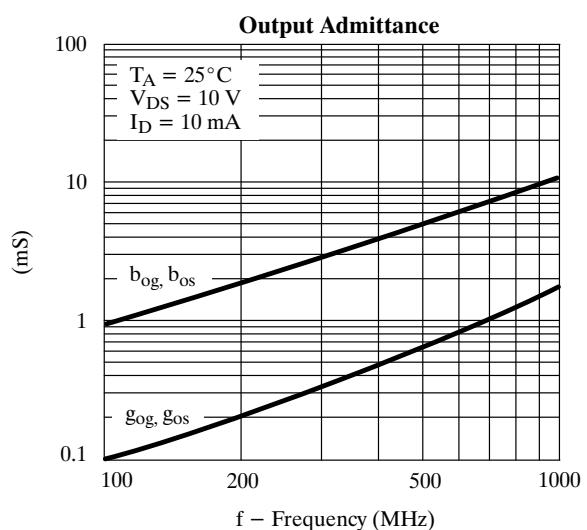
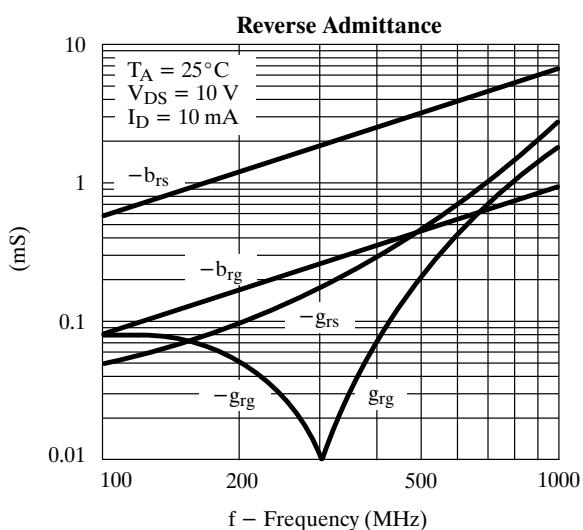
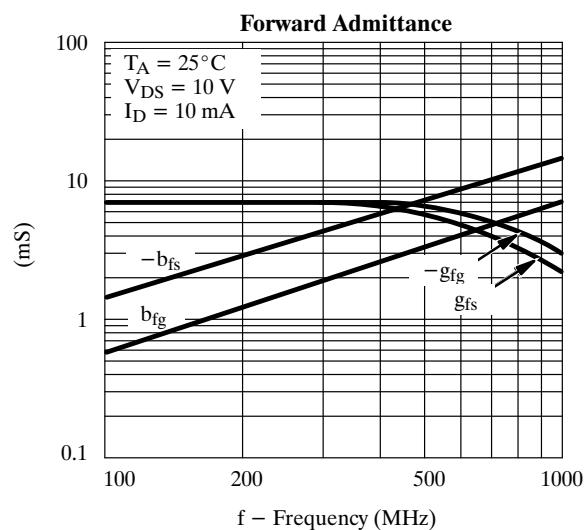
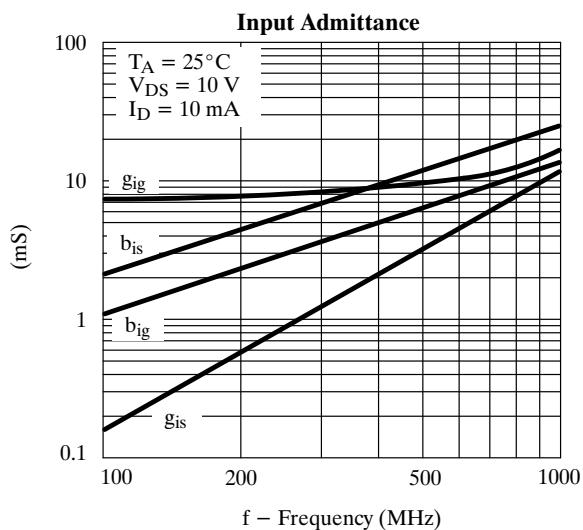
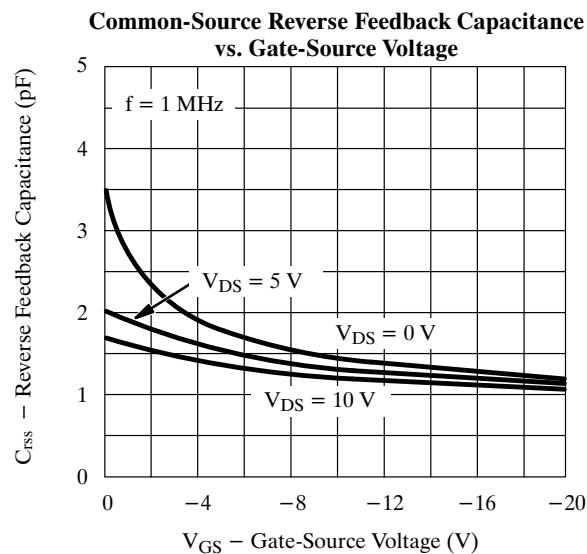
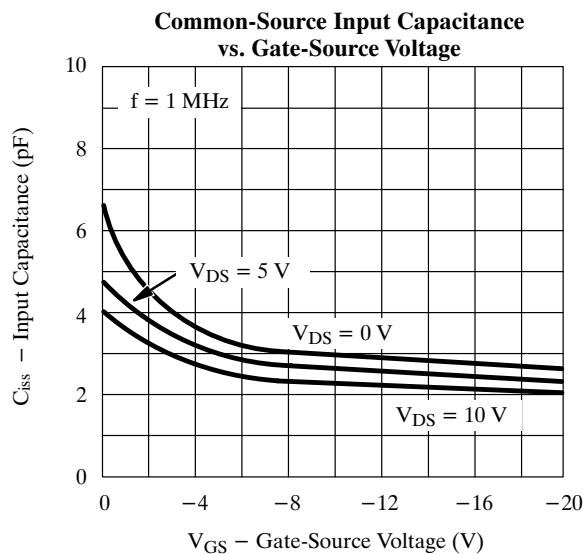
### Typical Characteristics (Cont'd)



### Typical Characteristics (Cont'd)



## Typical Characteristics (Cont'd)



# J210/211/212

TEMIC

Siliconix

## Typical Characteristics (Cont'd)

