surface mount environmental specifications

All MCL products are manufactured under exacting guality assurance and control standards, and are capable of meeting published specifications a fter being subjected to any or all of the following physical and environmental tests of MIL-STD-202, unless otherwise noted.

MIL-STD-202		Package Type		
Method		Hermetic	Non-Hermetic ⁽⁴⁾	
_	—	-55° to 100°	-40° to 85°	
—	—	-55° to 100°	-55° to 100°	
105	E	Yes	—	
106	—	Yes ⁽²⁾	—	
107	В	-65° to 125°	-55° to 100°	
112	C&D	1x10 ⁻⁷	Not Appl.	
208	—	Yes	Yes	
—	—	Yes	Yes	
215	—	Yes	Yes	
	Method — 105 106 107 112 208 —	Method	Method Hermetic 55° to 100° 55° to 100° 105 E Yes 106 Yes ⁽²⁾ 107 B -65° to 125° 112 C&D 1x10 ⁻⁷ 208 Yes Yes Yes	

Extended temperature ranges are available for some models; consult factory. Operating Temperature for RAM models, -55°C to 125°C and VNA, -40°C to 70°C, or as noted on spec. page.
Humidity not guaranteed for VSW, VSWA models.
Humidity not guaranteed for VSW, VSWA models.
Yeak temperature during reflow soldering 230°C, see typical profiles page 22. Technical Section.

4. Including aqueous washable models

understanding surface mount

The main reason for the utilization of surface mount technology today is PWB area efficiency and the corresponding reduction of the system volume. Small surface mount components are used to manufacture electronic assemblies that are significantly smaller than the similar function assemblies using thru-hole components. Usually, the board area for most systems can be reduced by a factor of two for a single sided board. When components are mounted on both sides of the board, size reduction by another factor of two can be achieved. Conversely, the functional density can be doubled in the same area with the use of surface mount components. Since the height of surface mount components are generally smaller, significant reduction in system volume can also be achieved. Fewer, denser assemblies and correspondingly fewer connectors, less mechanical hardware and shorter lead lengths can yield another reduction factor of 2 to 4. Add to that the use of Application Specific IC's (ASIC's), and a total volume amounting to approximately 20 percent of the original system can result.

Size reduction however, is not the only SMT benefit. As a secondary benefit, the ever increasing demand for higher a similar electronic function.

speed circuitry causes time delay reductions of even nanoseconds to be beneficial. Typically, an SMT package will have significantly lower values of inductance, capacitance and resistance compared to dual in-line packages (DIP). This is shown in the table below. Surface mount assembly essential is a four step manufacturing process:

- 1 Print the solder paste
- 2 Place components
- 3 Reflow the paste to form the solder interconnection
- between the component an the land area on the PWB 4 Clean-up

The use of a pick-and-place machined can perform the equivalent function handled by 3 to 4 machines in a standard thru-hole assembly area. Additionally, an automated SMT manufacturing line conserves space, used fewer people, has a shorter cycle time and reduces work in progress inventory. This helps achieve higher yields through more rapid feedback and use of statistical process control data. In all, the savings from automated SMT manufacturing can be as great as 50% compared to a thru-hole manufacturing area designed to build

SURFACE MOUNT VS. DUAL IN-LINE PACKAGES INDUCTANCE AND CAPACITANCE VALUE COMPARISONS

	DUAL IN-LINE PKG. SMALL OUTLI (DIP) (SO)			PLASTIC LEADED CHIP CARRIER (PLCC)				
LEADS	L (nH)	C (pF)	L (nH)	C (pF)	L (nH)	C (pF)		
8	3.2-9.3	0.35-1.0	2.3-3.8	0.22-0.48				
14	3.2-10.2	0.38-1.1	2.6-3.8	0.22-0.54				
16	3.3-10.5	0.44-1.1	2.4-4.3	0.22-0.50				
20	3.4-13.7	0.53-1.5	4.9-8.5	0.45-0.85	4.2-5.0	0.60-0.65		
24	3.8-18.1	0.53-1.6	4.9-9.5	0.43-1.86				
28	3.8-14.4	0.58-1.7			5.3-6.9	0.70-0.75		
40/44	3.8-15.3	0.60-1.9			5.3-6.6	0.75-0.80		
64/68	7.1-28.1	0.63-2.4			5.1-6.4	0.78-0.83		
Values given are Min, and Max								



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