

2.5 GHz Low Power Prescaler With Stand-By Mode

The MC12095 is a single modulus prescaler for low power frequency division of a 2.5 GHz high frequency input signal. Motorola's advanced MOSAIC[™] V technology is utilized to acheive low power dissipation of 24 mW at a minimum supply voltage of 2.7 V.

On-chip output termination provides output current to drive a 2.0 pF (typical) high impedance load. If additional drive is required for the prescaler output, an external resistor can be added in parallel from the OUT pin to GND to increase the output power. Care must be taken not to exceed the maximum allowable current through the output.

Divide ratio control input (SW) selects the required divide ratio of $\div 2$ or $\div 4$. Stand–By mode is available to reduce current drain to 100µA typical when the standby pin SB is switched LOW disabling the prescaler.

- 2.5 GHz Toggle Frequency
- Supply Voltage 2.7 V to 5.5 Vdc
- Low Power 8.7 mA Typical
- Operating Temperature –40 to 85°C
- Divide by 2 or 4 Selected by the SW Pin

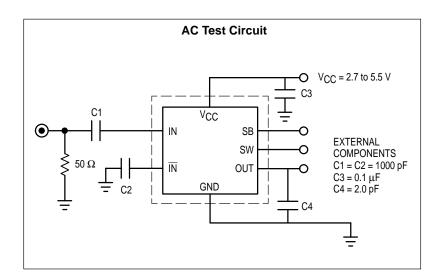
NOTE: For applications up to 1.1 GHz, please consult the MC12093 datasheet.

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FUNCTIONAL TABLE

SW	Divide Ratio	
н		2
L		4

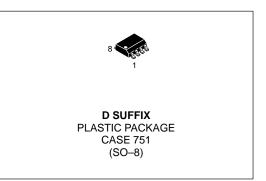
NOTES: 1. SW: H = (V_{CC} - 0.4 V) to V_{CC}; L = OPEN 2. SB: H = 2.0 V to V_{CC}; L = GND to 0.8 V

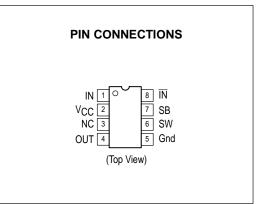


MC12095

MECL PLL COMPONENTS ÷2, ÷4 LOW POWER PRESCALER WITH STAND-BY MODE

> SEMICONDUCTOR TECHNICAL DATA





ORDERING INFORMATION

Device	Operating Temp Range	Package	
MC12095D	$T_A = -40$ to $85^{\circ}C$	SO–8	

MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Power Supply Voltage, Pin 2	VCC	-0.5 to 6.0	Vdc
Operating Temperature Range	Т _А	-40 to 85	°C
Storage Temperature Range	Tstg	–65 to 150	°C
Maximum Output Current, Pin 4	۱ ₀	8.0	mA

NOTE: ESD data available upon request.

ELECTRICAL CHARACTERISTICS (V_{CC} = 2.7 to 5.5 V; T_A = -40 to 85°C, unless otherwise noted.)

Parameter		Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave)		ft	500	3.0	2.5	GHz
Supply Current		ICC	-	8.7	14	mA
Stand–By Current		ISB	-	100	200	μA
Stand–By Input HIGH (SB)		VIH1	2.0	-	V _{CC} + 0.5 V	V
Stand–By Input LOW (SB)		VIL1	GND	-	0.8	V
Divide Ratio Control Input HIGH (SW)		V _{IH2}	V _{CC} – 0.4	Vcc	V _{CC} + 0.5 V	V
Divide Ratio Control Input LOW (SW)		V _{IL2}	OPEN	OPEN	OPEN	
Output Voltage Swing (2pF Load)	500–1000 MHz Input 1000–1500 MHz Input 1500–2500 MHz Input	VOUT	800 400 200	- 450 250	- - -	mVpp
Input Voltage Sensitivity		VIN	200	-	1000	mVpp

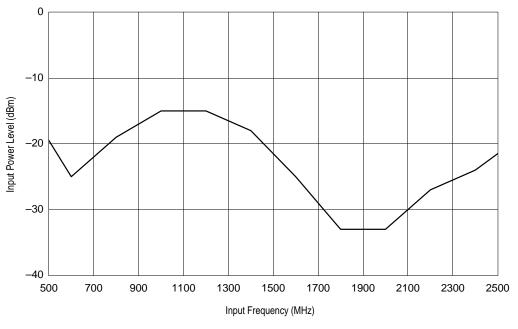


Figure 1. Typical Minimum Input Sensitivity versus Input Frequency

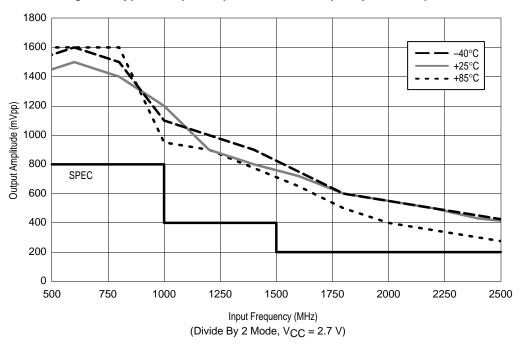


Figure 2. Typical Output Amplitude versus Frequency over Temperature

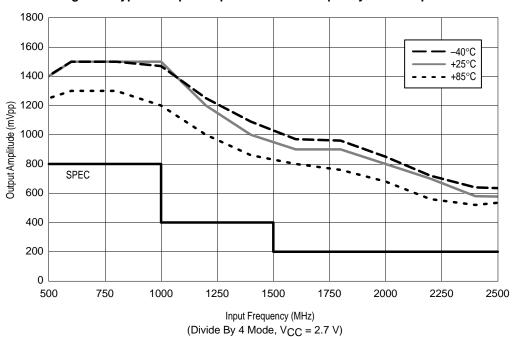
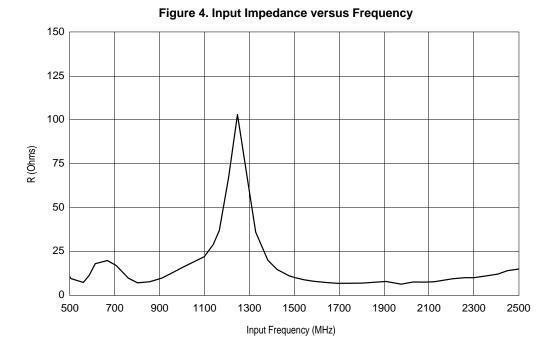


Figure 3. Typical Output Amplitude versus Frequency over Temperature



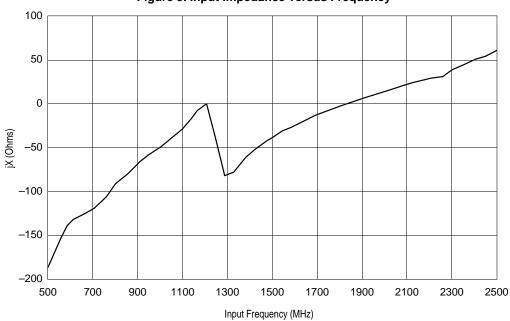
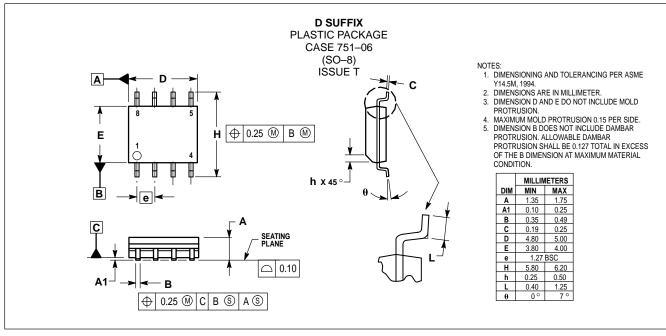


Figure 5. Input Impedance versus Frequency

OUTLINE DIMENSIONS



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